SPECIFICATIONS FOR
LCD MODULE

CUSTOMER	
CUSTOMER PART NO.	
AMPIRE PART NO.	**AM-1024768YBTZQW-TH1H**
APPROVED BY	
DATE	

☐ **Preliminary Specification**
☑ **Formal Specification**

APPROVED BY	CHECKED BY	ORGANIZED BY
Patrick	Mark	Tank

*This specification is subject to change without notice.

Intelligent Display Solutions, Unit 2, Berkshire Business Centre, Berkshire Drive, Thatcham, Berkshire, RG19 4EW
Telephone : +44 (0)1635 294600 Fax : +44 (0)1635 869200 Email: info@i-lcd.com
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RECORD OF REVISION

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Intelligent Display Solutions, Unit 2, Berkshire Business Centre, Berkshire Drive, Thatcham, Berkshire, RG19 4EW
Telephone : +44 (0)1635 294600 Fax : +44 (0)1635 869200 Email: info@i-lcd.com
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1. General specification

This is a color active matrix thin film transistor (TFT) liquid crystal display (LCD) that uses amorphous silicon TFT as a switching device. This model is composed of a TFT LCD panel, a driving circuit and a back light system. This TFT LCD has a 10.4 inch diagonally measured active display area with HD (1024 horizontal by 768 vertical pixels) resolution.

(1) Construction: 10.4” a-Si TFT active matrix, White LED Backlight.
(2) Resolution (pixel): 1024(R.G.B) X 768
(3) Number of the Colors : 16.2M (R , G , B 8 bit digital each)
(4) LCD type : Normally black
(5) Interface : 24 Bit LVDS interface
(6) HDMI Board
(7) Projective Capacitive Touch
   a. Interface : USB
   b. Touch Controller: ILI2511

1.1 Display Characteristics

Item		Specification	Unit
Outline Dimension		245.0 (H) x 195.0 (V) x 12.86 (D) (Typ.)	mm
Display area		211.2(H) x 158.4(V) (10.4” diagonal)	mm
Number of Pixel		1024(H) x 768(V)	pixels
Pixel pitch		0.20625(H) x 0.20625(V)	mm
Pixel arrangement		RGB Vertical Stripe	
Display mode		Normally Black	
NTSC		70(Typ.)	%
Back-light		Single LED (Side-Light type)	
Power Consumption	Logic System (White Pattern)	TBD(max) @V_{DVDD}=3.3V	W
	B/L System	TBD	W

2. Optical Characteristics

Item		Symbol	Condition	Min.	Typ.	Max.	Unit	Note
Contrast		CR	Θ=0 Normal viewing angle	600	900	–		(1)(2)
Response time	Rising	TR+TF		–	30	40	msec	(1)(3)
	Falling							
White luminance (Center)		Y_L		425	510	–	cd/m^2	(1)(4) (I_L=480mA)
Color chromaticity (CIE1931)	White	W_x		0.273	0.313	0.353		(1)(4)
		W_y		0.289	0.329	0.369		
	Red	R_x			TBD			
		R_y			TBD			
	Green	G_x			TBD			
		G_y			TBD			
	Blue	B_x			TBD			
		B_y			TBD			
Viewing angle	Hor.	$Θ_L$	CR>10	80	85	–		
		$Θ_R$		80	85	–		
	Ver.	$Θ_U$		80	85	–		
		$Θ_D$		80	85	–		
Brightness uniformity		B_{UNI}	Θ=0	70	80	–	%	(5)
Optima View Direction		Free						(6)

Measuring Condition

- Measuring surrounding: dark room
- LED current I_L= 480mA
- Ambient temperature 25±2°C
- 15min, warm-up time.

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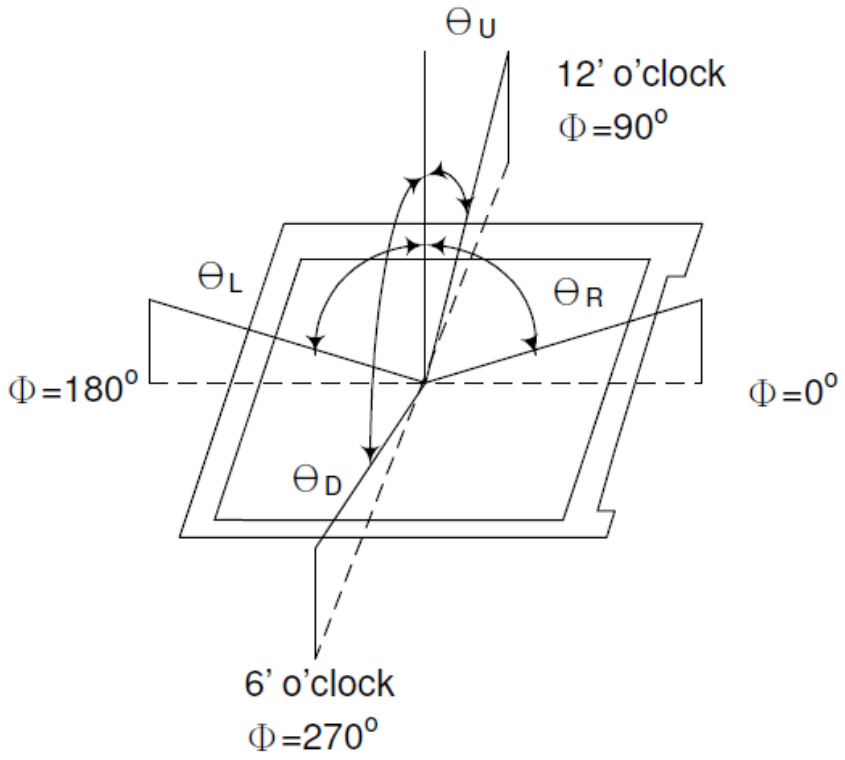
Measuring Equipment

- FPM520 of Westar Display technologies, INC., which utilized SR-3 for Chromaticity and BM-5A for other optical characteristics.
- Measuring spot size : 20 ~ 21 mm

Note (1) Definition of Viewing Angle:



Note (2) Definition of Contrast Ratio (CR) :
measured at the center point of panel

$$CR = \frac{\text{Luminance with all pixels white}}{\text{Luminance with all pixels black}}$$

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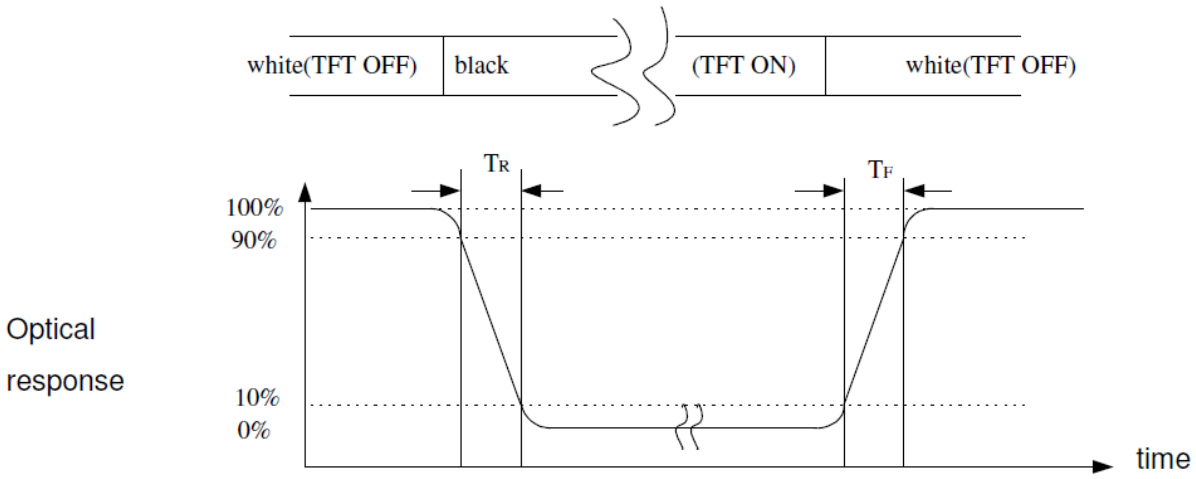
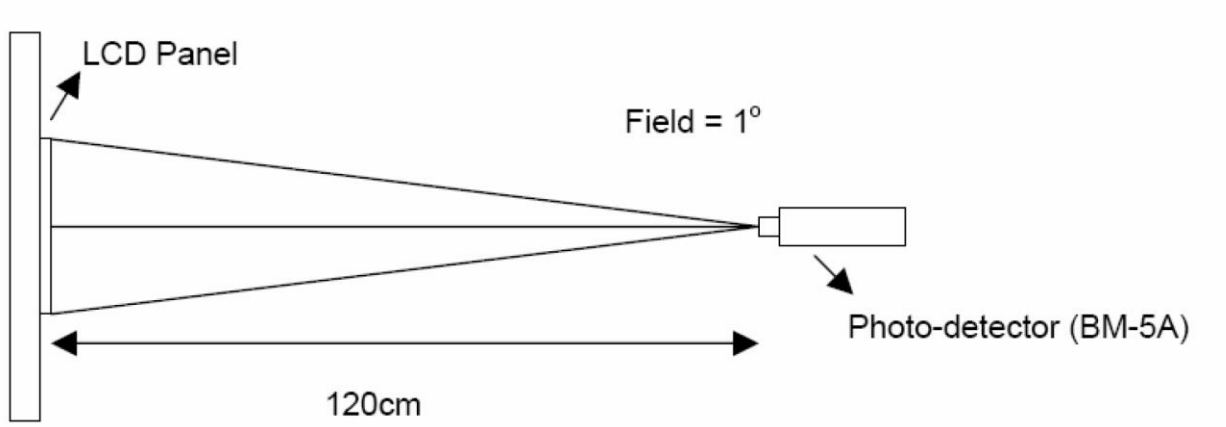
Note (3) Definition of Response Time : Sum of T_R and T_F



Note (4) Definition of optical measurement setup



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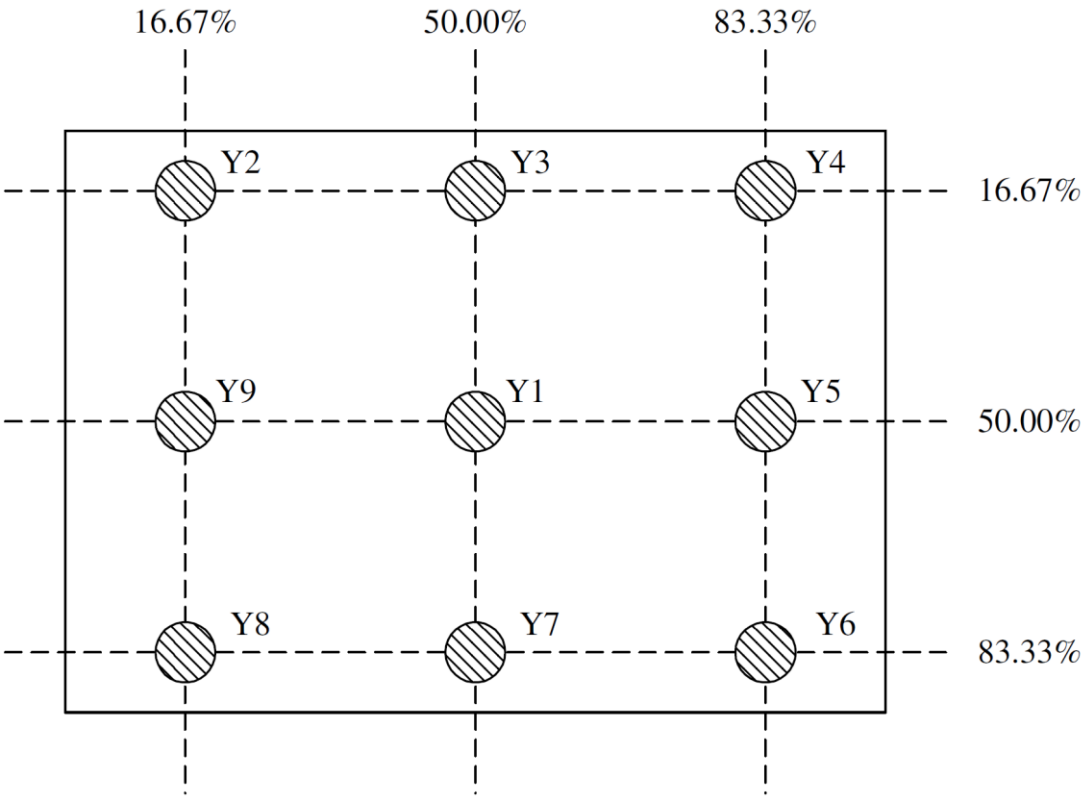
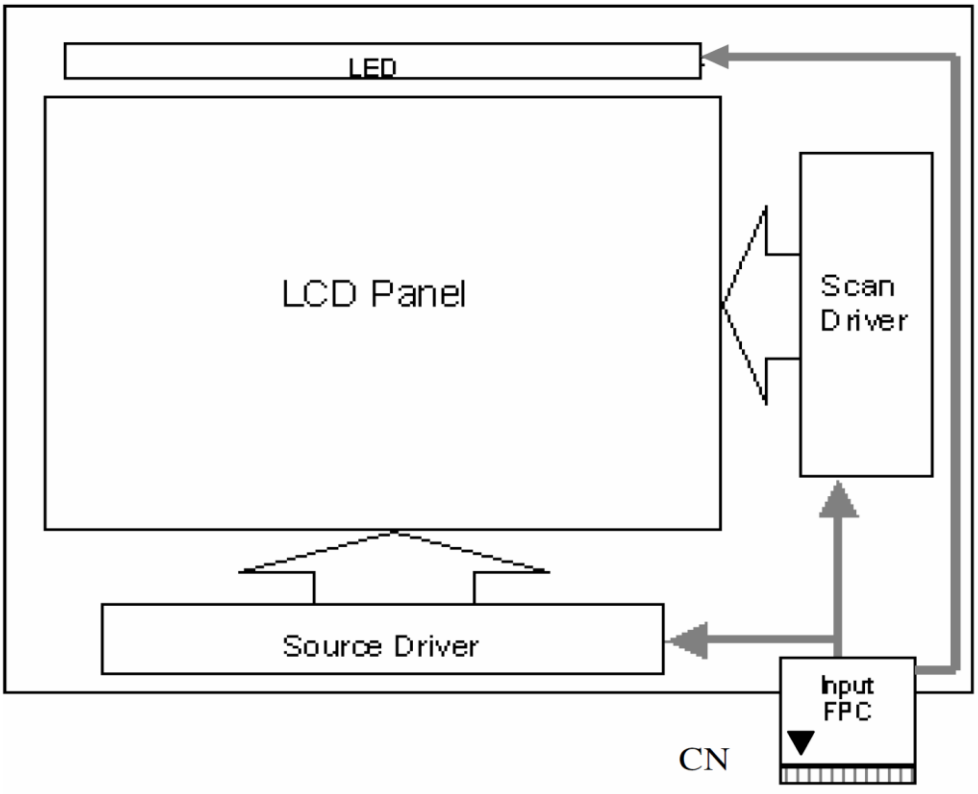
Note (5) Definition of brightness uniformity



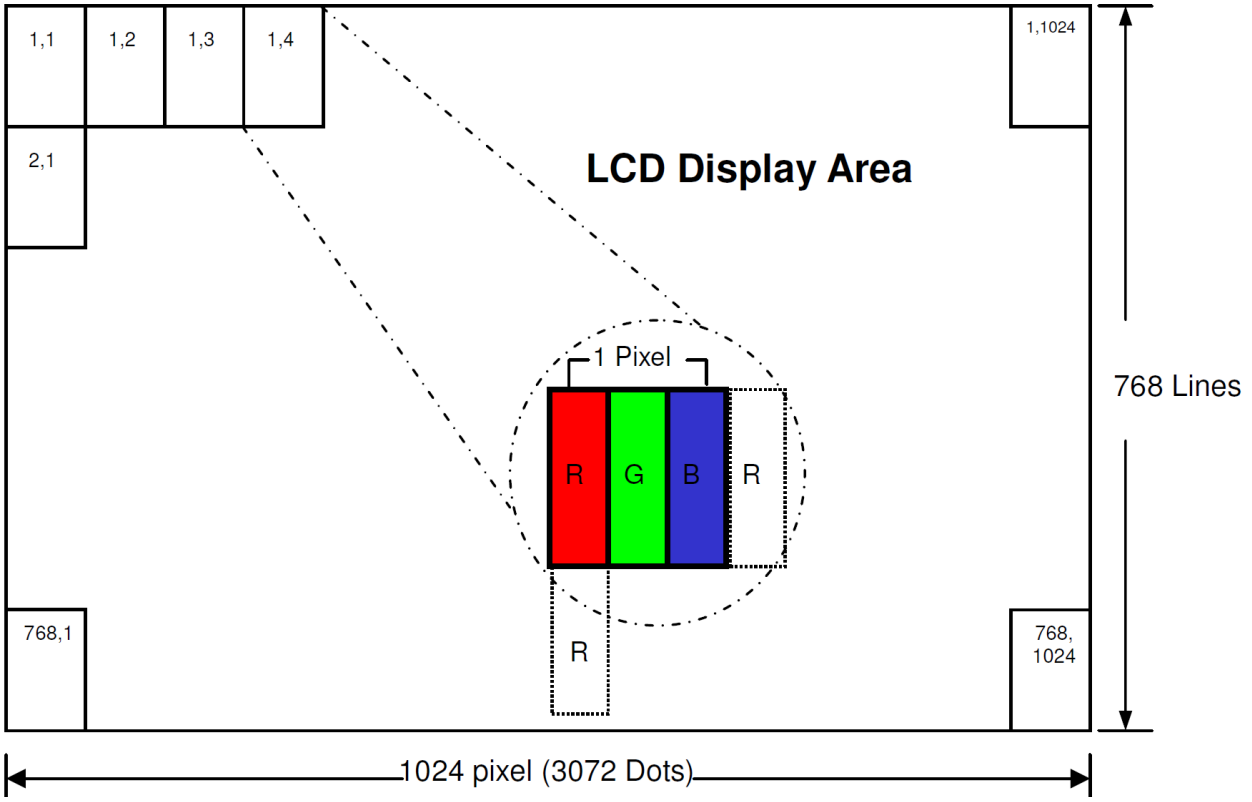
$$\text{Luminance uniformity} = \frac{\text{(Min Luminance of 9 points)}}{\text{(Max Luminance of 9 points)}} \times 100\%$$

Note (6)：Rubbing Direction (The different Rubbing Direction will cause the different optima view direction.

3. Functional Block Diagram



Pixel Format



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3.1 Relationship between Displayed Color and Input

	Display	MSB R7	R6	R5	R4	R3	R2	R1	LSB R0	MSB G7	G6	G5	G4	G3	G2	G1	LSB G0	MSB B7	B6	B5	B4	B3	B2	B1	LSB B0	Gray scale Level
Basic color	Black	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	-
	Blue	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	H	H	H	H	H	H	H	H	-
	Green	L	L	L	L	L	L	L	L	H	H	H	H	H	H	H	H	L	L	L	L	L	L	L	L	-
	Light Blue	L	L	L	L	L	L	L	L	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	-
	Red	H	H	H	H	H	H	H	H	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	-
	Purple	H	H	H	H	H	H	H	H	L	L	L	L	L	L	L	L	H	H	H	H	H	H	H	H	-
	Yellow	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	L	L	L	L	L	L	L	L	-
	White	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	-
Gray scale of Red	Black	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L0
	Dark	L	L	L	L	L	L	L	H	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L1
	↑	L	L	L	L	L	L	H	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L2
	↑						:							:									:			L3…L251
	↓	H	H	H	H	H	H	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L252
	Light	H	H	H	H	H	H	L	H	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L253
		H	H	H	H	H	H	H	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L254
	Red	H	H	H	H	H	H	H	H	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	Red L255
Gray scale of Green	Black	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L0
	Dark	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	H	L	L	L	L	L	L	L	L	L1
	↑	L	L	L	L	L	L	L	L	L	L	L	L	L	L	H	L	L	L	L	L	L	L	L	L	L2
	↑						:							:									:			L3…L251
	↓	L	L	L	L	L	L	L	L	H	H	H	H	H	H	L	L	L	L	L	L	L	L	L	L	L252
	Light	L	L	L	L	L	L	L	L	H	H	H	H	H	H	L	H	L	L	L	L	L	L	L	L	L253
		L	L	L	L	L	L	L	L	H	H	H	H	H	H	H	L	L	L	L	L	L	L	L	L	L254
	Green	L	L	L	L	L	L	L	L	H	H	H	H	H	H	H	H	L	L	L	L	L	L	L	L	Green L255
Gray scale of Blue	Black	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L0
	Dark	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	H	L1
	↑	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	H	L	L2
	↑						:							:									:			L3…L251
	↓	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	H	H	H	H	H	H	L	L	L252
	Light	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	H	H	H	H	H	H	L	H	L253
		L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	H	H	H	H	H	H	H	L	L254
	Blue	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	H	H	H	H	H	H	H	H	Blue L255
Gray scale of White & Black	Black	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L0
	Dark	L	L	L	L	L	L	L	H	L	L	L	L	L	L	L	H	L	L	L	L	L	L	L	H	L1
	↑	L	L	L	L	L	L	H	L	L	L	L	L	L	L	H	L	L	L	L	L	L	L	H	L	L2
	↑						:							:									:			L3…L251
	↓	H	H	H	H	H	H	L	L	H	H	H	H	H	H	L	L	H	H	H	H	H	H	L	L	L252
	Light	H	H	H	H	H	H	L	H	H	H	H	H	H	H	L	H	H	H	H	H	H	H	L	H	L253
		H	H	H	H	H	H	H	L	H	H	H	H	H	H	H	L	H	H	H	H	H	H	H	L	L254
	White	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	White L255

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4. ABSOLUTE MAXIMUM RATINGS

4.1 Absolute Ratings of TFT LCD Module

Item	Symbol	Min.	Max.	Unit	Note
Power supply voltage	VDD	-0.3	5	V	
Logic Signal Input Level	V_{DVDD} V_{DVDD_LVDS}	-0.3	5	V	

4.2 Absolute Ratings of Environment

Item	Symbol	Min.	Max.	Unit	Note
Operating Temperature	T_{opa}	-20	70	℃	
Storage Temperature	T_{stg}	-30	80	℃	

5. ELECTRICAL CHARACTERISTICS

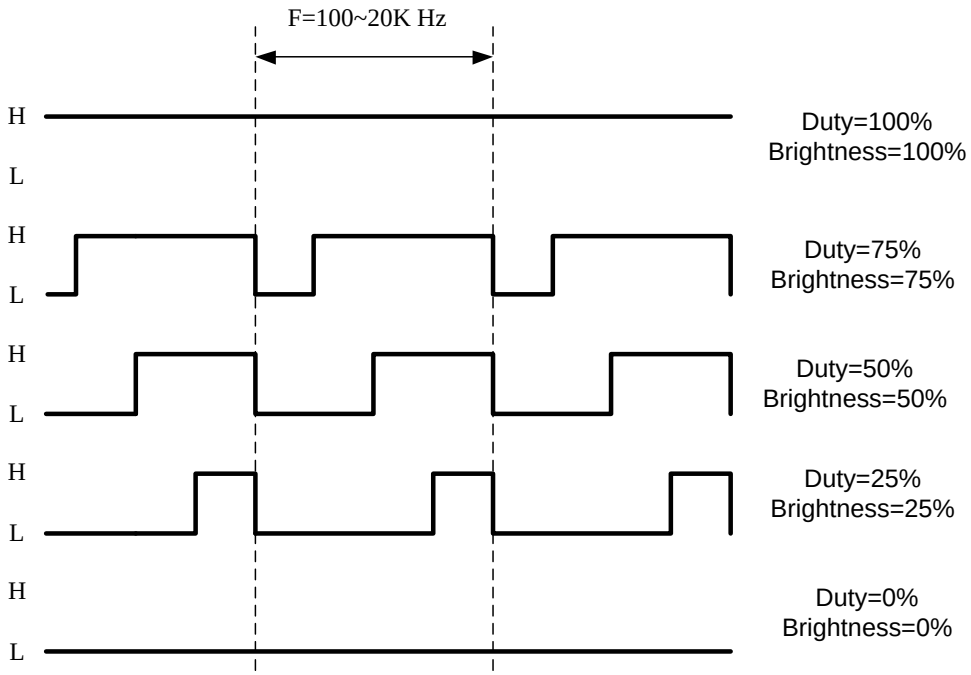
5.1 TFT LCD Module

Item	Symbol	Min.	Typ.	Max.	Unit	Note
Supply Voltage	VDD	3	3.3	3.6	V	
	VLED		12		V	
	ADJ		5		V	
	ADJ Frequency	100		20K	Hz	
Input signal voltage	ViH	0.8 VDD		VLED	V	
	ViL	0		0.2VDD	V	

5.2 Switching Characteristics for LVDS Receiver

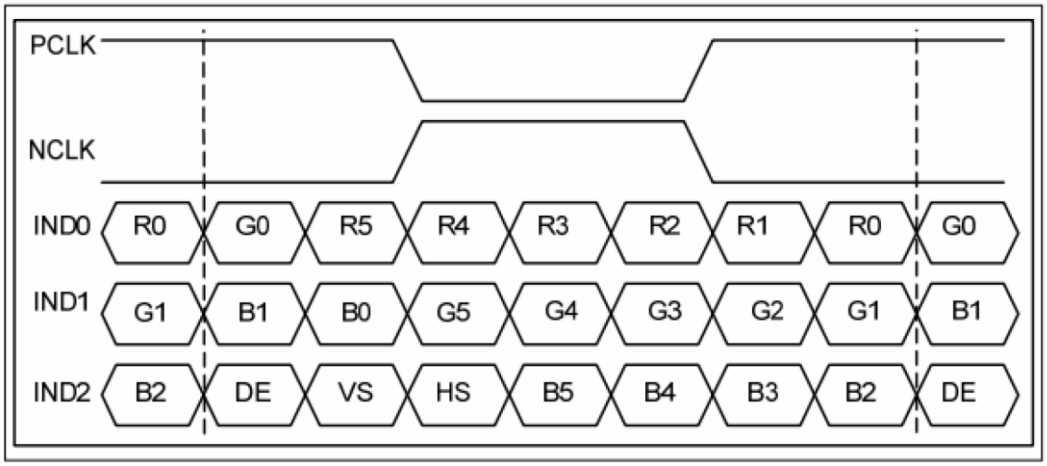
Item	Symbol	Min.	Typ.	Max.	Unit	Conditions
Differential Input High Threshold	Vth			100	mV	V_{CM}=1.2V
Differential Input Low Threshold	Vtl	-100			mV	
Input Current	I_{IN}	-10		10	uA	
Differential input Voltage	$\|V_{ID}\|$	0.1		0.6	V	
Common Mode Voltage Offset	V_{CM}	0.7	1.2	1.6	V	

PWM

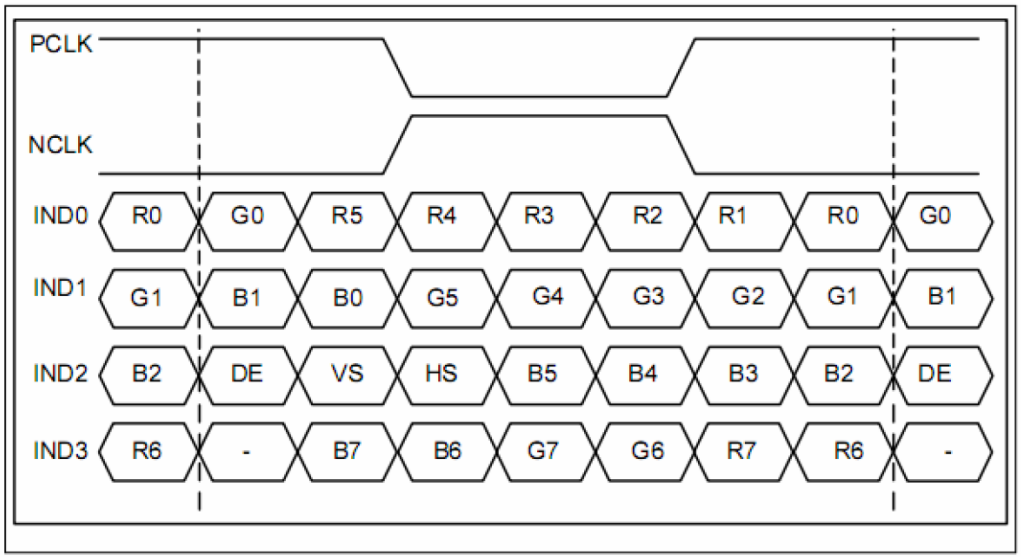


5.3 Bit LVDS input

5.3.1 6Bit LVDS input



5.3.2 8Bit LVDS input

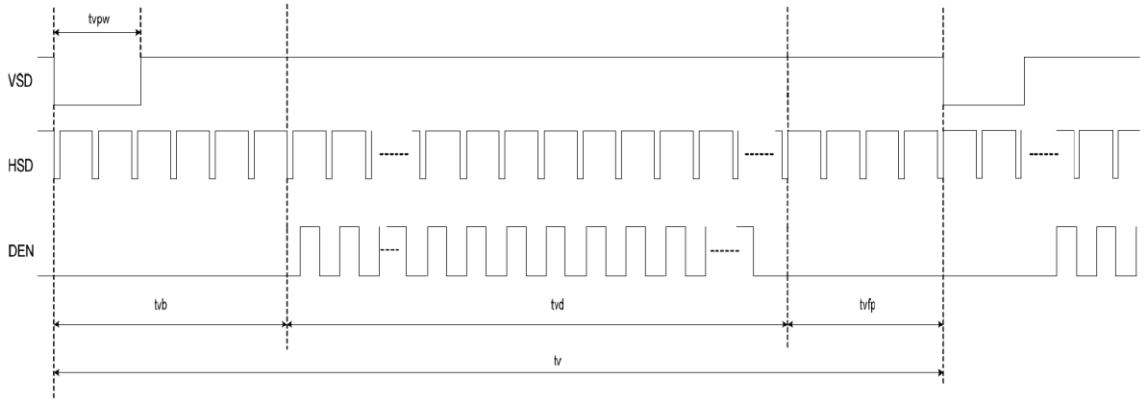


5.4 Interface Timing (DE mode)

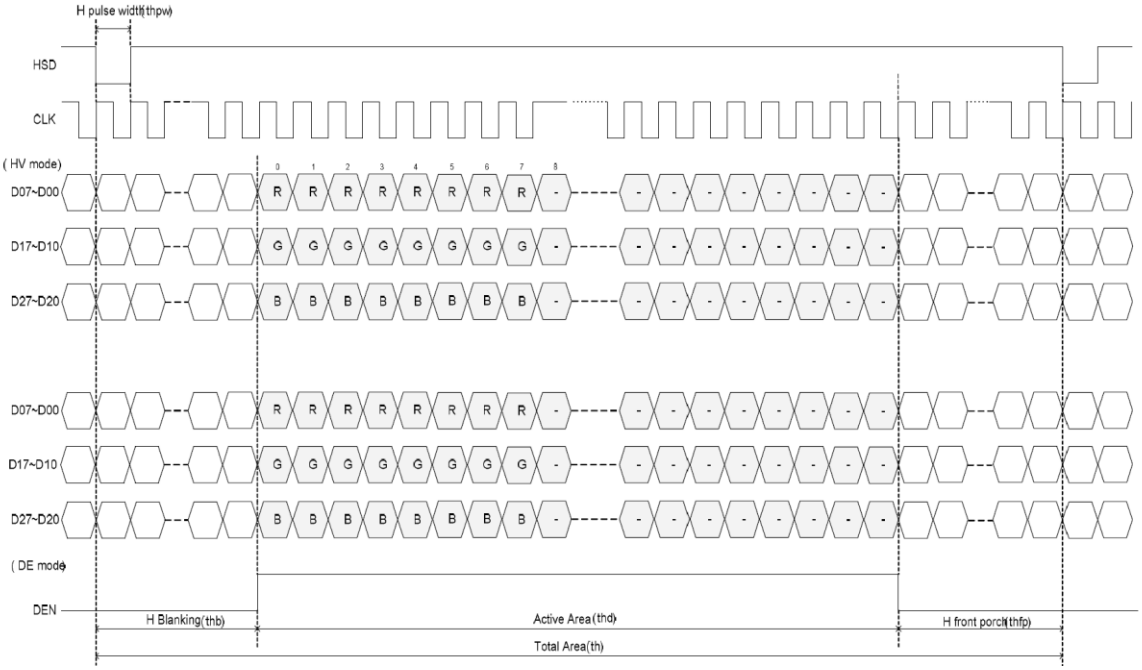
DE mode					
Parameter	Symbol	Value			Unit
		Min.	Typ.	Max.	
DCLK frequency @Frame rate=60hz	fclk	52	65	71	Mhz
Horizontal display area	thd		1024		DCLK
HSYNC period time	th	1114	1344	1400	DCLK
HSYNC blanking	thb+thfp	90	320	376	DCLK
Vertical display area	tvd		768		H
VSYNC period time	tv	778	806	845	H
VSYNC blanking	tvb+tvfp	10	38	77	H

Timing Diagram of Interface Signal (DE mode)

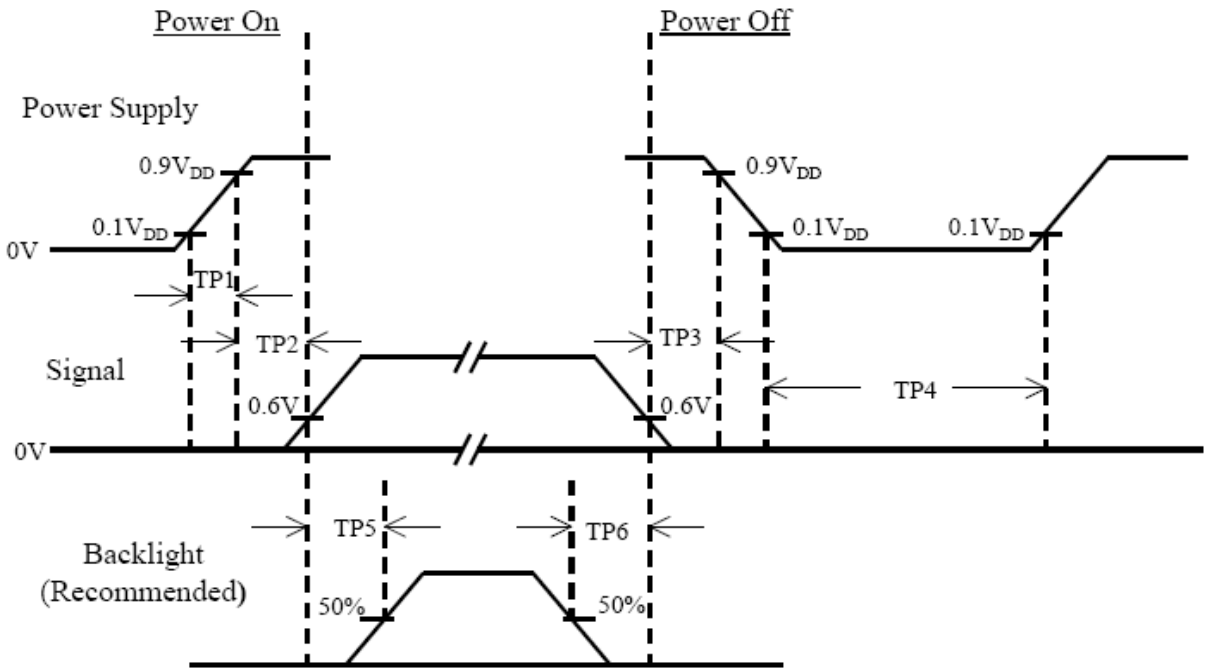
(1). Vertical input timing



(2). Horizontal input timing



5.5 Power On / Off Sequence



Item	Min.	Typ.	Max.	Unit	Remark
TP1	0.5	--	10	msec	
TP2	0	--	50	msec	
TP3	0	--	50	msec	
TP4	500	--	--	msec	
TP5	200	--	--	msec	
TP6	200	--	--	msec	

Note：

(1) The supply voltage of the external system for the module input should be the same as the definition of VDD.

(2) Apply the lamp voltage within the LCD operation range. When the back-light turns on before the LCD operation or the LCD turns off before the back-light turns off, the display may momentarily become white.

(3) In case of VDD = off level, please keep the level of input signal on the low or keep a high impedance.

(4) TP4 should be measured after the module has been fully discharged between power off and on period.

(5) Interface signal shall not be kept at high impedance when the power is on.

5.6 Backlight Unit

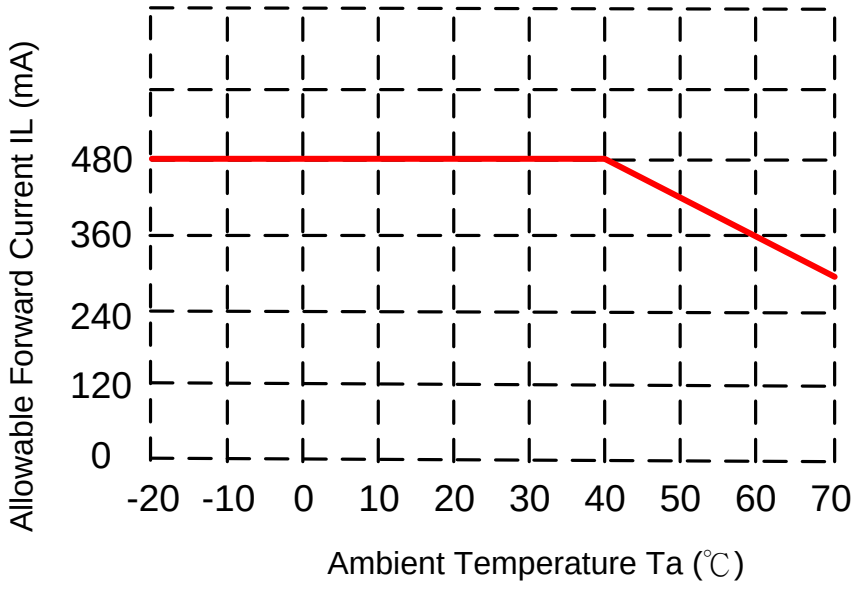
Parameter	Symbol	Min	Typ	Max	Units	Condition
LED Current	I_L	--	480	--	mA	Ta=25℃
LED Voltage	V_L	--	12.9	13.6	Volt	Ta=25℃
LED Life-Time	N/A	30,000	--	--	Hour	Ta=25℃ I_F=60mA Note (2)

Note (1) LED life time (Hr) can be defined as the time in which it continues to operate under the condition: Ta=25±3 °C, typical IL value indicated in the above table until the brightness becomes less than 50%.

Note (2) The "LED life time" is defined as the module brightness decrease to 50% original brightness at Ta=25℃ and IL=480mA. The LED lifetime could be decreased if operating IL is larger than 480mA. The constant current driving method is suggested.

Note (3) LED Light Bar Circuit

When LCM is operated over 40℃ ambient temperature, the IL should be follow :



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6. INTERFACE PIN CONNECTION

CN2 LVDS connector: P1.0 20pin/CP100-S20G-H16

Pin No.	Symbol	I/O	Description	Note
1	VDD	P	Power Voltage for Logic: 3.3V	
2	VDD	P	Power Voltage for Logic: 3.3V	
3	GND	P	Ground	
4	GND	P	Ground	
5	IN0-	I	- LVDS differential data input	
6	IN0+	I	+ LVDS differential data input	
7	GND	P	Ground	
8	IN1-	I	- LVDS differential data input	
9	IN1+	I	+ LVDS differential data input	
10	GND	P	Ground	
11	IN2-	I	- LVDS differential data input	
12	IN2+	I	+ LVDS differential data input	
13	GND	P	Ground	
14	CLK-	I	- LVDS differential data input	
15	CLK+	I	+ LVDS differential data input	
16	GND	P	Ground	
17	IN3-	I	- LVDS differential data input	
18	IN3+	I	+ LVDS differential data input	
19	VLED	P	Power Voltage for Logic: 12V	
20	ADJ	P	Power Voltage for Logic: 5V	

7. Projected capacitive-type touch panel specification

7.1 Basic Characteristic

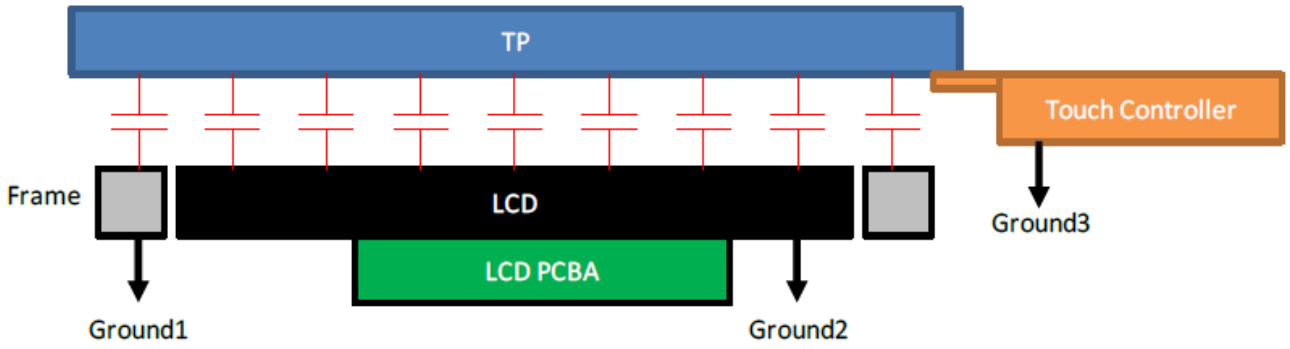
Item	Specification
Interface Type	Projective Capacitive Multi-Touch Panel
Activation	Multi-finger or Single-finger
X/Y Position Reporting	Absolute Position
Touch Force	No contact pressure required
Calibration	No need for calibration
Report Rate	Approx. 80 points/sec
Interface	**USB**
Control IC	**ILI2511**
Protocol	**V3.X**

Item	Symbol	Min.	Typ.	Max.	Unit
Touch panel power supply	PVDD	4.75	5	5.25	V
Touch panel power supply current at Normal operation mode	IPVDD	--	45(Reference)	--	mA
Touch panel power supply current at USB suspend mode	IPVDD	--	TBD	--	μA

7.2 Interface

CN6		
Pin No.	**Symbol**	**Function**
1	VCC 5V	Power Supply for TP controller
2	D+	USB Data+
3	D-	USB Data-
4	NC	Not Connect
5	NC	Not Connect
6	GND	GND

TP needs to work in environment with stable stray capacitance. In order to minimize the variation in stray capacitance, all conductive mechanical parts must not be floating. Intermittent floating any conductive part around the touch sensor may cause significant stray capacitance change and abnormal touch function. It is recommended to keep all conductive parts having same electrical potential as the GND of the touch controller module.



GND1, GND2 and GND3 should be connected together to have the same ground

8. RELIABILITY TEST CRITERIA

Test Item	Test Conditions	Note
High Temperature Operation	70±3°C , t=240 hrs	
Low Temperature Operation	-20±3°C , t=240 hrs	
High Temperature Storage	80±3°C , t=240 hrs	1,2
Low Temperature Storage	-30±3°C , t=240 hrs	1,2
Storage at High Temperature and Humidity	60°C, 90% RH , 240 hrs	1,2
Thermal Shock Test	-20°C (30min) ~ 70°C (30min) 100 cycles	1,2
Vibration Test (Packing)	Sweep frequency : 10 ~ 55 ~ 10 Hz/1min Amplitude : 0.75mm Test direction : X.Y.Z/3 axis Duration : 30min/each axis	2

Note 1: Condensation of water is not permitted on the module.

Note 2: The module should be inspected after 1 hour storage in normal conditions (15-35°C, 45-65%RH).

Note 3: The module shouldn't be tested more than one condition, and all the test conditions are independent.

Note 4: All the reliability tests should be done without protective film on the module.

Definitions of life end point:

- Current drain should be smaller than the specific value.
- Function of the module should be maintained.
- Appearance and display quality should not have degraded noticeably.
- Contrast ratio should be greater than 50% of the initial value.

9. USE PRECAUTIONS

9.1 Handling precautions

1) The polarizing plate may break easily so be careful when handling it. Do not touch, press or rub it with a hard-material tool like tweezers.
2) Do not touch the polarizing plate surface with bare hands so as not to make it dirty. If the surface or other related part of the polarizing plate is dirty, soak a soft cotton cloth or chamois leather in benzine and wipe off with it. Do not use chemical liquids such as acetone, toluene and isopropyl alcohol. Failure to do so may bring chemical reaction phenomena and deteriorations.
3) Remove any spit or water immediately. If it is left for hours, the suffered part may deform or decolorize.
4) If the LCD element breaks and any LC stuff leaks, do not suck or lick it. Also if LC stuff is stuck on your skin or clothing, wash thoroughly with soap and water immediately.

9.2 Installing precautions

1) The PCB has many ICs that may be damaged easily by static electricity. To prevent breaking by static electricity from the human body and clothing, earth the human body properly using the high resistance and discharge static electricity during the operation. In this case, however, the resistance value should be approx. 1MΩ and the resistance should be placed near the human body rather than the ground surface. When the indoor space is dry, static electricity may occur easily so be careful. We recommend the indoor space should be kept with humidity of 60% or more. When a soldering iron or other similar tool is used for assembly, be sure to earth it.
2) When installing the module and ICs, do not bend or twist them. Failure to do so may crack LC element and cause circuit failure.
3) To protect LC element, especially polarizing plate, use a transparent protective plate (e.g., acrylic plate, glass etc) for the product case.
4) Do not use an adhesive like a both-side adhesive tape to make LCD surface (polarizing plate) and product case stick together. Failure to do so may cause the polarizing plate to peel off.

9.3 Storage precautions

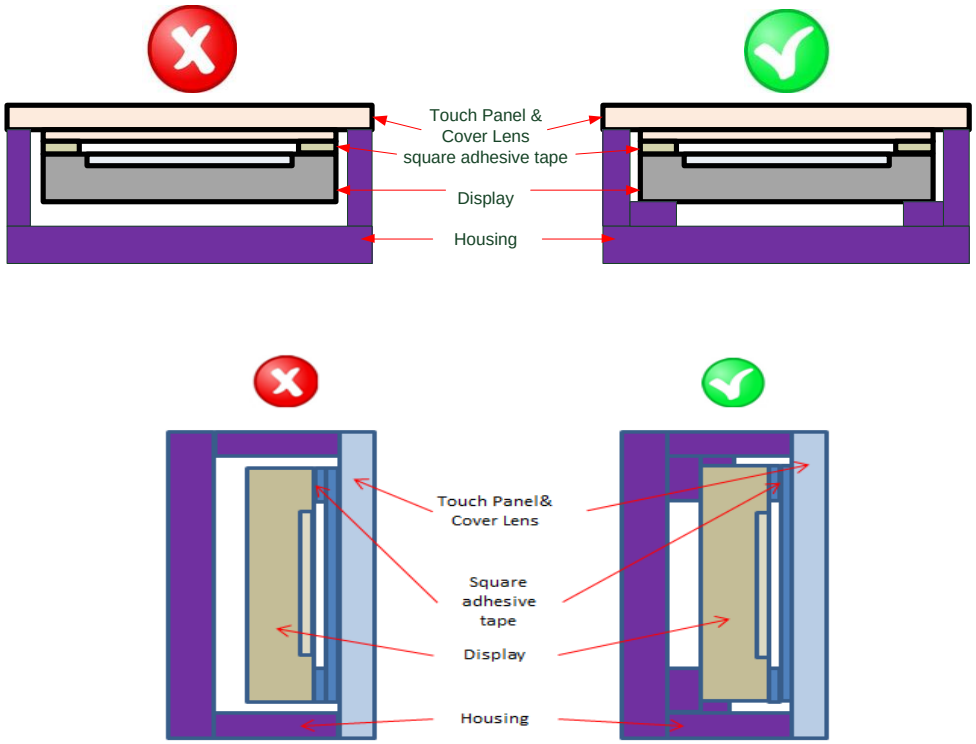
1) Avoid a high temperature and humidity area. Keep the temperature between 0°C and 35°C and also the humidity under 60%.
2) Choose the dark spaces where the product is not exposed to direct sunlight or fluorescent light.
3) Store the products as they are put in the boxes provided from us or in the same conditions as we recommend.

9.4 Operating precautions

1) Do not boost the applied drive voltage abnormally. Failure to do so may break ICs. When applying power voltage, check the electrical features beforehand and be careful. Always turn off the power to the LC module controller before removing or inserting the LC module input connector. If the input connector is removed or inserted while the power is turned on, the LC module internal circuit may break.
2) The display response may be late if the operating temperature is under the normal standard, and the display may be out of order if it is above the normal standard. But this is not a failure; this will be restored if it is within the normal standard.
3) The LCD contrast varies depending on the visual angle, ambient temperature, power voltage etc. Obtain the optimum contrast by adjusting the LC dive voltage.
4) When carrying out the test, do not take the module out of the low-temperature space suddenly. Failure to do so will cause the module condensing, leading to malfunctions.
5) Make certain that each signal noise level is within the standard (L level: 0.2Vdd or less and H level: 0.8Vdd or more) even if the module has functioned properly. If it is beyond the standard, the module may often malfunction. In addition, always connect the module when making noise level measurements.
6) The CMOS ICs are incorporated in the module and the pull-up and pull-down function is not adopted for the input so avoid putting the input signal open while the power is ON.
7) The characteristic of the semiconductor element changes when it is exposed to light emissions, therefore ICs on the LCD may malfunction if they receive light emissions. To prevent these malfunctions, design and assemble ICs so that they are shielded from light emissions.
8) Crosstalk occurs because of characteristics of the LCD. In general, crosstalk occurs when the regularized display is maintained. Also, crosstalk is affected by the LC drive voltage. Design the contents of the display, considering crosstalk.

9.5 Mechanism

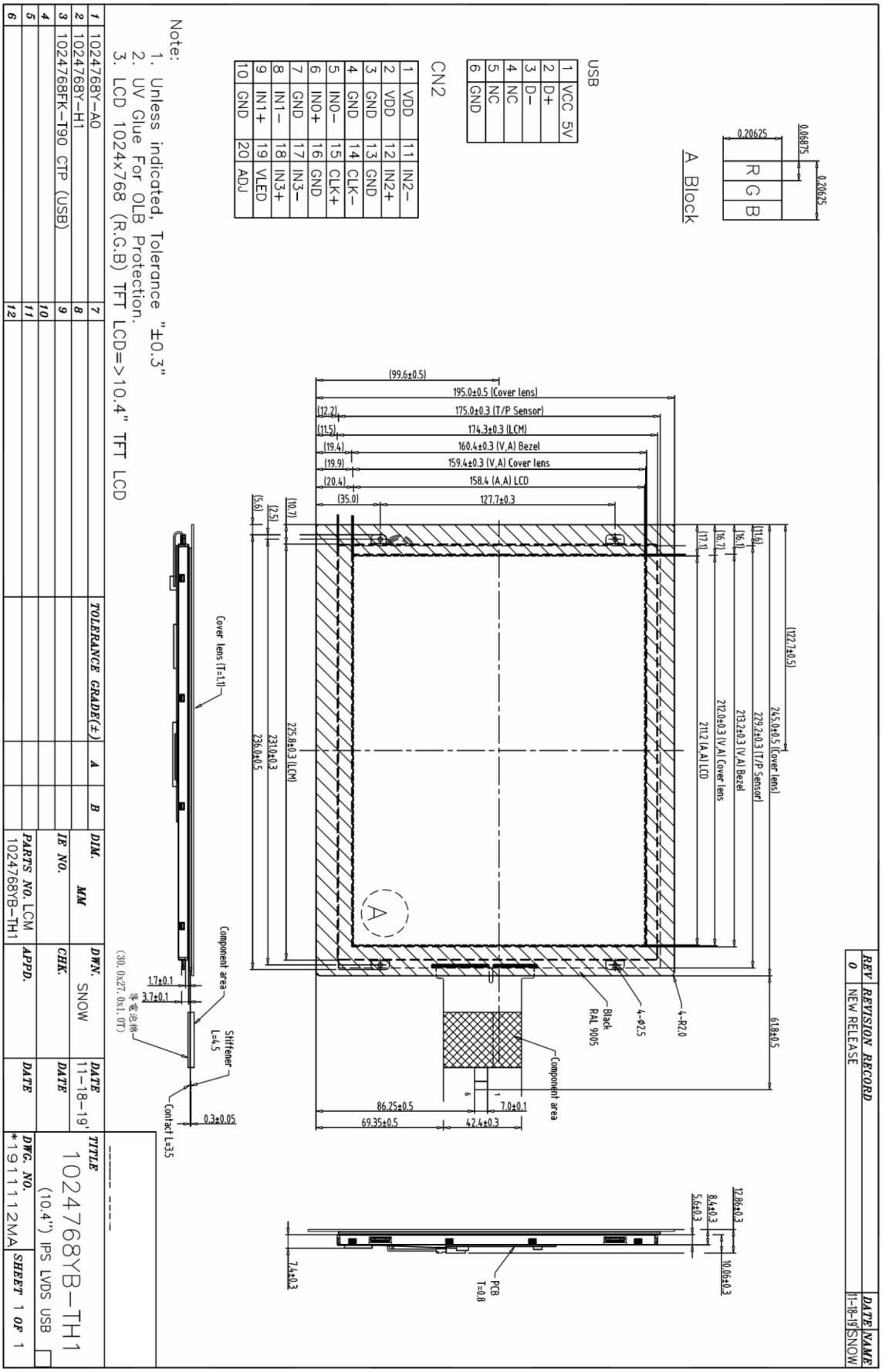
1) Please mount LCD module by using mounting holes arranged in four corners tightly.
2) The square adhesive tape which is between the touch panel and display can't provide well supporting in the long term and high ambient temperature condition. Whether upright or horizontal position the support holder which is in the back side of the display is needed. Do not let the display floating.



9.6 Other

1) Do not disassemble or take the LC module into pieces. The LC modules once disassembled or taken into pieces are not the guarantee articles.
2) Do not keep the LCD at the same display pattern continually. The residual image will happen and it will damage the LCD. Please use screen saver.
3) AMIPRE will provide one year warranty for all products and three months warrantee for all repairing products.

9. OUTLINE DIMENSION



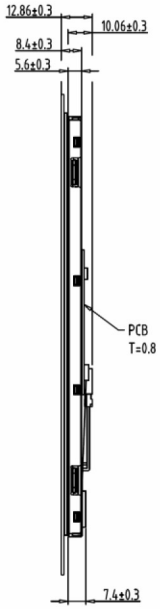
REV	REVISION RECORD	DATE	NAME
0	NEW RELEASE	11-18-19	SNOW

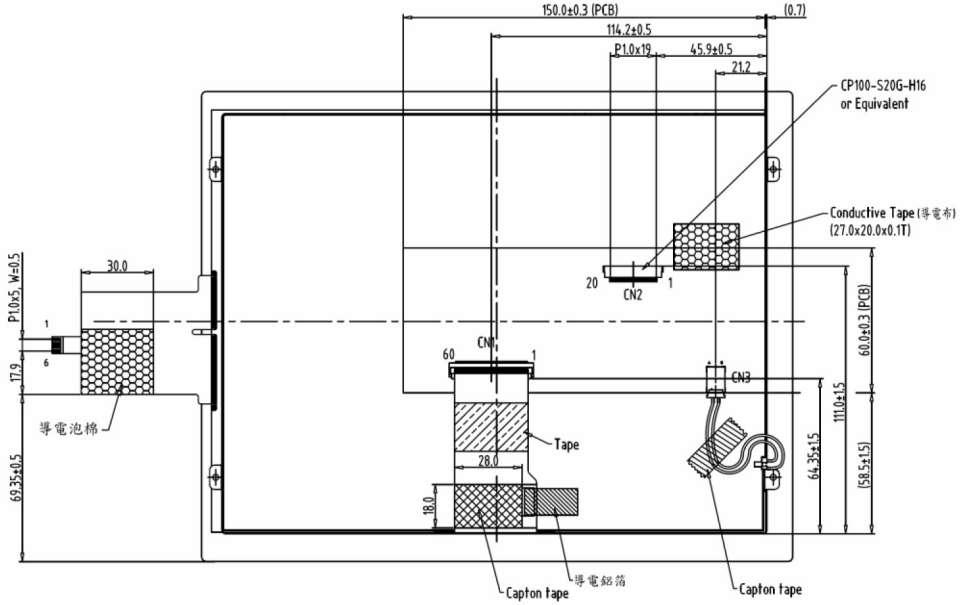
USB

1	VCC 5V
2	D+
3	D−
4	NC
5	NC
6	GND

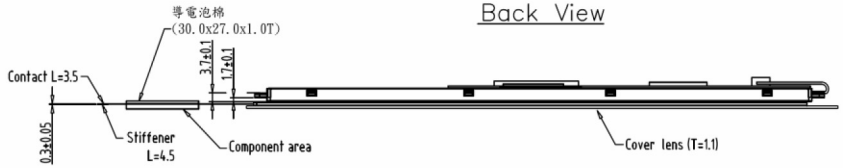
CN2

1	VDD	11	IN2−
2	VDD	12	IN2+
3	GND	13	GND
4	GND	14	CLK−
5	IN0−	15	CLK+
6	IN0+	16	GND
7	GND	17	IN3−
8	IN1−	18	IN3+
9	IN1+	19	VLED
10	GND	20	ADJ





Back View



Note:
1. Unless indicated, Tolerance "±0.3"
2. UV Glue For OLB Protection.
3. LCD 1024x768 (R.G.B) TFT LCD=>10.4" TFT LCD

				TOLERANCE GRADE(±)	A	B	DIM. MM	DWN. SNOW	DATE 11-18-19'	TITLE 1024768YB-TH1 (10.4") IPS LVDS USB
1	1024768Y-A0	7								
2	1024768Y-H1	8					IE NO.	CHK.	DATE	
3	1024768FK-T90 CTP (USB)	9								
4		10					PARTS NO. LCM-1 1024768YB-TH1	APPD.	DATE	DWG. NO. *1911113MA SHEET 1 OF 1
5		11								
6		12								

Date: 2020/08/10

Auxiliary

AMPIRE HDMI Board

REV.D

Intelligent Display Solutions, Unit 2, Berkshire Business Centre, Berkshire Drive, Thatcham, Berkshire, RG19 4EW
Telephone : +44 (0)1635 294600 Fax : +44 (0)1635 869200 Email: info@i-lcd.com
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RECORD OF REVISION

Revision Date	Page	Contents	Editor
2018/06/19	-	New Release	Mark

Intelligent Display Solutions, Unit 2, Berkshire Business Centre, Berkshire Drive, Thatcham, Berkshire, RG19 4EW
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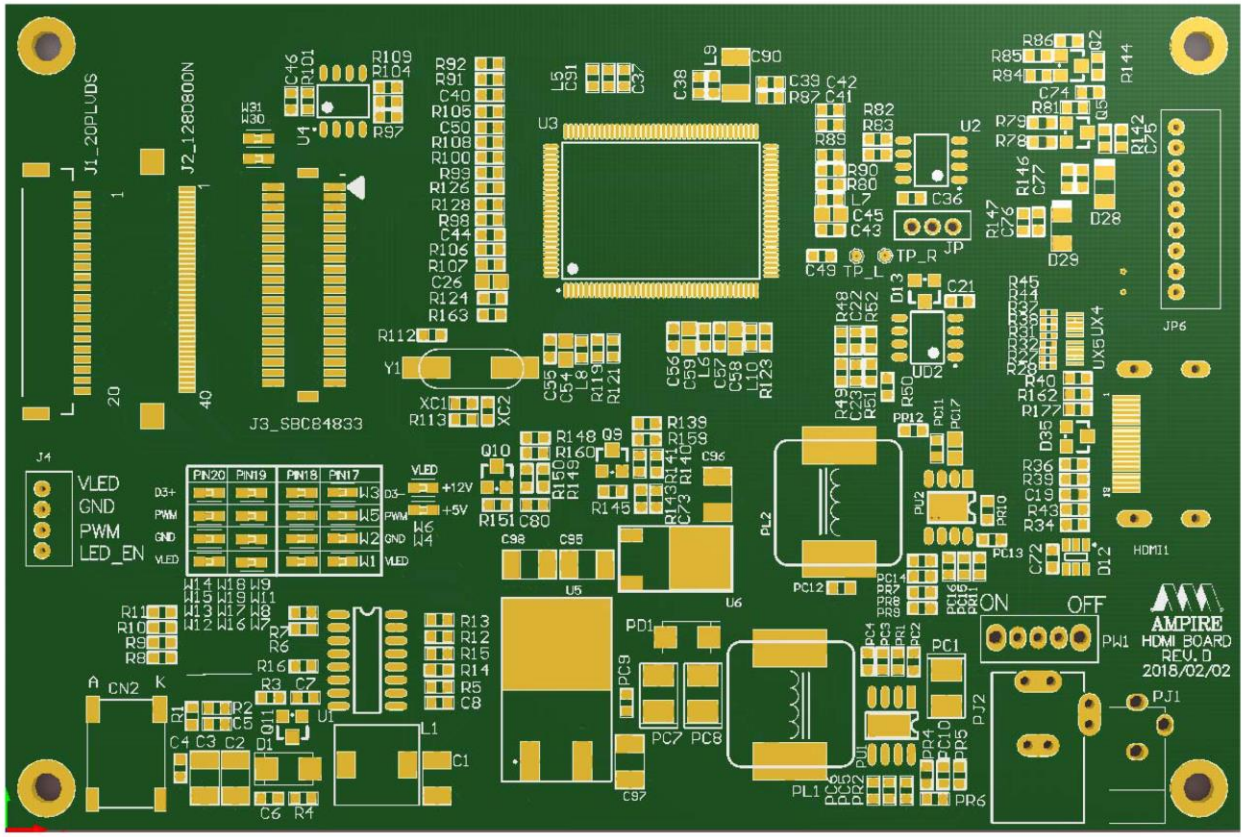
1. Features

HDMI to LCD interface board

- Single Power input: 12V / 2A power input. (Connector: PJ1 or PJ2).
- LCD LVDS output: 24 BIT Single LVDS
- HDMI Digital input : (Connector: HDMI1)
  - HDMI 1.4a Compliant
  - Single-link (Type A HDMI) on-chip TMDS receiver up to 225MHz. Support long cable.
  - Do not support HDCP.



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2. Support input video format :

Resolution	V Sync	Resolution	V Sync
640x480	60	1280x800	60
640x480	72	1280x800	75
640x480	75	1280x960	60
800x600	56	1280x1024	60
800x600	60	1280x1024	75
800x600	72	1360x768	60
800x600	75	1366x768	60
848x480	60	1400x1050	60
1024x768	60	1400x1050	75
1024x768	70	1440x900	60
1024x768	75	1440x900	75
1152x864	75	1600x900	60
1280x720	60	1680x1050	60
1280x768	60	1680x1050	75
1280x768	75	1920x1080	60

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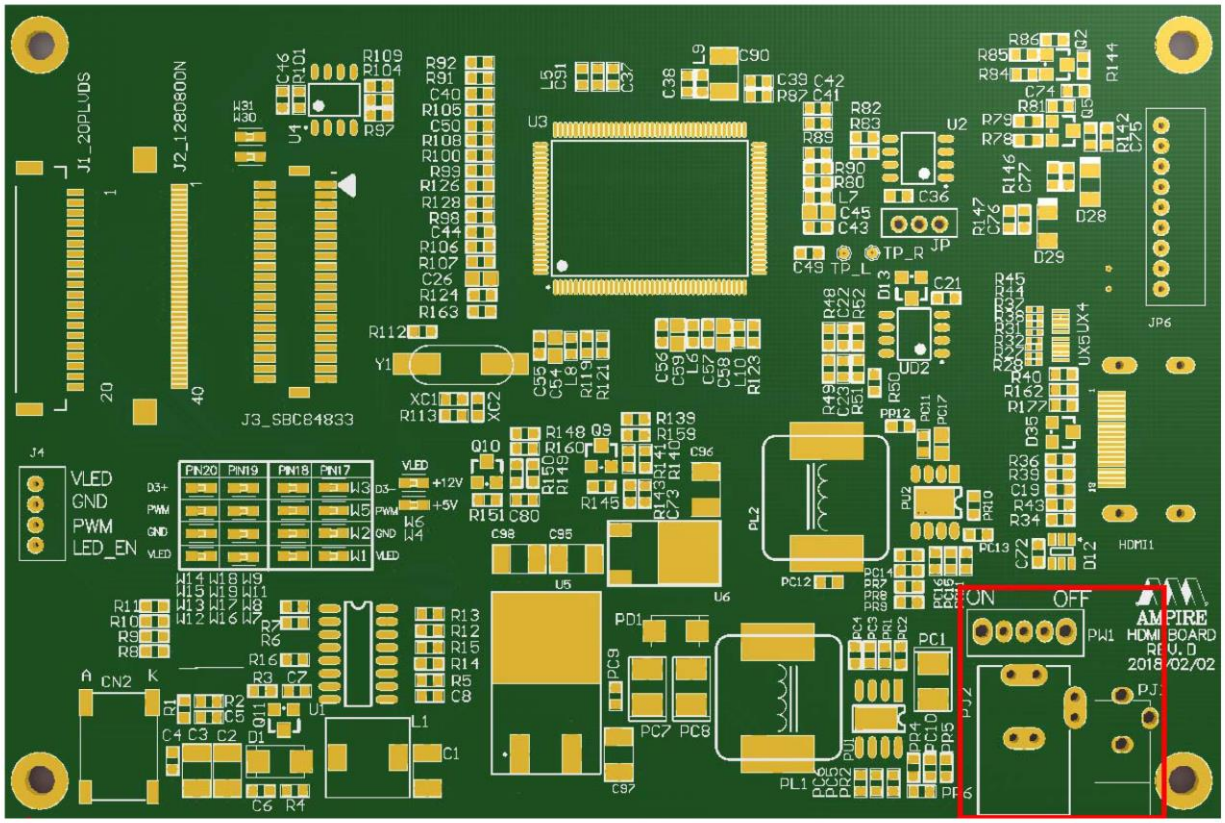
3. CONNECTOR

3.1 POWER CONNECTOR (PJ1、PJ2)

PIN	Symbol	Description
1	+12V	POWER SUPPLY +12V
3	GND	POWER SUPPLY GROUND



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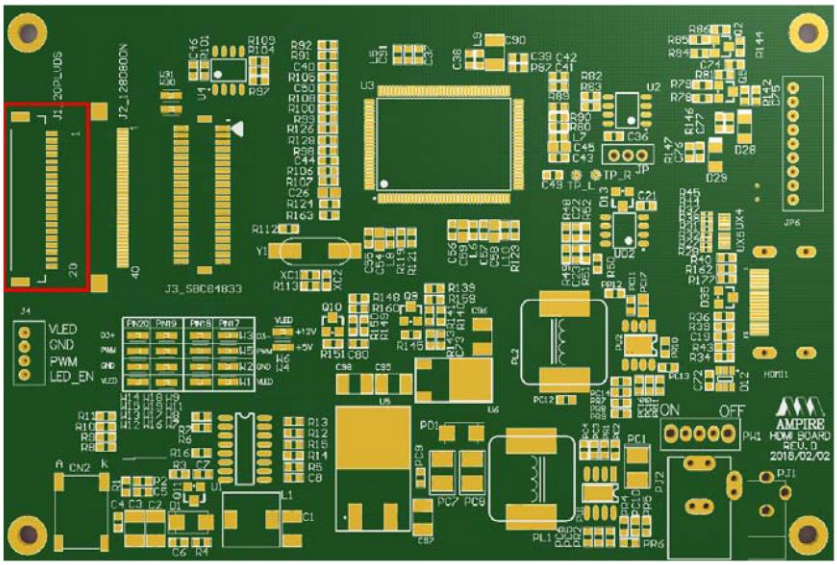
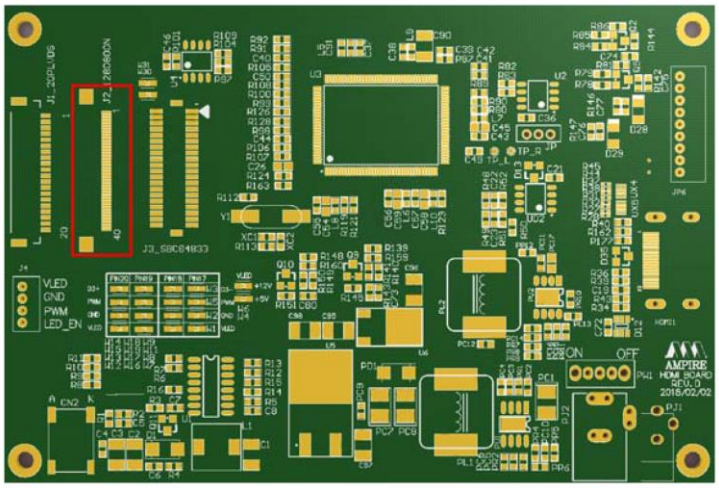
3.2 J1_20PIN LVDS

Pin No.	Symbol	Function
1	VDD	POWER SUPPLY:3.3V
2	VDD	POWER SUPPLY:3.3V
3	GND	Power Ground
4	GND	Power Ground
5	IN0-	Transmission Data of Pixels
6	IN0+	Transmission Data of Pixels
7	GND	Power Ground
8	IN1-	Transmission Data of Pixels 1
9	IN1+	Transmission Data of Pixels 1
10	GND	Power Ground
11	IN2-	Transmission Data of Pixels 2
12	IN2+	Transmission Data of Pixels 2
13	GND	Power Ground
14	CLK-	Sampling Clock
15	CLK+	Sampling Clock
16	GND	Power Ground
17	JUMP	JUMP
18	JUMP	JUMP
19	JUMP	JUMP
20	JUMP	JUMP



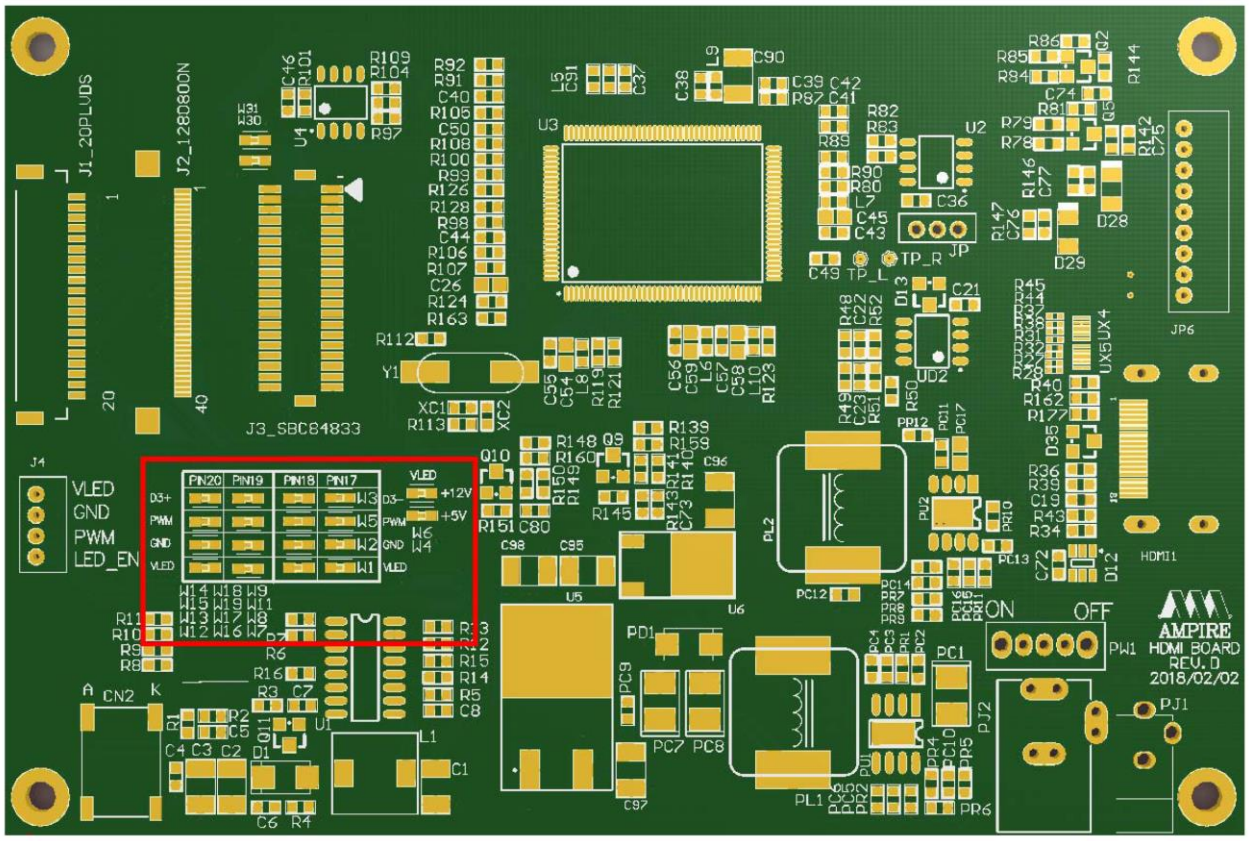
3.3 J2_40PIN LVDS

Pin #	Signal Name	Description	Remarks
1	NC	Not Connect	-
2	VDD	Power Supply, 3.3V (typical)	-
3	VDD	Power Supply, 3.3V (typical)	
4	NC	Not Connect	
5	NC	Not Connect	
6	NC	Not Connect	
7	NC	Not Connect	
8	LV0N	-LVDS differential data input	
9	LV0P	+LVDS differential data input	
10	GND	Ground	
11	LV1N	-LVDS differential data input	
12	LV1P	+LVDS differential data input	
13	GND	Ground	
14	LV2N	-LVDS differential data input	
15	LV2P	+LVDS differential data input	
16	GND	Ground	
17	LVCLKN	-LVDS differential data input	
18	LVCLKP	+LVDS differential data input	
19	GND	Ground	
20	LV3N	-LVDS differential data input	
21	LV3P	+LVDS differential data input	
22	GND	Ground	
23	LED_GND	Ground for LED Driving	
24	LED_GND	Ground for LED Driving	
25	LED_GND	Ground for LED Driving	
26	NC	Not Connect	
27	LED_PWM	PWM Input signal for LED driver	
28	LED_EN	LED Enable Pin	
29	Not Connect	NC	
30	NC	Not Connect	
31	LED_VCC	Power Supply for LED Driver	
32	LED_VCC	Power Supply for LED Driver	
33	LED_VCC	Power Supply for LED Driver	
34	NC	Not Connect	
35	BIST	BIST pin. (Keep NC or GND if not use.)	
36-40	NC	Not Connect	



JUMP FOR PIN17,18,19 and VLED

- **These jump only for J1_20PLVDS**
- **For Design reference only. These supply voltage and signals do not need to input by end user.**



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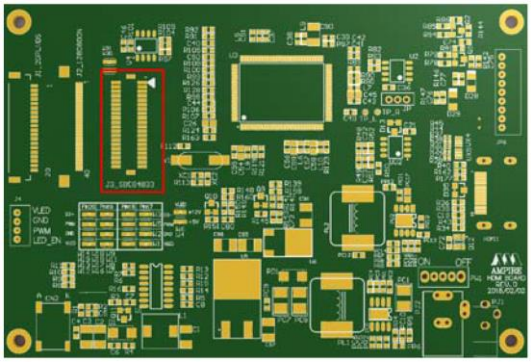
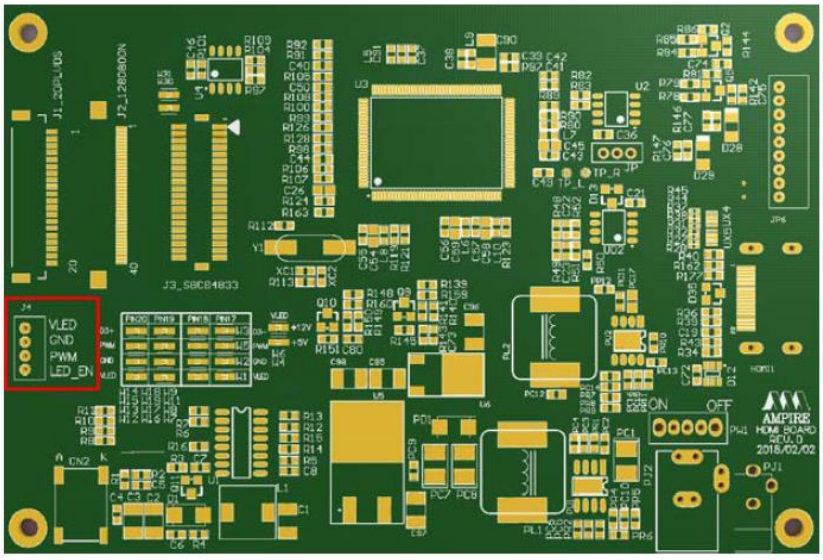
3.4 J3_40PIN LVDS

Pin #	Signal Name	Description	Remarks
1	VDD	Power Supply, 3.3V (typical)	-
2	VDD	Power Supply, 3.3V (typical)	-
3	VDD	Power Supply, 3.3V (typical)	
4	VDD	Power Supply, 3.3V (typical)	
5	VDD	Power Supply, 3.3V (typical)	
6	VDD	Power Supply, 3.3V (typical)	
7	NC	Not Connect	
8	NC	Not Connect	
9	GND	Ground	
10	GND	Ground	
11	LV8N	-LVDS differential data input	
12	LV5N	-LVDS differential data input	
13	LV8P	+LVDS differential data input	
14	LV5P	+LVDS differential data input	
15	GND	Ground	
16	GND	Ground	
17	LVCLK1N	-LVDS differential data input	
18	LV6N	-LVDS differential data input	
19	LVCLK1P	+LVDS differential data input	
20	LV6P	+LVDS differential data input	
21	GND	Ground	
22	GND	Ground	
23	LV0N	-LVDS differential data input	
24	LV7N	-LVDS differential data input	
25	LV0P	+LVDS differential data input	
26	LV7P	+LVDS differential data input	
27	GND	Ground	
28	GND	Ground	
29	LV1N	-LVDS differential data input	
30	LV3N	-LVDS differential data input	
31	LV3P	+LVDS differential data input	
32	LV7P	+LVDS differential data input	
33	GND	Ground	
34	GND	Ground	
35	LV2N	-LVDS differential data input	
36	LVCLK0N	-LVDS differential data input	
37	LV2P	+LVDS differential data input	
38	LVCLK0P	+LVDS differential data input	
39	GND	Ground	
40	GND	Ground	



3.5 J4_BackLight Controller Connector

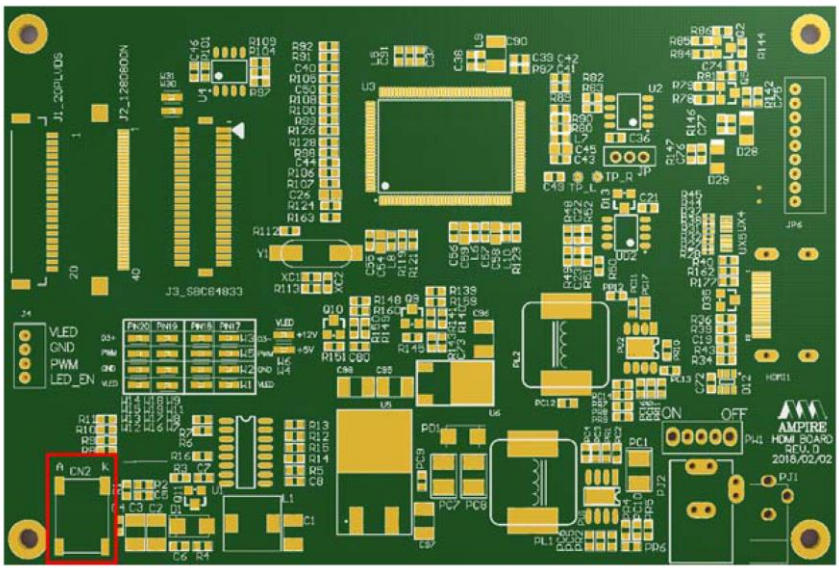
Pin No.	Symbol	I/O	Description	Note
1	VLED	P	Voltage for LED circuit (5.0V or 12V)	
2	GND	I	Power ground	
3	ADJ	P	Adjust the LED brightness by PWM	
4	LED_EN	I	LED BLU ON/OFF. High level: ON; Low level: OFF.	



3.6 BackLight _A,K Connector

- **Only for external backlight connector**

Pin No.	Symbol	Description
1	A	Anode
2	K	Cathode



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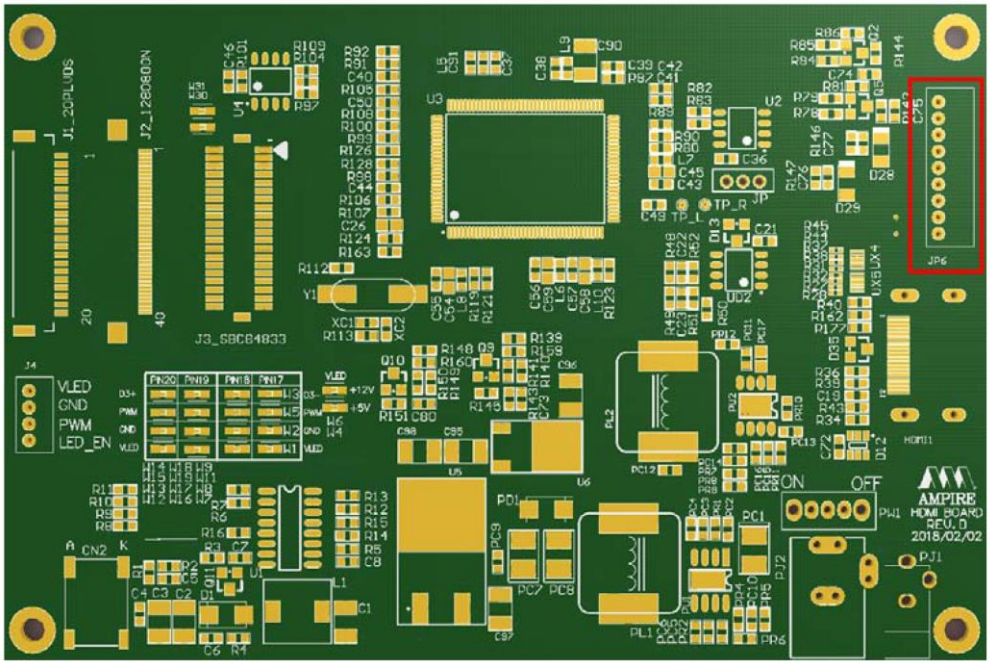
3.7 JP6 Keypad connector for HDMI Board

- **Optional item**
- **If customer need, please check with Ampire sales for new part no. and sample.**



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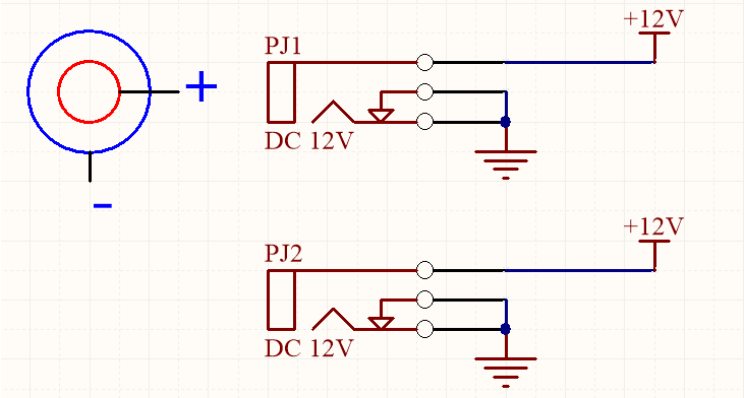
4. INTERFACE PIN CONNECTION

INTERFACE (HDMI Interface Board)

- **PJ1 & PJ2 Power Supply Power Jack:**

  Inner terminal is positive. Outer terminal is GND



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- **HDMI1: HDMI Type A Connector**

HDMI			
			
PIN	**SIGNAL**	**PIN**	**SIGNAL**
1	TMDS Data2+	11	TMDS Clock Shield (Ground)
2	TMDS Data2 Shield (Ground)	12	TMDS Clock-
3	TMDS Data2-	13	CEC (not used)
4	TMDS Data1+	14	Reserved (No Connection)
5	TMDS Data1 Shield (Ground)	15	SCL
6	TMDS Data1-	16	SDA
7	TMDS Data0+	17	DDC/CED (Ground)
8	TMDS Data0 Shield (Ground)	18	+5V input
9	TMDS Data0-	19	Hot Plug Detect
10	TMDS Clock+		

5. RELIABILITY TEST CONDITIONS

Test Item	Test Conditions	Note
High Temperature Operation	70±3°C ,Dry t=240 hrs	
Low Temperature Operation	-20±3°C, Dry t=240 hrs	
High Temperature Storage	80±3°C , Dry t=240 hrs	1,2
Low Temperature Storage	-30±3°C ,Dry t=240 hrs	1,2
Thermal Shock Test	-20°C ~ 25°C ~ 70°C 30 m in. 5 min. 30 min. (1 cycle) Total 100 cycle(Dry)	1,2
Storage Humidity Test	60 °C, Humidity 90%, 240 hrs	1,2
Vibration Test (Packing)	Sweep frequency : 10 ~ 55 ~ 10 Hz/1min Amplitude : 0.75mm Test direction : X.Y.Z/3 axis Duration : 30min/each axis	2

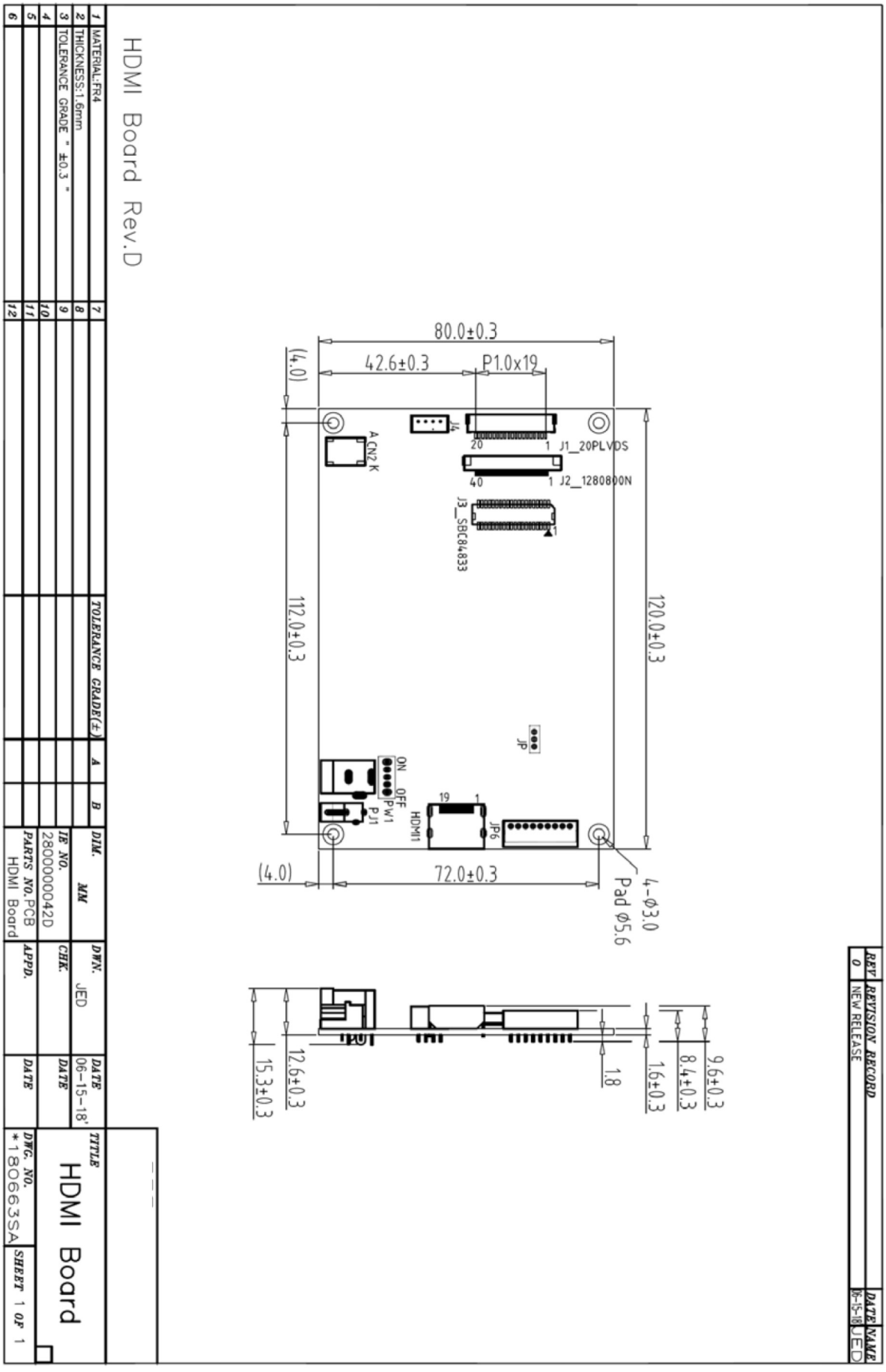
Note 1 : Condensation of water is not permitted on the module.

Note 2 : The module should be inspected after 1 hour storage in normal conditions (15-35°C , 45-65%RH).

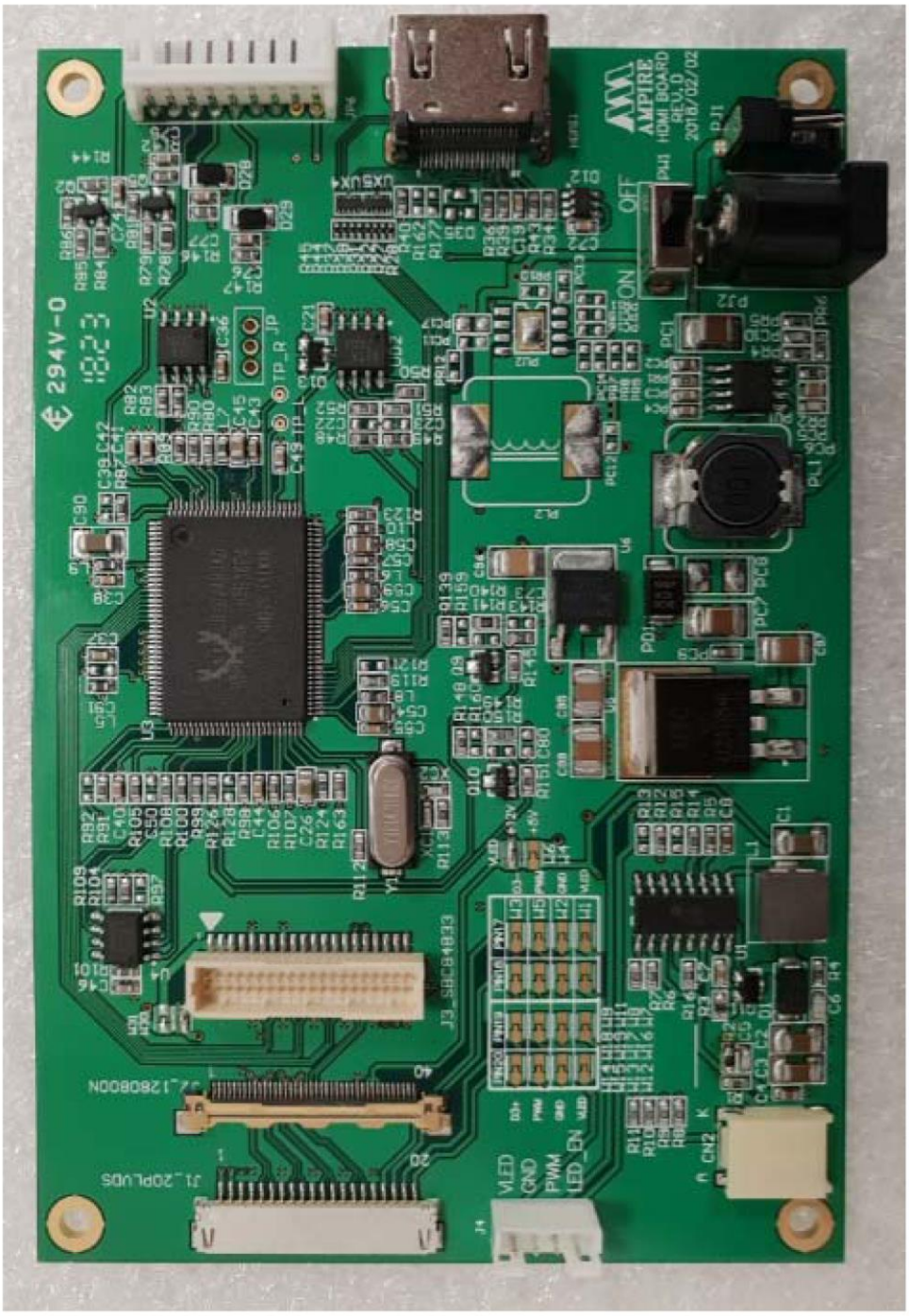
Definitions of life end point :

- Current drain should be smaller than the specific value.
- Function of the module should be maintained.

6. Outline Dimension



7. Photo



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