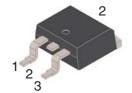
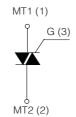
**RoHS** 



## HIGH COMMUTATION TRIAC

### **TO-263AB (D2PAK)**





#### On-State Current

#### **Gate Trigger Current**

25 Amp

 $\leq$  50 mA

**Off-State Voltage** 

400 V ÷ 800 V

#### **FEATURES**

- Glass/passivated die junctions
- High current Triac
- Low thermal resistance
- Ideal for automated placement



- High surge current capability
- Low forward voltage drop
- Solder dip 260°C, 10s
- Component in accordance to RoHS 2011/65/EU and WEEE 2002/96/EC
- Meets MSL level 3, per J-STD-020, LF maximum peak of 260° C

#### **MECHANICAL DATA**

- Case: TO-263AB (D2PAK). Epoxy meets UL 94V-0 flammability rating.
- Polarity: As marked on the body.
- **Terminals:** Matte tin plated leads, solderable per MIL-STD-750 Method 2026, J-STD-002 and JESD22-B102. Consumer grade, meets JESD 201 class 1A whisker test.

#### **TYPICAL APPLICATIONS**

 Used on inductive loads, thanks to their high commutation performance.

# Maximun Ratings and Electrical Characteristics at 25°C

SYMBOL	PARAMETER	CONDITIONS	Value	Unit
I <sub>T(RMS)</sub>	RMS On-state Current (full sine wave)	All Conduction Angle, T <sub>c</sub> = 100 °C	25	А
I <sub>TSM</sub>	Non-repetitive On-State Current	Full Cycle, 60 Hz (t = 16.7 ms)	215	А
I <sub>TSM</sub>	Non-repetitive On-State Current	Full Cycle, 50 Hz (t = 20 ms)	200	А
I <sup>2</sup> t	Fusing Current	tp = 10 ms, Half Cycle	313	A <sup>2</sup> s
I <sub>GM</sub>	Peak Gate Current	20 μs max. Tj = 125 °C	4	А
$P_{G(AV)}$	Average Gate Power Dissipation	Tj = 125 °C	1	W
dI/dt	Critical rate of rise of on-state current	$I_G = 2x I_{GT}, t_r \le 100 \text{ns}$	50	A/µs
		f = 120 Hz, T <sub>j</sub> = 125 °C		
T <sub>j</sub>	Operating Temperature		(-40 +125)	°C
T <sub>stg</sub>	Storage Temperature		(-40 +150)	°C
T <sub>sld</sub>	Soldering Temperature	10s max	260	°C

SYMBOL	PARAMETER	VOLTAGE			Unit
		D	M	N	OTHE
$V_{DRM}/V_{RRM}$	Repetitive Peak Off State Voltage	400	600	800	V

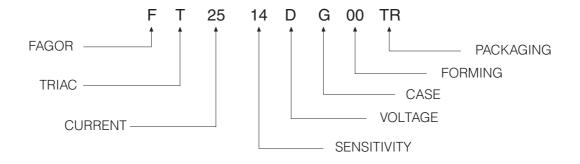


## Electrical Characteristics at Tamb = 25 °C

CVMDOL	DADAMETED	CONDITIONS	0		SENSITIVITY		1125
SYMBOL	PARAMETER	CONDITIONS	Quadrant		14	16	Unit
I <sub>GT</sub> <sup>(1)</sup>	Gate Trigger Current	$V_D=12V_{DC},R_L=33\Omega,\;\;T_j=25\;{}^{\circ}C$	Q1÷Q3	MAX	35	50	mA
V <sub>GT</sub>	Gate Trigger Voltage	$V_D=12V_{DC},R_L=33\Omega,\ T_j=25\;{}^{\circ}C$	Q1÷Q3	MAX	1	.3	V
$V_{GD}$	Gate Non Trigger Voltage	$V_D = V_{DRM}, R_L = 3.3~K\Omega, T_j = 125~^{\circ}C$	Q1÷Q3	MIN	0	.2	V
I <sub>H</sub> <sup>(2)</sup>	Holding Current	$I_T$ =100 mA,Gate open, $T_j$ = 25 °C		MAX	35	50	mA
IL	Latching Current	$I_G = 1.2 I_{GT}, T_j = 25 ^{\circ}\text{C}$	Q1, Q3	MAX	50	70	mA
			Q2	MAX	60	80	mA
dV/dt (2)	Critical Rate of Voltage Rise	$V_D = 0.67 \times V_{DRM}$ , Gate open		MIN	500	1000	V/µs
		$T_j = 125  ^{\circ}\text{C}$					
(dl/dt)c (2)	Critical Rate of Current Rise	$(dv/dt)c = 0.1 V/\mu s$ $T_j = 125 °C$		MIN	-	-	A/ms
		$(dv/dt)c = 10 V/\mu s$ $T_j = 125 °C$		MIN	-	-	
		without snubber $T_j = 125$ °C		MIN	13	22	
V <sub>TM</sub> <sup>(2)</sup>	On-state Voltage	$I_T = 35$ Amp, tp = 380 $\mu$ s, $T_j = 25$ °C		MAX	1.	55	V
V <sub>t (0)</sub> (2)	Threshold Voltage	T <sub>j</sub> = 125 °C		MAX	0.	85	V
r <sub>d</sub> <sup>(2)</sup>	Dynamic resistance	T <sub>j</sub> = 125 °C		MAX	16	6	mΩ
I <sub>DRM</sub> /I <sub>RRM</sub>	Off-State Leakage Current	$V_D = V_{DRM}, \hspace{1cm} T_j = 125 \ ^{\circ}C$		MAX	2	2	mA
		$V_R = V_{RRM}$ , $T_j = 25  ^{\circ}C$		MAX	į	5	μΑ
R <sub>th(j-c)</sub>	Thermal Resistance	for AC 360° conduction angle			1	.0	°C/W
	Junction-Case						
R <sub>th(j-a)</sub>	Thermal Resistance	$S = 1 \text{ cm}^2$			4	5	°C/W
	Junction-Ambient						

<sup>(1)</sup> Minimum  $I_{\mbox{\scriptsize GT}}$  is guaranted at 5% of  $I_{\mbox{\scriptsize GT}}$  max.

#### **Part Number Information**



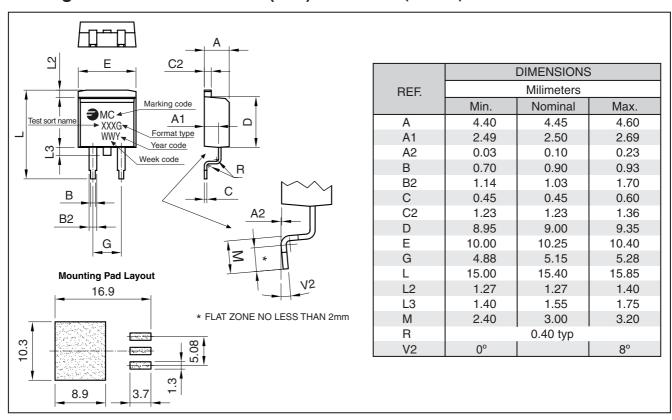
<sup>(2)</sup> For either polarity of electrode MT2 voltage with reference to electrode MT1.



## **Ordering information**

PREFERRED P/N	PACKAGE CODE	DELIVERY MODE	BASE QUANTITY	UNIT WEIGHT (g)
FT2514MG 00TR	TR	13" diameter tape and reel	800	1.50

# Package Outline Dimensions: (mm) TO-263AB (D2PAK)





## Ratings and Characteristics (Ta 25 °C unless otherwise noted)

Fig. 1: Maximum power dissipation versus RMS on-state current (full cycle)

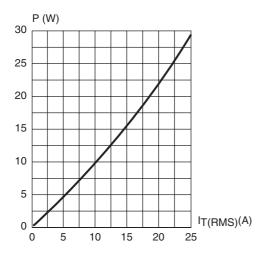


Fig. 3: Relative variation of thermal impedance versus pulse duration.

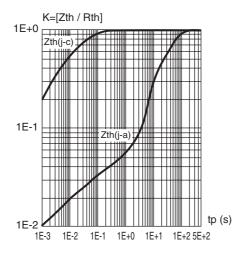


Fig. 5: Surge peak on-state current versus number of cycles

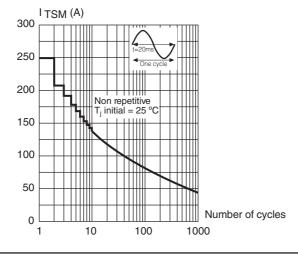


Fig. 2: RMS on-state current versus case temperature (full cycle).

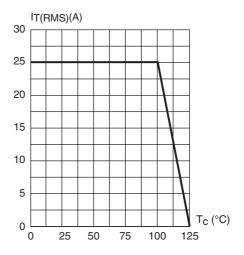


Fig. 4: On-state characteristics (maximum values)

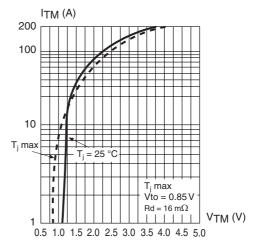
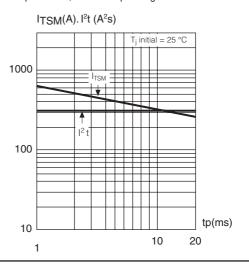


Fig. 6: Non repetitive surge peak on-state current for a sinusoidal pulse with width: tp < 20 ms, and corresponding value of l²t.





# Ratings and Characteristics (Ta 25 °C unless otherwise noted)

Fig. 7: Relative variation of gate trigger current, holding current and latching versus junction temperature (typical values)

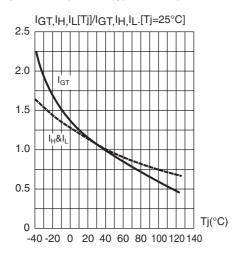
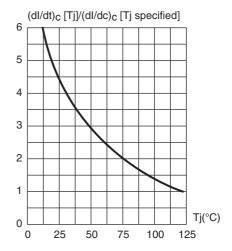


Fig. 8: Relative variation of critical rate of decrease of main current versus junction temperature





#### **Revision History**

Date	Revision	Description of Changes
14-Jun-2011	0	Original Data Sheet
4-May-2016	1	200V and 700V eliminated

## **Disclaimer**

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Revision: 1