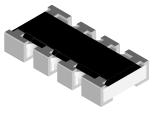
ACAS 0612 - Precision

Vishay Beyschlag

Precision Thin Film Chip Resistor Array



www.vishay.com

The ACAS 0612 thin film chip resistor arrays combine the proven reliability of precision thin film chip resistor products with the advantages of chip resistor arrays. Defined relative tolerance (matching) and relative TCR (tracking) make this product perfectly suited for applications with outstanding requirements towards stable fixed resistor ratios. A small package enables the design of high density circuits in combination with reduction of assembly costs. The ACAS is available with equal or different resistor values.

FEATURES

- Advanced thin film technology
- Two pairs or four equal resistor values
- Relative TCR down to ± 5 ppm/K (tracking)
- Relative tolerance down to ± 0.05 % (matching)
- Pure Sn termination on Ni barrier layer
- Material categorization: For definitions of compliance please see <u>www.vishay.com/doc?99912</u>

APPLICATIONS

- Precision analogue circuits
- Voltage divider
- Feedback circuits
- Signal conditioning

TECHNICAL SPECIFICATIONS	TECHNICAL SPECIFICATIONS						
DESCRIPTION	ACAS 0612						
EIA size	0612						
Metric size	RR1632M						
Configuration, isolated	4 x 0603						
Design:							
All equal	AE						
Two pairs	TP						
Resistance values	47 Ω to 221 k Ω ⁽¹⁾						
Absolute tolerance	± 0.1 %						
Relative tolerance	± 0.05 %						
Absolute temperature coefficient	± 25 ppm/K; ± 15 ppm/K; ± 10 ppm/K						
Relative temperature coefficient	± 15 ppm/K; ± 10 ppm/K; ± 5 ppm/K						
Max. resistance ratio R _{min.} /R _{max.}	1:5						
Rated dissipation: P ₇₀							
Element	0.1 W						
Package, 4 x 0603	0.3 W						
Operating voltage	75 V						
Operating temperature range	- 55 °C to 125 °C						
Permissible film temperature	125 °C ⁽²⁾						
Insulation voltage (<i>U</i> _{ins}) against ambient and between isolated resistors, continuous	75 V						

Notes

 The relative figures of tolerance, TCR and drift are related to a medial axis between the maximum and minimum permissable deviation of the resistor array. For detailed information please refer to the application note: Increasing Accuracy in Feedback Circuits and Voltage Dividers with Thin Film Chip Resistor Arrays (www.vishay.com/doc?28194).

⁽¹⁾ Resistance values to be selected from E24; E192.

(2) For higher max. film temperature and AEC-Q200 qualification please refer to data sheet ACAS 0606 AT, ACAS 0612 AT - Precision available on our web site at <u>www.vishay.com/doc?28770</u>.



COMPLIANT



Vishay Beyschlag

APPLICATION INFORMATION

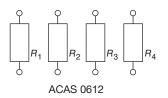
The power dissipation on the resistor generates a temperature rise against the local ambient, depending on the heat flow support of the printed-circuit board (thermal resistance). The rated dissipation applies only if the permitted film temperature is not exceeded. These resistors do not feature a limited lifetime when operated within the permissible limits.

MAXIMUM RESISTANCE CHANGE AT	RATED POWER (1)	
DESCRIPTION	AC4	AS 0612
Configuration, isolated	4 >	0603
Operation mode	Precision	Standard
Rated power per element, P70	0.032 W	0.1 W
Rated power per packaging, P70	0.1 W	0.3 W
Film temperature	85 °C	125 °C
Operating voltage, U _{max.} AC/DC	25 V	75 V
Max. resistance change at P_{70} $\Delta R/R$ max., after:		
1000 h	± 0.1 %	± 0.25 %
8000 h	± 0.25 %	± 0.5 %
Max. relative resistance change (relative drift) at P_{70} $\Delta R/R$ max., after:		
1000 h	± 0.05 %	± 0.125 %
8000 h	± 0.125 %	± 0.25 %

Note

⁽¹⁾ Figures are given for arrays with equal values, design type AE.

CIRCUITS

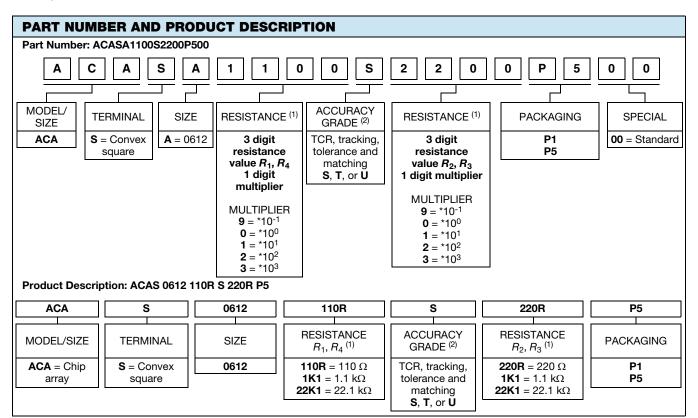


DESIGN					
	ACAS 0612				
AE	$R_1 = R_2 = R_3 = R_4$				
ТР	$R_1 = R_4 < R_2 = R_3$				



www.vishay.com

Vishay Beyschlag



Notes

Products can be ordered using either the PART NUMBER or the PRODUCT DESCRIPTION.

⁽¹⁾ $R_1 = R_4 \le R_2 = R_3$.

⁽²⁾ For historical temperature coefficient and resistance ranges please refer to the end of the data sheet.

TEMPERATURE COEFFICIENT AND RESISTANCE RANGE									
ТҮРЕ	ACCURACY	ABSO	LUTE	RELA	RESISTANCE				
ITFE	GRADE	TCR	TOLERANCE	TCR	TOLERANCE	VALUE			
	S	± 25 ppm/K	± 0.1 %	± 15 ppm/K	± 0.05 %	47 Ω to 221 k Ω			
ACAS 0612	Т	± 15 ppm/K	± 0.1 %	± 10 ppm/K	± 0.05 %	47 Ω to 150 k Ω			
	U	± 10 ppm/K	± 0.1 %	± 5 ppm/K	± 0.05 %	47 Ω to 100 k Ω			

Notes

• For historical temperature coefficent and resistance range please refer to the end of the data sheet.

• Relative TCR (tracking) down to ± 2.5 ppm/K on request.

• Relative tolerance for resistance values < 80 Ω on request.

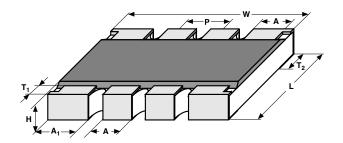


Vishay Beyschlag

DACKAGING

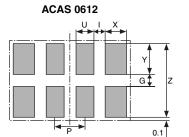
PACKAGING									
ТҮРЕ	CODE	QUANTITY	PACKAGING STYLE	WIDTH	РІТСН	REEL DIAMETER			
ACAS 0612	P1	1000	Tape and reel cardboard tape	9 mm	4 mm	180 mm/7"			
AUAS 0012	P5	5000	acc. IEC 60286-3 Type I	8 mm		180 mm/7"			

DIMENSIONS



DIMENSION AND MASS									
ТҮРЕ	L (mm)	W (mm)	H (mm)	P (mm)	A ₁ (mm)	A (mm)	T ₁ (mm)	T ₂ (mm)	MASS (mg)
ACAS 0612	1.5 ± 0.15	3.2 ± 0.15	0.45 ± 0.1	0.8 ± 0.1	0.6 ± 0.1	0.4 ± 0.1	0.3 ± 0.15	0.4 ± 0.15	6.6

PATTERN STYLES FOR CHIP RESISTOR ARRAY



Dimensions in mm

RECOMMENDED SOLDER PAD DIMENSIONS								
ТҮРЕ	G (mm)	Y (mm)	X (mm)	U (mm)	Z (mm)	l (mm)	P (mm)	
ACAS 0612	0.7	0.7	0.64	0.5	2.1	0.3	0.8	



ACAS 0612 - Precision

Vishay Beyschlag

DESCRIPTION

The production of the components is strictly controlled and follows an extensive set of instructions established for reproducibility. A homogeneous film of metal alloy is deposited on a high grade (96 % Al₂O₃) ceramic substrate using a mask to separate the adjacent resistors and conditioned to achieve the desired temperature coefficient. Specially designed inner contacts are realized on both sides. A special laser is used to achieve the target value by smoothly cutting a meander groove in the resistive layer without damaging the ceramics.

The resistor elements are covered by a protective coating designed for electrical, mechanical and climatic protection. The terminations receive a final pure tin on nickel plating.

The result of the determined production is verified by an extensive testing procedure and optical inspection performed on 100 % of the individual chip resistors. Only accepted products are laid directly into the paper tape in accordance with **IEC 60286-3** ⁽³⁾.

ASSEMBLY

The resistors are suitable for processing on automatic SMD assembly systems. They are suitable for automatic soldering using reflow or vapour phase as shown in **IEC 61760-1** ⁽³⁾. The encapsulation is resistant to all cleaning solvents commonly used in the electronics

industry, including alcohols, esters and aqueous solutions. The suitability of conformal coatings, if applied, shall be qualified by appropriate means to ensure the long-term stability of the whole system. The resistors are RoHS compliant; the pure tin plating provides compatibility with lead (Pb)-free and lead-containing soldering processes. The permitted storage time is 20 years, whereas the solderability is specified for 2 years after production or requalification. The immunity of the plating against tin whisker growth has been proven under extensive testing.

All products comply with the **GADSL**⁽¹⁾ and the **CEFIC-EECA-EICTA**⁽²⁾ list of legal restrictions on hazardous substances. This includes full compliance with the following directives:

- 2000/53/EC End of Life Vehicle Directive (ELV) and Annex II (ELV II)
- 2011/65/EC Restriction of the use of Hazardous Substances directive (RoHS)
- 2002/96/EC Waste Electrical and Electronic Equipment Directive (WEEE)

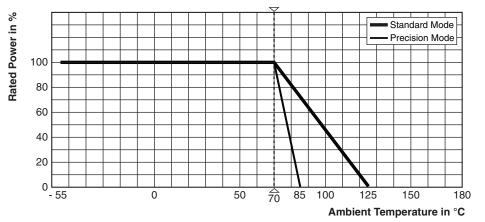
TESTS

Where applicable, the resistors are tested in accordance with **EN 140401-801** which refers to **EN 60115-1** and **EN 140400**.

Notes

- ⁽¹⁾ Global Automotive Declarable Substance List, see <u>www.gadsl.org</u>.
- (2) CEFIC (European Chemical Industry Council), EECA (European Electronic Component Manufacturers Association), EICTA (European trade organisation representing the information and communications technology and consumer electronics), see www.eicta.org → policy → environmental policy group → chemicals → jig → Joint Industry Guide (JIG-101 Ed 2.0).
- ⁽³⁾ The quoted IEC standards are also released as EN standards with the same number and identical contents.

FUNCTIONAL PERFORMANCE



For permissible resistance change please refer to table MAXIMUM RESISTANCE CHANGE AT RATED POWER

Derating

Revision: 12-Apr-13

ACAS 0612 - Precision



Vishay Beyschlag

TESTS AND REQUIREMENTS

Essentially all tests are carried out in accordance with the following specifications:

EN 60115-1, generic specification

EN 140400, sectional specification

EN 140401-801, detail specification

The testing also covers most of the requirements specified by EIA/IS-703 and JIS-C-5202.

The tests are carried out in accordance with **IEC 60068** ⁽¹⁾ and under standard atmospheric conditions according to **IEC 60068-1** ⁽¹⁾, 5.3. Climatic category LCT/UCT/56 (rated temperature range: Lower category temperature, upper

category temperature; damp heat, long term, 56 days) is valid (LCT = - 55 °C/UCT = 125 °C).

Unless otherwise specified the following values apply:

Temperature: 15 °C to 35 °C

Relative humidity: 45 % to 75 %

Air pressure: 86 kPa to 106 kPa (860 mbar to 1060 mbar)

The requirements stated in the "Test Procedures and Requirements" table are based on the required tests and permitted limits of EN 140401-801 where applicable.

TEST P	ROCEDUR	ES AND REQUIREM	IENTS	
EN 60115-1 CLAUSE	IEC 60068-2 ⁽¹⁾ TEST METHOD	TEST	PROCEDURE	REQUIREMENTS PERMISSIBLE CHANGE ⁽²⁾ (Δ <i>R</i>)
			Stability for product types:	
			ACAS 0612	47 Ω to 221 kΩ
4.5	-	Resistance -		± 0.1 % <i>R</i>
4.8.4.2	-	Temperature coefficient	At (20/-55/20) °C and (20/125/20) °C	± 25 ppm/K; ± 15 ppm/K; ± 10 ppm/K
			$U = \sqrt{P_{70} \times R} \text{ or } U = U_{\text{max.}};$ 1.5 h on; 0.5 h off; whichever is the less severe;	
	Endurance at 70 °C: Precision operation mode	1000 h: Absolute Relative	\pm (0.1 % R + 0.05 Ω) \pm (0.05 % R + 0.05 Ω)	
4.25.1	4 25 1		8000 h: Absolute Relative	\pm (0.25 % R + 0.05 Ω) \pm (0.125 % R + 0.05 Ω)
4.20.1			$U = \sqrt{P_{70} \times R} \text{ or } U = U_{\text{max.}};$ 1.5 h on; 0.5 h off; whichever is the less severe;	
		Endurance at 70 °C: Standard operation mode	1000 h: Absolute Relative	\pm (0.25 % R + 0.05 Ω) \pm (0.125 % R + 0.05 Ω)
			8000 h: Absolute Relative	\pm (0.5 % R + 0.05 Ω) \pm (0.25 % R + 0.05 Ω)
4.25.3	_	Endurance at upper	85 °C; 1000 h: Absolute Relative	\pm (0.1 % R + 0.05 Ω) \pm (0.05 % R + 0.05 Ω)
		category temperature	125 °C; 1000 h: Absolute Relative	\pm (0.25 % R + 0.05 Ω) \pm (0.125 % R + 0.05 Ω)
4.24	78 (Cab)	Damp heat, steady state	(40 ± 2) °C; 56 days; (93 ± 3) % RH	± (0.25 % <i>R</i> + 0.05 Ω)
4.13	-	Short time overload ⁽³⁾ Standard operation mode	$U = 2.5 \times \sqrt{P_{70} \times R}$ or $U = 2 \times U_{\text{max}}; 5 \text{ s}$	\pm (0.1 % R + 0.01 Ω) no visible damage
4.19	14 (Na)	Rapid change of temperature	30 min at - 55 °C and 30 min at 125 °C; 5 cycles	\pm (0.1 % R + 0.01 Ω) no visible damage

Revision: 12-Apr-13

Document Number: 28751

THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT www.vishay.com/doc?91000



Vishay Beyschlag

TEST P	TEST PROCEDURES AND REQUIREMENTS							
EN 60115-1 CLAUSE	15-1 60068-2 () TEST PROCEDURE		PROCEDURE	REQUIREMENTS PERMISSIBLE CHANGE ⁽²⁾ (ΔR)				
			Stability for product types:					
			ACAS 0612	47 Ω to 221 kΩ				
4.18.2	58 (Td)	Resistance to soldering heat	Reflow method 2 (IR/forced gas convection); (260 ± 5) °C; (10 ± 1) s	\pm (0.1 % R + 0.01 Ω) no visible damage				
4.47.0	50 (T.I)		Solder bath method; SnPb; non-activated flux accelerated ageing 4 h/155 °C (215 ± 3) °C; (3 ± 0.3) s	Good tinning (≥ 95 % covered);				
4.17.2	58 (Td)	Solderability	Solder bath method; SnAgCu; non-activated flux accelerated ageing 4 h/155 °C (235 ± 3) °C; (2 ± 0.2) s	no visible damage				
4.32	21 (Ue ₃)	Shear (adhesion)	45 N	No visible damage				
4.33	21 (Ue ₁)	Substrate bending	Depth 2 mm, 3 times	\pm (0.1 % R + 0.01 Ω) no visible damage; no open circuit in bent position				
4.7	-	Voltage proof	$U_{\text{RMS}} = U_{\text{ins}}$ (60 ± 5) s; against ambient, between adjacent resistors	No flashover or breakdown				

Notes

⁽¹⁾ The quoted IEC standards are also released as EN standards with the same number and identical contents.

⁽²⁾ Figures are given for arrays with equal values, design type AE.

⁽³⁾ For a single element.

HISTORICAL	ISTORICAL TEMPERATURE COEFFICIENT AND RESISTANCE RANGES									
	DESCRIPTION									
ACCURACY GRADE	ABSOLUTE TCR	TCR TRACKING	ABSOLUTE TOLERANCE	TOLERANCE MATCHING	ACAS 0612					
А	± 25 ppm/K	10 ppm/K	± 0.25 %	0.1 %	47 Ω to 221 k Ω					
В	± 25 ppm/K	10 ppm/K	± 0.5 %	0.25 %	47 Ω to 221 k Ω					
E	± 25 ppm/K	15 ppm/K	± 0.25 %	0.1 %	47 Ω to 221 kΩ					
F	± 25 ppm/K	15 ppm/K	± 0.5 %	0.25 %	47 Ω to 221 k Ω					
J	± 25 ppm/K	25 ppm/K	± 0.25 %	0.1 %	47 Ω to 221 k Ω					
К	± 25 ppm/K	25 ppm/K	± 0.5 %	0.25 %	47 Ω to 221 kΩ					
Ν	± 50 ppm/K	25 ppm/K	± 0.5 %	0.5 %	47 Ω to 221 k Ω					
Р	± 50 ppm/K	50 ppm/K	± 0.5 %	0.5 %	47 Ω to 221 k Ω					

Note

Special temperature coefficent and resistance combinations remain available. For optimized availability please refer to the table TEMPERATURE COEFFICENT AND RESISTANCE.



Vishay

Disclaimer

ALL PRODUCT, PRODUCT SPECIFICATIONS AND DATA ARE SUBJECT TO CHANGE WITHOUT NOTICE TO IMPROVE RELIABILITY, FUNCTION OR DESIGN OR OTHERWISE.

Vishay Intertechnology, Inc., its affiliates, agents, and employees, and all persons acting on its or their behalf (collectively, "Vishay"), disclaim any and all liability for any errors, inaccuracies or incompleteness contained in any datasheet or in any other disclosure relating to any product.

Vishay makes no warranty, representation or guarantee regarding the suitability of the products for any particular purpose or the continuing production of any product. To the maximum extent permitted by applicable law, Vishay disclaims (i) any and all liability arising out of the application or use of any product, (ii) any and all liability, including without limitation special, consequential or incidental damages, and (iii) any and all implied warranties, including warranties of fitness for particular purpose, non-infringement and merchantability.

Statements regarding the suitability of products for certain types of applications are based on Vishay's knowledge of typical requirements that are often placed on Vishay products in generic applications. Such statements are not binding statements about the suitability of products for a particular application. It is the customer's responsibility to validate that a particular product with the properties described in the product specification is suitable for use in a particular application. Parameters provided in datasheets and/or specifications may vary in different applications and performance may vary over time. All operating parameters, including typical parameters, must be validated for each customer application by the customer's technical experts. Product specifications do not expand or otherwise modify Vishay's terms and conditions of purchase, including but not limited to the warranty expressed therein.

Except as expressly indicated in writing, Vishay products are not designed for use in medical, life-saving, or life-sustaining applications or for any other application in which the failure of the Vishay product could result in personal injury or death. Customers using or selling Vishay products not expressly indicated for use in such applications do so at their own risk. Please contact authorized Vishay personnel to obtain written terms and conditions regarding products designed for such applications.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document or by any conduct of Vishay. Product names and markings noted herein may be trademarks of their respective owners.

Material Category Policy

Vishay Intertechnology, Inc. hereby certifies that all its products that are identified as RoHS-Compliant fulfill the definitions and restrictions defined under Directive 2011/65/EU of The European Parliament and of the Council of June 8, 2011 on the restriction of the use of certain hazardous substances in electrical and electronic equipment (EEE) - recast, unless otherwise specified as non-compliant.

Please note that some Vishay documentation may still make reference to RoHS Directive 2002/95/EC. We confirm that all the products identified as being compliant to Directive 2002/95/EC conform to Directive 2011/65/EU.

Vishay Intertechnology, Inc. hereby certifies that all its products that are identified as Halogen-Free follow Halogen-Free requirements as per JEDEC JS709A standards. Please note that some Vishay documentation may still make reference to the IEC 61249-2-21 definition. We confirm that all the products identified as being compliant to IEC 61249-2-21 conform to JEDEC JS709A standards.