

TBLK1 – single or multi strand wire 16-26AWG

PL4, PL5, PL6, PL7, *PL8 & PL11 are 0.64mm square posts on 2.54mm pitch. There are various means of connection including Ribbon Cable, Discrete Wire and via add-on boards (future development).

PL4 – is designed for ribbon cable (IDC) or non-polarised crimped wire connections.

PL5 & PL6 – both are designed to be connected via add-on boards. However, it would be possible to use non-polarised crimped wire connections.

PL7 – is designed for use with a polarised crimped wire connection.

*PL8 – is used to configure the SGD 43-A for 4-20mA applications. Pin pairs (1&2, 3&4, 5&6, 7&8) are linked as required using a jumper shunt.

PL11 – is designed for ribbon cable (IDC) or non-polarised crimped wire connection.

Suggested connector part numbers are provided in the table below.

PL4 Crimp	TE 1-104482-5 (Shell) + TE 1-104481-2 (crimps x 34) or equiv.
PL4 Ribbon Cable	FCI 71600-034LF or equiv.
PL5 Crimp	TE 104482-9 (Shell) + TE 1-104481-2 (crimps x 20) or equiv.
PL6 Crimp	TE 1-104482-0 (Shell) + TE 1-104481-2 (crimps x 22) or equiv.
PL7 Crimp	TE 281838-5 (Shell) + TE 182734-2 (crimps x 5) or equiv.
*PL8 Jumper	Harwin M7567-05 or equiv.
PL11 Crimp	TE 104482-3 (Shell) + TE 1-104481-2 (crimps x 8) or equiv.
PL11 Ribbon Cable	FCI 71600-008LF or equiv.

Pin	Function
1	Input Voltage (VIN+)
2	0V
3	Alarm O/P 1 (open collector)
4	Alarm O/P 2 (open collector)
5	I2C0 SCL
6	I2C0 SDA
7	SPI SS1
8	SPI MOSI
9	SPI MISO
10	SPI SCK
11	Digital I/O 0
12	Digital I/O 1
13	Digital I/O 2
14	Digital I/O 3
15	Digital I/O 4
16	Digital I/O 5
17	Digital I/O 6
18	Digital I/O 7
19	Digital I/O 8 (PWM 0)
20	Digital I/O 9 (PWM 1)
21	Digital I/O 10 (PWM 2)
22	Digital I/O 11 (PWM 3)
23	DUART TX
24	DUART RX
25	USB D+
26	USB D-
27	I2C1 SDA
28	I2C1 SCL
29	+5V
30	High Speed ADC
31	+3V3
32	0V
33	Firmware Upgrade Enable
34	Firmware Upgrade Enable

PL4 - USER INTERFACE

Pin	Function
1	TX
2	RTS
3	RX
4	CTS
5	0V

PL7 - RS 232

Pin	Function
1	ENT CLK
2	ENT MDC
3	ENT MDIO
4	ENT RXD0
5	ENT RXD1
6	ENT RX EN
7	ENT TXD0
8	ENT TXD1
9	ENT TX EN
10	Digital I/O 12 (Expansion)
11	Digital I/O 13 (Expansion)
12	Digital I/O 14 (Expansion)
13	Digital I/O 15 (Expansion)
14	Digital I/O 16 (Expansion)
15	Digital I/O 17 (Expansion)
16	Digital I/O 18 (Expansion)
17	Digital I/O 19 (Expansion)
18	Digital I/O 20 (Expansion)
19	Digital I/O 21 (Expansion)
20	Digital I/O 22 (Expansion)
21	External Module Hardware ID
22	External Module Hardware ID

PL6 - ETHERNET & EXPANSION IO

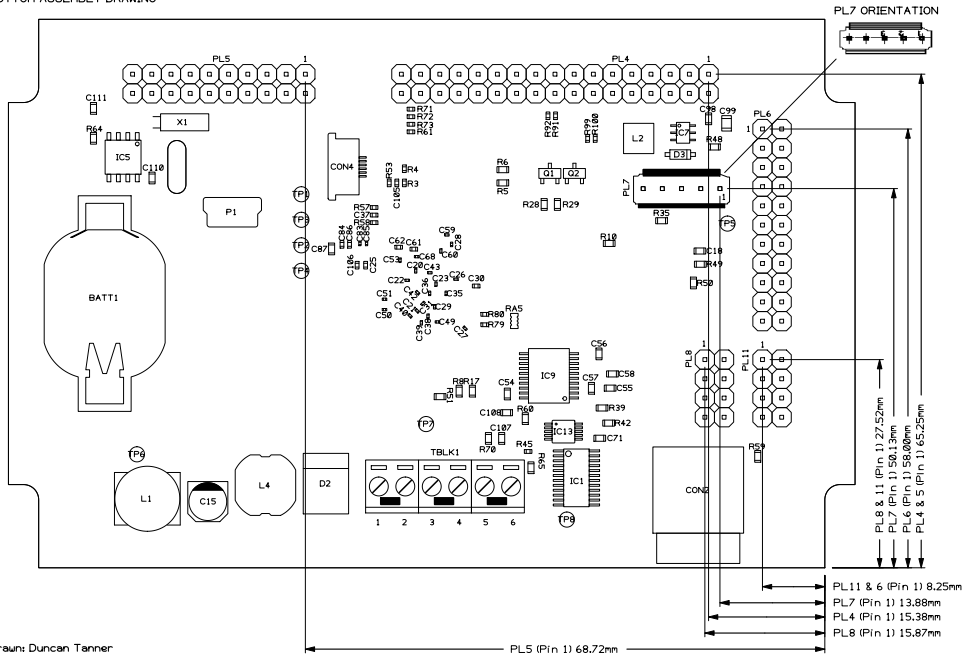
Pin	Function
1-2	Short pins for 4-20mA current loop measurement (IN1)
3-4	Short pins for 4-20mA current loop measurement (IN2)
5-6	Short pins for 4-20mA current loop measurement (IN3)
7-8	Short pins for 4-20mA current loop measurement (IN4)

PL8
4-20mA
HEADER

Pin	Function
1	Analogue Input 1
2	0V
3	Analogue Input 2
4	0V
5	Analogue Input 3
6	0V
7	Analogue Input 4
8	0V

PL11
ANALOGUE INPUT

PCB SGD 43-A Issue 7
 BOTTOM ASSEMBLY DRAWING



Drawn: Duncan Tanner
 Checked: Leo Lam
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