

Product Specification Information - VCXO

Available options

Type	package	Supply Voltage(V)	Tri-State Function	Freq.Stability/ Pulling Range (ppm)	Temp. Range(°C)	Output Logic and Symmetry	Oscillator Mode	Appearance	Lead Free	Dash	Freq.(MHz)
V: VCXO	T: 7.0x5.0 (6 pad) W: 5.0x3.2	C: 5 E: 3.3 J: 2.5 K: 1.8	U: Relative Pulling (Refer to Center Voltage) with Tri-State to pin 2 M: Multiplier Frequency with Tri-State to pin 2 S: Enable Low	M: $\pm 25/\pm 100$ P: $\pm 50/\pm 100$	I: -10~+60 C: -20~+70 L: -40~+85	L: PECL/50 \pm 5% V: LVDS/50 \pm 5%	-A: AT Fundamental NOT SELECTABLE BY CUSTOMER	N :Normal	F: RoHs Compliant		
	C: 7.5x5.0 (4 pad)	C: 5 E: 3.3	U: Enable High M: Multiplier Frequency	M: $\pm 25/\pm 100$ P: $\pm 50/\pm 100$	I: -10~+60 C: -20~+70 L: -40~+85	50 \pm 5% J CMOS 15 pF F CMOS 50 pF					
	H: 12.8x12.8 (Dip) F: 20.4x12.8 (Dip)	Through Hole T: 5.0 E: 3.3 Gull Wing G: 5.0 F: 3.3		M: $\pm 25/\pm 15/\pm 100$ T: $\pm 25/\pm 15/\pm 150$ G: $\pm 35/\pm 20/\pm 100$ P: $\pm 50/\pm 20/\pm 100$ R: $\pm 50/\pm 20/\pm 150$	C: -20~+70 D: -30~+80 L: -40~+85	50 \pm 5% A 10 TTL 15pF J CMOS 15pF F CMOS 50 pF					

V T E S P C L A N F - 155.52

*Not all combinations of options are available.

Example: VTESPCLANF-155.52

Type	VCXO
Package	7.0 x 5.0 mm
Supply Voltage(V)	3.3 V
Tri-State.	Enable Low
Freq. Stability ± 50 ppm	Pulling Range ± 100 ppm
Temp Range	-20~+70 °C
PECL Output	Symmetry 50 \pm 5%
Oscillator Mode	AT Fundamental
Appearance	Normal Appearance
Lead Free	RoHs Compliant
Frequency	155.52 MHz