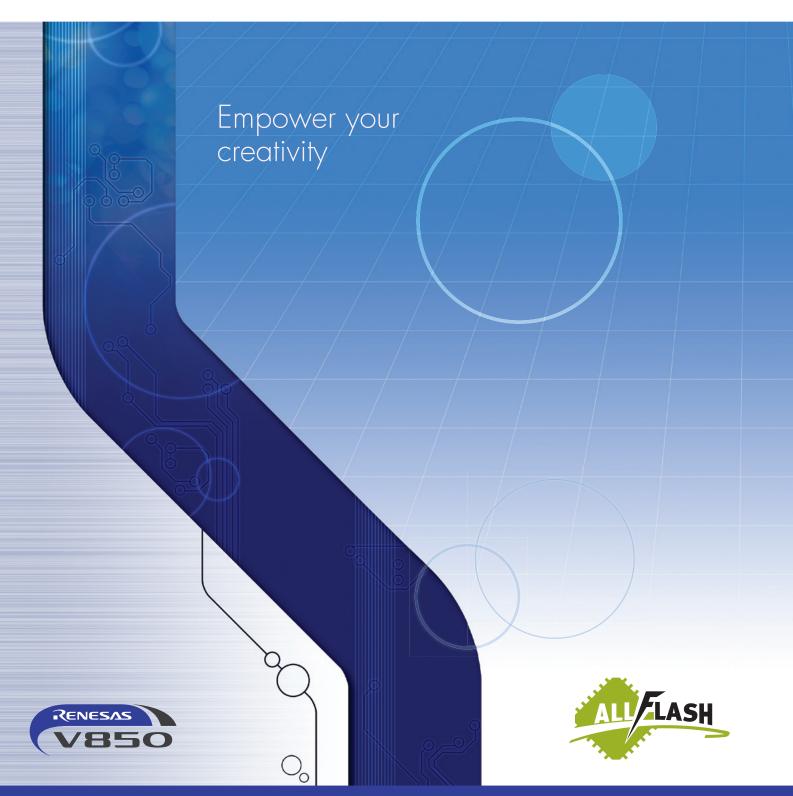


# Renesas Microcomputer **32-bit All Flash** V850E, V850ES Microcontrollers







### Shifting to All Flash is bringing about a manufacturing revolution.

Shifting to All Flash means switching the entire lineup of microcontrollers offered by Renesas Electronics to flash memory products.

All Flash contributes to improved development efficiency, improved quality, and improved system competitiveness due to reduced costs.

The V850 All Flash lineup includes the V850E2/MN4<sup>Note 1</sup> which boasts a world-beating performance of 1024 MIPS when operating at 200 MHz<sup>Note 2</sup>.

The lineup also includes the V850ES/Jx3-L, which consumes even less power than a 16-bit microcontroller; the V850ES/Jx3, which features a large-capacity ROM for future peace of mind; the V850ES/Jx3-U with an on-chip USB host controller; the V850E/Ix4 and V850E/Ix4-H, which include an inverter controller; and the V850ES/Jx3-E, which supports Ethernet.

User-friendly development environments that only users of All Flash microcontrollers can experience are also provided.

By offering a lineup of products and environment resources that allow full use of the merits of flash memory products, namely stock reduction, fast delivery, and flexible adaptation to fluctuations in demand, Renesas Electronics is helping to bring about a manufacturing revolution for customers.

Note 1. Figures apply to the  $\mu$ PD70F3514 and 70F3515 with on-chip dual-core CPUs. 2. Dhrystone 2.1

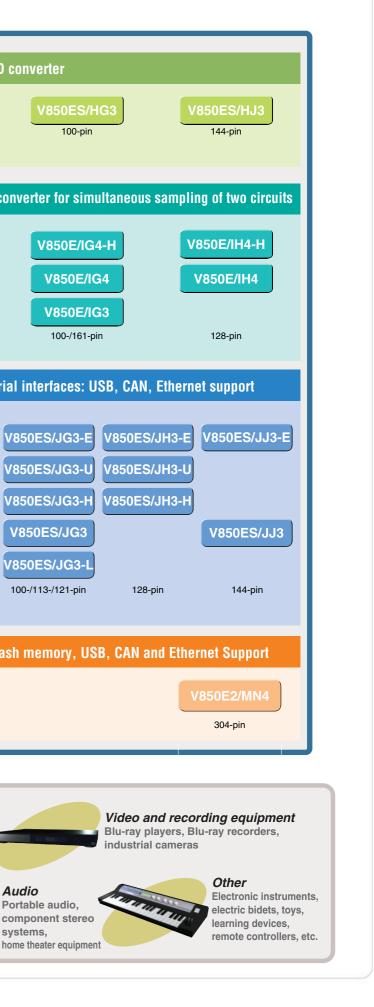
### **Application examples**

All Flash microcontrollers are suitable for systems that use 16-bit microcontrollers and 32-bit microcontrollers, and add value to these systems.



s h	5 V operation & on-chip multi-channel A/D co
AII Flash	V850ES/HE3 64-pin 80-pin
۷	5 V inverter control support & on-chip A/D conv
	V850ES/IE2         V850E/IF3           64-pin         80-pin
	3 V operation & on-chip multi-channel serial
	V850ES/JE3-E V850ES/JF3-E V8
	V850ES/JC3-H V850ES/JE3-H V8
	V850ES/JC3-L         V850ES/JE3-L         V850ES/JF3-L         V8           40-/48-pin         64-pin         80-pin         10
	M High-performance CPU, large-capacity flash





**Providing flash microcontrollers is one way** that Renesas Electronics contributes to reforming the consumer supply chain.



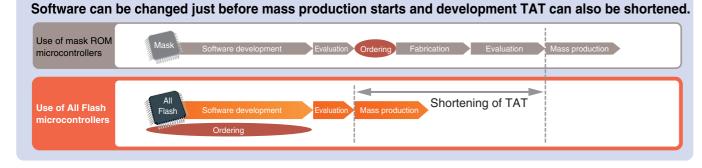
### Flash microcontrollers offer overwhelming advantages.

Compared to mask ROM microcontrollers, flash microcontrollers definitely contribute to speeding up system development. Microcontrollers can be ordered before program completion and programs can be written even after the microcontroller has been mounted on the board. Microcontroller order placement and program development can therefore be done concurrently, allowing TAT to be shortened as a result.

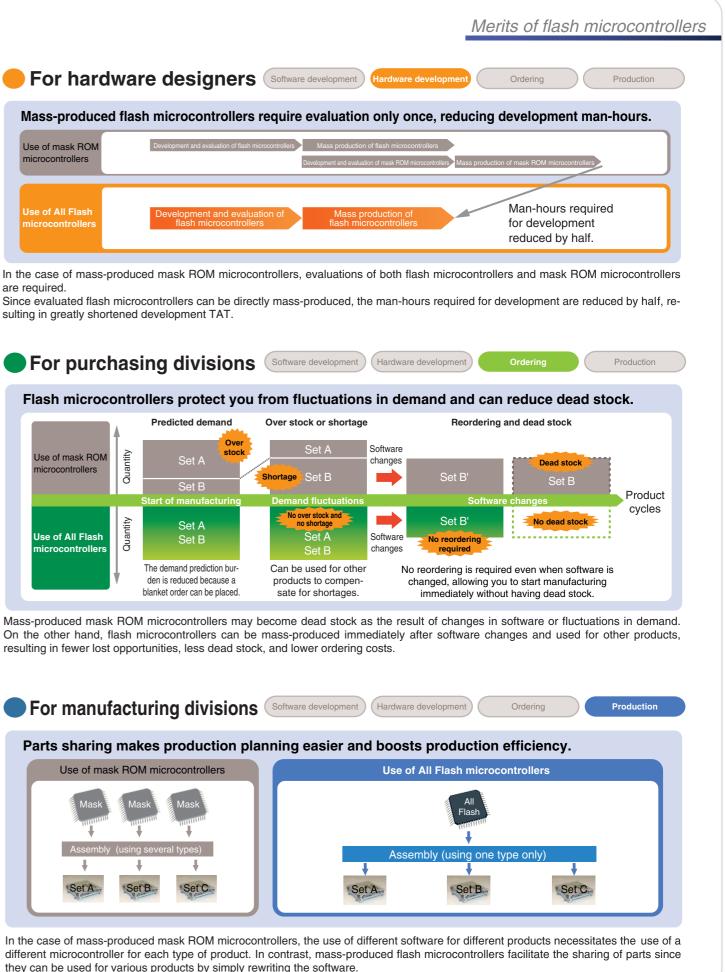
In addition, when flash microcontrollers are used for products with many different versions or that are localized for specific regions, the cost of ordering mask ROM microcontrollers is eliminated and purchase and stock management costs can be slashed.

### For software designers Software development

(Hardware development) Ordering Production

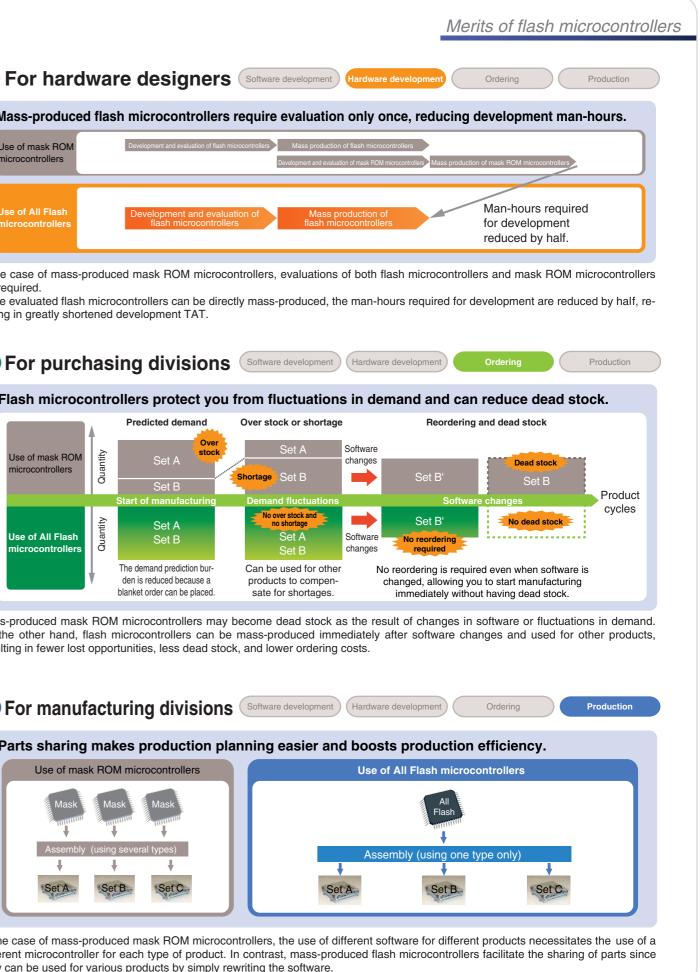


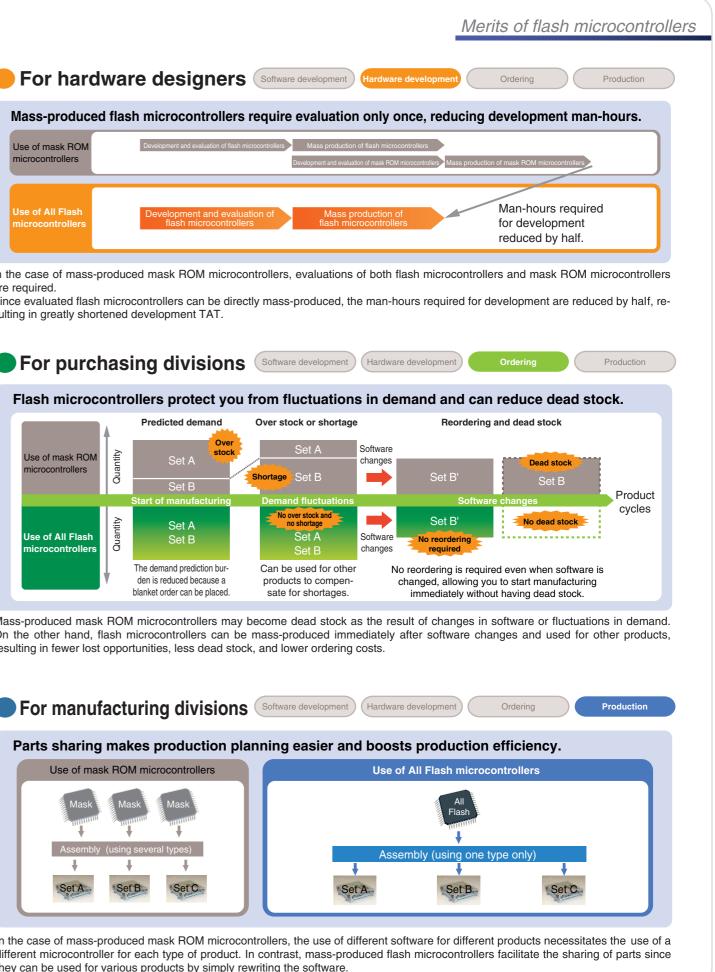
Since mask ROM microcontrollers cannot be ordered until their specifications are finalized, last-minute software changes can be problematic. On the other hand, specifications for flash microcontrollers can be changed just prior to the start of mass production. Thus orders for flash microcontrollers can be placed while the software is still being developed, allowing the development TAT to be shortened.

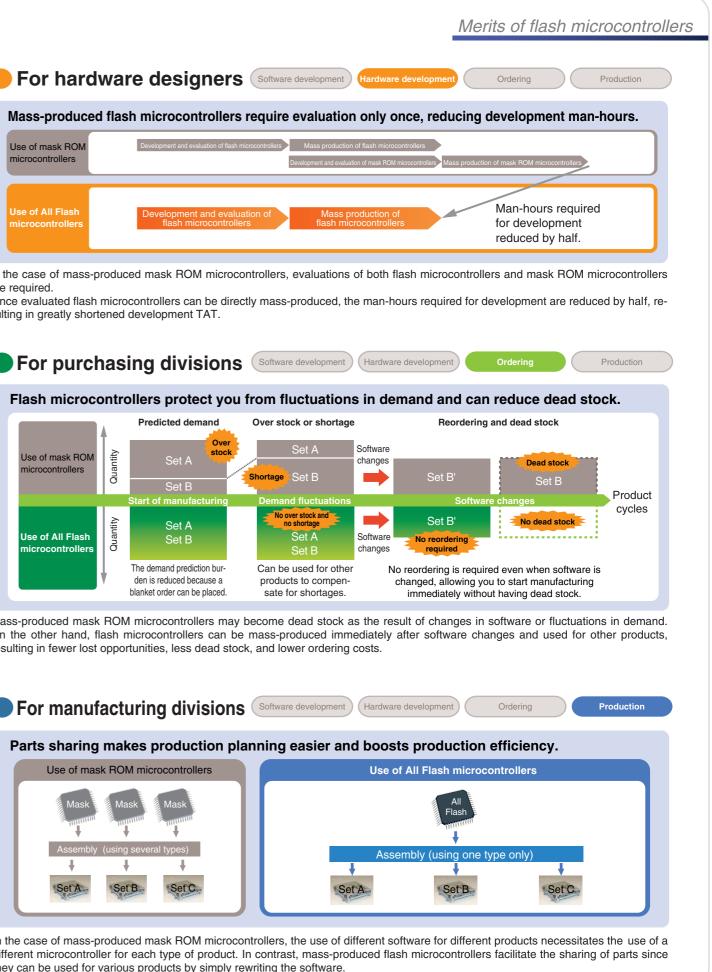


are required.

sulting in greatly shortened development TAT.







they can be used for various products by simply rewriting the software.

# **Worried about taking** the first step? V850 All Flash puts you at ease.

### Use new products with confidence

For applications with large-scale programming and complicated systems, the development burden is heavy, so anxiety is even greater when it comes to using a new product. V850 All Flash eliminates the anxiety of using a new prod-

uct, because Renesas Electronics reviews not only products themselves, but all other related aspects, including development, production, and distribution.

8 K 48 K

32 K

60 K

60 K

24 K 16 K 12 K 64 K 16 K 24 K 64 K 64 K 66 K 24 K 76 K 16 K 12 K

8K 48K 48K 8K

32 K 32 K

24 K 56 K

64Kx2

60 K

60 K

40 K 24 K 76 K 32 K 40 K

V850ES/Jx3-E

(factory automation equipment,

building management systems)

24 K 40 K 40 K 40 K 22 K 24 K 76 K 82 K

## Large selection

### 149 models available

A total of 149 32-bit flash microcontroller models are available to meet all your needs.

With memories of from 16 to 2048 KB, and packages of 40 to 304 pins, the ideal product for your application can be selected from our extensive product lineup, according to the required voltage range or processing performance.

The 40-pin package is a thin and small WQFN just 6 mm x 6 mm in size and 0.75 mm thick. This package is approximately 46% thinner and 82% smaller than our conventional 32-bit microcontroller package (which is a 100-pin LQFP with a size of 14 mm x 14 mm), helping you reduce the size of your set.

## **Specialized lineup**

Products ideally suited to specific applications

Renesas Electronics provides a range of products ideally suited to developing specific applications. These products include the V850ES/Jx3-H and V850ES/Jx3-U, which feature USB 2.0 communication capability and require only the internal ROM and RAM to develop applications requiring USB connectivity; the V850ES/Jx3-E, which includes an on-chip Ethernet controller enabling remote and monitoring control; and the V850E/Ix4 and V850E/Ix4-H, which feature a faster CPU-100 MHz-and fine-grained inverter control.

#### V850ES/Jx3-H, V850ES/Jx3-U (portable POS terminals printers scanners)

BOM size (bytes) 2048 K

1024 K

768 K

512 K/ 480 K

384 K/ 376 K

256 K 24 K 24 K 16 K 16 K

64 K 24 K 24 K

32 K 16 K 16 K

16 K 8K 8K

40 48 -pin -pin

128 K 24 K 24 K 8 K 6 K 24 K

6 K 32 K 8 K

16 K 8 K

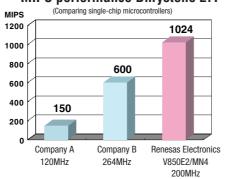
8 K



V850E/Ix4, V850E/Ix4-H, V850E/Ix3 (inverter air conditioners)



### MIPS performance Dhrvstone 2.1



## **High reliability**

### Software protection based on experience, technology and strong security features

plving more than 2 billion microcontroller units (as of May, 2010) for more than 1,000 types of applications, and the technology we have developed for flash microcontrollers for the automotive field. Our products also provide features that disable reading and malicious software rewriting and erasing, thus offering maximum protection of your valuable software.

## Supportive environment for development

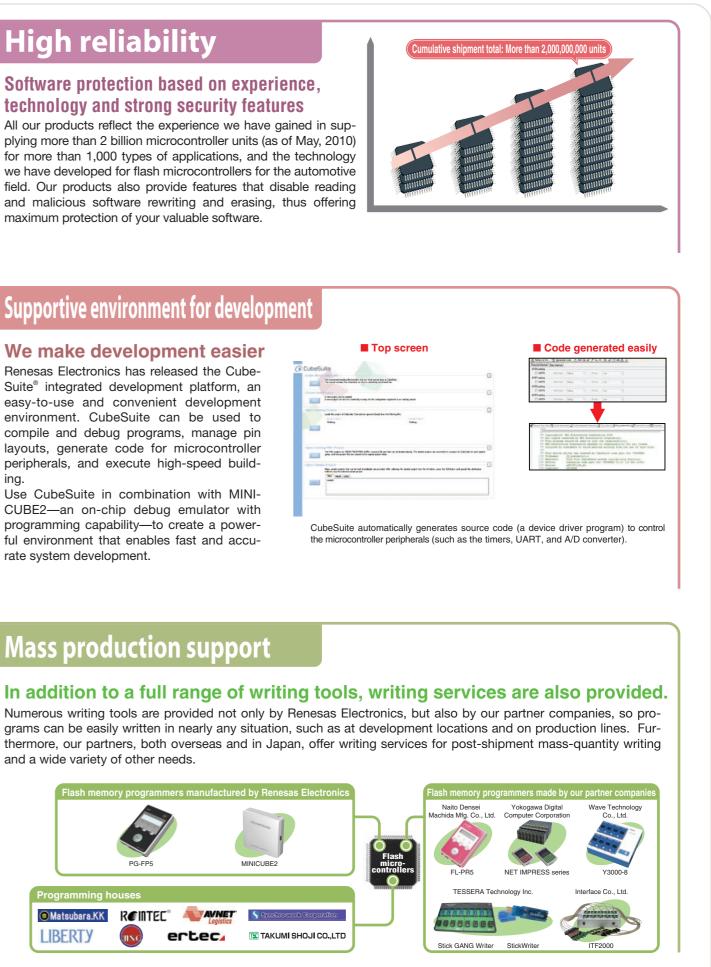
### We make development easier

Renesas Electronics has released the Cube-Suite® integrated development platform, an easy-to-use and convenient development environment. CubeSuite can be used to compile and debug programs, manage pin layouts, generate code for microcontroller peripherals, and execute high-speed building.

Use CubeSuite in combination with MINI-CUBE2-an on-chip debug emulator with programming capability-to create a powerful environment that enables fast and accurate system development.

## Mass production support

and a wide variety of other needs.



**High performance** 

### All Flash microcontroller with dual-core 32-bit CPU boasting world-beating performance (V850E2/MN4)

We have now developed the V850E2M, an ultra-fast CPU core based on the high-performance V850E2 (with 2-way parallel superscalar architecture and a 7-stage pipeline) but featuring faster 600 conditional branch instruction execution and a new variable-step division instruction to enable highspeed division calculations. The V850E2M realizes an outstanding performance of 2.56 MIPS/MHzclose to 1.4 times faster than our other cores when operating at the same frequency The V850E2/MN4 lineup features products that incorporate two of these ultra-fast CPU cores to en-

able a world-beating performance of 1024 MIPS when operating at 200 MHz. Our dual-core microcontrollers also consume only 0.88 mW/MIPS-60% of the power of single-core products-and provide a wide range of on-chip peripherals, letting you build smaller systems with fewer components.

1

We offer fleep microsontrollers in various peakages and POM and PAM size

Generic Name	V850ES/HE3	V850ES/HF3	V850ES/HG3	V850ES/HJ3	V850ES/IE2	V850E/IF3	V850E/IG3	V850E/IG4	V850E/IH4	V850E/IG4-H	V850E/IH4-H
Pin Count I (Bytes)	64-pin	80-pin	100-pin	144-pin	64-pin	80-pin	100/161-pin	100-pin	128-pin	100-pin	128-pin
2048 K	Part Number (RAM: Bytes)										
1024 K							V	850ES/Ix2	2 and V85	0E/Ixx	
768 K	V	/850ES	/Hx3				Microc	ontroller	s for inve	rter contr	ol
512 K/ 480 K	Mic	rocon	trollers	μ <b>PD70F3757</b> (32 K)				μ <b>PD70F3915</b> (24 K)	μ <b>PD70F3918</b> (24 K)	μ <b>PD70F3921</b> (24 K)	μ <b>ΡD70F3924</b> (24 K)
384 K/ 376 K								μ <b>PD70F3914</b> (24 K)	μ <b>PD70F3917</b> (24 K)	μ <b>PD70F3920</b> (24 K)	μ <b>ΡD70F3923</b> (24 K)
256 K		μ <b>PD70F3750</b> (16 K)	μ <b>PD70F3752</b> (16 K)	μ <b>ΡD70F3755</b> (16 K)		μ <b>PD70F3452</b> (12 K)	μ <b>ΡD70F3454</b> (12 K)	μ <b>PD70F3913</b> (24 K)	μ <b>PD70F3916</b> (24 K)	μ <b>PD70F3919</b> (24 K)	μ <b>ΡD70F3922</b> (24 K)
128 K	μ <b>PD70F3747</b> (8 K)				μ <b>ΡD70F3714</b> (6 K)	μ <b>ΡD70F3451</b> (8 K)	μ <b>ΡD70F3453</b> (8 K)				
64 K					μ <b>ΡD70F3713</b> (6 K)						
32 K											
16 K											
Package	64-pin LQFP GB Thickness: 1.4 mm 10 x 10 mm Pitch: 0.5 mm	80-pin LQFP GK Thickness:1.4 mm 12 x 12 mm Pitch: 0.5 mm	100-pin LQFP GC Thickness: 1.4 mm 14 x 14 mm Pitch: 0.5 mm	144-pin LQFP         GJ         Dickness: 1.4 mm         Dickness: 1.4 mm         Pitch: 0.5 mm	64-pin LOFP GC Thickness: 1.4 mm Pitch: 0.8 mm	80-pin LQFP GC Intickness: 1.4 mm Pitch: 0.65 mm	100-pin LQFP GC Thickness: 1.4 mm Pitch: 0.5 mm 100-pin LQFP GF Thickness: 1.4 mm 14 x 20 mm Pitch: 0.65 mm 161-pin FBGA <sup>Note</sup> F1 Thickness: 1.13 mm 10 x 10 mm Pitch: 0.65 mm	100-pin LQFP GC Thickness: 1.4 mm Pitch: 0.5 mm Intervention Intervent	128-pin LQFP         GF         Thickness: 1.4 mm         14 x 20 mm         Pitch: 0.5 mm	100-pin LQFP GC Thickness: 1.4 mm Pitch: 0.5 mm	128-pin LQFP GF Thickness: 1.4 mm Jitch: 0.5 mm

We offer flash microcontrollers in various packages and ROM and RAM sizes, allowing you to select the best flash microcontroller for your product or application.

Number Λ: Bytes) V850 Microc DF3838 <sup>Note 1</sup> 16 K)	48-pin ES/Jx3- controllo		80-pin	100/121-pin μPD70F3793, μPD70F3796 <sup>Note 1</sup> (40 K) μPD70F3792 (32 K), μPD70F3795 <sup>Note 1</sup> (40 K)	100-pin μPD70F3742 (60 K) μPD70F3741 (60 K) μPD70F3740 (40 K) μPD70F3739 (32 K)	μΡD7( (60 μΡD7( (60 μΡD7( (40 μΡD7( (32
A: Bytes) <b>V850</b> <b>Microc</b> DF3838 <sup>Note 1</sup> 16 K) DF3800 <sup>Note 1</sup> μPE	Controll 070F3839 <sup>Note 1</sup> (16 K)	<b>ETS</b> μPD70F3840 <sup>Note 1</sup>	11DD7052726	μΡD70F3796 <sup>Note 1</sup> (40 K) μ <b>PD70F3792 (32 K),</b>	(60 K) μPD70F3741 (60 K) μPD70F3740 (40 K) μPD70F3739	(60 μPD7( (60 μPD7( (40 μPD7(
<b>Μicroc</b> DF3838 <sup>Note 1</sup> μPE 16 K) DF3800 <sup>Note 1</sup> μPE	Controll 070F3839 <sup>Note 1</sup> (16 K)	<b>ETS</b> μPD70F3840 <sup>Note 1</sup>	11DD7052726	μΡD70F3796 <sup>Note 1</sup> (40 K) μ <b>PD70F3792 (32 K),</b>	(60 K) μPD70F3741 (60 K) μPD70F3740 (40 K) μPD70F3739	(60 μPD7( (60 μPD7( (40 μPD7(
<b>Μicroc</b> DF3838 <sup>Note 1</sup> μPE 16 K) DF3800 <sup>Note 1</sup> μPE	Controll 070F3839 <sup>Note 1</sup> (16 K)	<b>ETS</b> μPD70F3840 <sup>Note 1</sup>	DD7052726	μΡD70F3796 <sup>Note 1</sup> (40 K) μ <b>PD70F3792 (32 K),</b>	(60 K) μΡD70F3740 (40 K) μΡD70F3739	(60 μΡD7( (40 μΡD7(
<b>Μicroc</b> DF3838 <sup>Note 1</sup> μPE 16 K) DF3800 <sup>Note 1</sup> μPE	Controll 070F3839 <sup>Note 1</sup> (16 K)	<b>ETS</b> μPD70F3840 <sup>Note 1</sup>	DD7052726	μΡD70F3796 <sup>Note 1</sup> (40 K) μ <b>PD70F3792 (32 K),</b>	(40 K) μ <b>PD70F3739</b>	μ <b>ΡD7(</b>
DF3838 <sup>Note 1</sup> μPD 16 K) DF3800 <sup>Note 1</sup> μPD	D70F3839 <sup>Note 1</sup> (16 K)	μ <b>ΡD70F3840<sup>№ote 1</sup></b>	UDD7052726			•
16 K) DF3800 <sup>Note 1</sup> μPD	(16 K)	•	UDD70E9796			(0-
•			μ <b>ΡD70F3736</b> (16 K)	μ <b>PD70F3738 (16 K),</b> μ <b>PD70F3794<sup>Note 1</sup> (40 K)</b>	V850E	S/Jx3
	D70F3804 <sup>Note 1</sup> (8 K)	μ <b>PD70F3808</b> <sup>Note 1</sup> (8 K)	μ <b>PD70F3735</b> (8 K)	μ <b>ΡD70F3737</b> (8 K)	Microco	ontroll
DF3799 <sup>Note 1</sup> µPD (8 K)	070F3803 <sup>Note 1</sup> (8 K)	μ <b>PD70F3807</b> <sup>Note 1</sup> (8 K)				
0F3798 <sup>Νοτε 1</sup> μΡΕ (8 K)	070F3802 <sup>Note 1</sup> (8 К)	μ <b>ΡD70F3806</b> <sup>Note 1</sup> (8 K)				
0F3797 <sup>Note 1</sup> μΡΕ (8 K)	070F3801 <sup>Note 1</sup> (8 K)	μ <b>ΡD70F3805</b> <sup>Note 1</sup> (8 K)				
K8 ess: 0.75 mm Thic x 6 mm h: 0.5 mm P	GA ckness: 1.4 mm 7 x 7 mm Pitch: 0.5 mm	64-pin LQFP GB Thickness: 1.4 mm 10 x 10 mm Pitch: 0.5 mm International Content of the second	80-pin LOFP GK Thickness: 1.4 mm 12 x 12 mm Pitch: 0.5 mm 80-pin LOFP GC Thickness: 1.4 mm 14 x 14 mm Pitch: 0.65 mm	100-pin LQFP GC Thickness:1.4 mm 14 x 14 mm Pitch: 0.5 mm IOO-pin LQFP <sup>Note 2</sup> GF Thickness: 1.4 mm 14 x 20 mm Pitch: 0.65 mm Pitch: 0.65 mm III-pin FBGA	fuo-pin LQFP GC Thickness: 1.4 mm Pitch: 0.5 mm	144-pin G Thicknes 20 x 2 Pitch:
	(8 K)       μPI         (9 K)       μPI         (9 K)       μPI         (10 K)       μPI	(8 K)       (8 K)         (9 K)       (8 K)         (10 WQFN       (10 K)         (10 K)       (10 K)         (10	(8 K)     (8 K)     (8 K)     (8 K)       0F3797 <sup>Note 1</sup> (8 K)     μPD70F3801 <sup>Note 1</sup> (8 K)     μPD70F3805 <sup>Note 1</sup> (8 K)       win WQFN K8 pss: 0.75 mm h: 0.5 mm     48-pin LQFP GA Thickness: 1.4 mm 7 x 7 mm Pitch: 0.5 mm     64-pin LQFP GB Thickness: 1.4 mm Pitch: 0.5 mm       48-pin WQFN K8 Thickness: 0.75 mm 7 x 7 mm Pitch: 0.5 mm     64-pin FBGA F1 Thickness: 0.91 mm 5 x 5 mm       48-pin WQFN K8     64-pin FBGA F1       5 x 5 mm Pitch: 0.5 mm     10 mm Pitch: 0.5 mm	8 K)       (8 K)       (8 K)         0F3797 <sup>Note 1</sup> 8 K)       μPD70F3801 <sup>Note 1</sup> (8 K)       μPD70F3805 <sup>Note 1</sup> (8 K)         in WOFN K8 8 K)       48-pin LQFP GA       64-pin LQFP B       80-pin LOFP GB         10 x 10 mm ht 0.5 mm       12 x 12 mm Pitch: 0.5 mm       12 x 12 mm Pitch: 0.5 mm         Image: Pitch work with the set of the se	(8 K)       (8 K)       (8 K)       (8 K)         (9 S797)***********************************	8 K)(8 K)(8 K)(6 K)(9 K)







## 3 llers

D70F3743 (32 K)

D70F3744 (40 K)

D70F3745 (60 K)

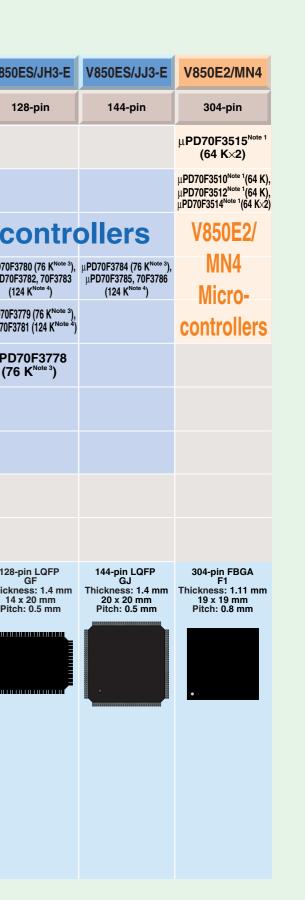
D70F3746 (60 K)

44-pin

50ES/JJ3

We offer flash microcontrollers in various packages and ROM and RAM sizes, allowing you to select the best flash microcontroller for your product or application.

Generic Name	V850ES	S/JC3-H	V850ES/JE3-H	V850ES/JG3-H	V850ES/JH3-H	V850ES/JG3-U	V850ES/JH3-U	V850ES/JE3-E	V850ES/JF3-E	V850ES/JG3-E	V850
Pin Count ROM (Bytes)	40-pin	48-pin	64-pin	100-pin	128-pin	100-pin	128-pin	64-pin	80-pin	100/113-pin	1
2048 K	Part Number (RAM: Bytes)										
1024 K			US	В					E	Etherne	t
768 K	V850E	S/Jx3-H	and Jx	3-U Mic	rocont	rollers		V850	ES/Jx3	-E Micr	oc
512 K/ 480 K				μ <b>PD70F3762</b> (56 K <sup>Note 2</sup> )	μ <b>PD70F3767</b> (56 K <sup>Note 2</sup> )	μ <b>PD70F3764</b> (56 K <sup>Note 2</sup> )	μ <b>PD70F3769</b> (56 K <sup>Note 2</sup> )				μ <b>PD70F</b> μ <b>PD70</b> (1
384 K/ 376 K				μ <b>PD70F3761</b> (48 K <sup>Note 2</sup> )	μ <b>PD70F3766</b> (48 K <sup>Note 2</sup> )	μ <b>PD70F3763</b> (48 K <sup>Note 2</sup> )	μ <b>ΡD70F3768</b> (48 K <sup>Note 2</sup> )				μ <b>PD70F</b> 3 μ <b>PD70F</b> 3
256 K	μ <b>PD70F3813</b> <sup>Note 1</sup> (24 K)	μPD70F3818 <sup>Note 1</sup> , μPD70F3819 <sup>Note 1</sup> (24 K)	μPD70F3824 <sup>Note 1</sup> , μPD70F3825 <sup>Note 1</sup> (24 K)	μ <b>PD70F3760,</b> μ <b>PD70F3770,</b> (40 K <sup>Note 2</sup> )	μ <b>PD70F3765,</b> μ <b>PD70F3771,</b> (40 K <sup>Note 2</sup> )			μPD70F3828 <sup>Note 1</sup> , μPD70F3829 <sup>Note 1</sup> (64 K <sup>Note 3</sup> )	μPD70F3832 <sup>Note 1</sup> , μPD70F3833 <sup>Note 1</sup> (64 K <sup>Note 3</sup> )	μPD70F3836 <sup>Note 1</sup> , μPD70F3837 <sup>Note 1</sup> (64 K <sup>Note 3</sup> )	μ <b>ΡD</b> (70
128 K	μ <b>PD70F3812</b> <sup>Note 1</sup> (24 K)	μ <b>PD70F3817</b> <sup>Note 1</sup> (24 K)	μ <b>PD70F3823</b> <sup>Note 1</sup> (24 K)					μ <b>PD70F3827</b> <sup>Note 1</sup> (48 K <sup>Note 3</sup> )	μ <b>PD70F3831</b> <sup>Note 1</sup> (48 K <sup>Note 3</sup> )	μ <b>PD70F3835</b> <sup>Note 1</sup> (48 K <sup>Note 3</sup> )	
64 K	μ <b>PD70F3811</b> <sup>Note 1</sup> (24 K)	μ <b>PD70F3816</b> <sup>Note 1</sup> (24 K)	μ <b>PD70F3822</b> <sup>Note 1</sup> (24 K)					μ <b>PD70F3826</b> <sup>Note 1</sup> (32 K <sup>Note 3</sup> )	μ <b>PD70F3830</b> <sup>Note 1</sup> (32 K <sup>Note 3</sup> )	μ <b>PD70F3834</b> <sup>Note 1</sup> (32 K <sup>Note 3</sup> )	
32 K	μ <b>PD70F3810</b> <sup>Note 1</sup> (16 K)	μ <b>PD70F3815</b> <sup>Note 1</sup> (16 K)	μ <b>PD70F3821</b> <sup>Note 1</sup> (16 K)								
16 K	μ <b>PD70F3809</b> <sup>Note 1</sup> (8 K)	μ <b>PD70F3814</b> <sup>Note 1</sup> (8 K)	μ <b>PD70F3820</b> <sup>Note 1</sup> (8 K)								
	40-pin WQFN K8 Thickness: 0.75 mm 6 x 6 mm Pitch: 0.5 mm	48-pin LQFP GA Thickness: 1.4 mm 7 x 7 mm Pitch: 0.5 mm	64-pin LQFP GB Thickness: 1.4 mm 10 x 10 mm Pitch: 0.5 mm	100-pin LQFP GC Thickness: 1.4 mm 14 x 14 mm Pitch: 0.5 mm	128-pin LQFP GF Thickness: 1.4 mm 14 x 20 mm Pitch: 0.5 mm	100-pin LQFP GC Thickness: 1.4 mm 14 x 14 mm Pitch: 0.5 mm	128-pin LQFP GF Thickness: 1.4 mm 14 x 20 mm Pitch: 0.5 mm	64-pin LQFP GB Thickness: 1.4 mm 10 x 10 mm Pitch: 0.5 mm	80-pin LQFP GK Thickness: 1.4 mm 12 x 12 mm Pitch: 0.5 mm	100-pin LQFP GC Thickness: 1.4 mm 14 x 14 mm Pitch: 0.5 mm	128 Thicki 14 Pito
			64-pin FBGA <sup>Note 5</sup>				•				
Package		48-pin WQFN K8 Thickness: 0.75 mm 7 × 7 mm	F1 Thickness: 1.11 mm 6 x 6 mm Pitch: 0.65 mm					64-pin WQFN K8 Thickness: 0.75 mm 9 x 9 mm		113-pin FBGA F1 Thickness: 0.91 mm 8 × 8 mm	
		Pitch: 0.5 mm	64-pin WQFN K8 Thickness: 0.75 mm 9 x 9 mm Pitch: 0.5 mm					Pitch: 0.5 mm		Pitch: 0.65 mm	



## **Specialized lineup**

We provide products ideally suited to specific applications.

With our extensive specialized lineup, you can choose the product that is perfect for your system.

#### Connectvity

Our All Flash V850 microcontrollers help you realize systems Connectivity of V850 products that are friendly both to people and the environment by providing organic inter-device connectivity, allowing data to be transferred between systems and effectively controlled, whatever the application.

In addition to standard serial interfaces, we can offer V850 microcontrollers with interfaces that enable data transfer types and speeds suited to each application, letting you easily build the system you need.

Function	U362.0 F	uii-speau	CAIN	Ellienie
Device	Function	Host		10/100 M
V850ES/JG3-L	0*			
V850ES/Jx3-H	0		0*	
V850ES/Jx3-U	0	0		
V850ES/Jx3-E	0		O*	0
V850E/lx4-H	0			
V850E2/MN4	0	0	0*	0*

USB certified.

USB host and function driver configuration

User application

File system<sup>Note</sup>

MSC 01<sup>Note 2</sup> CDC 01<sup>Note 2</sup> Other

USB host driver/ USB function drive

Hardware

Sample code for these sections is supplied

sas Electronics or a partner com

Notes 1. Can only be used with USB host. 2. Can only be used with USB function

Class drive

■ All our USB microcontrollers are

In some Products only

#### USB microcontrollers (V850ES/Jx3-H, V850ES/Jx3-U)

#### 1. Complete USB 2.0 functionality on a single chip

Applications that require USB connectivity can be developed guickly and easily because a USB 2.0 host<sup>Note</sup> and/or USB 2.0 function interface is integrated on the microcontroller chip, eliminating the need to connect an external USB chip.

#### ■ USB specifications

- On-chip USB 2.0 host (full-speed) interface x 1 ch<sup>Note</sup>/ USB 2.0 function (full-speed) interface x 1 ch
- USB host transfer modes<sup>Not</sup>
- : Control, Bulk, Interrupt, and Isochronous USB host interface connectivity<sup>Note</sup>
- : PPON (USB power supply output) pin OCI (overcurrent detection input) pin
- USB function interface endpoint configuration: Two endpoints for Control transfers, four endpoints for Bulk transfers, and one endpoint for Interrupt transfers Note V850ES/Jx3-U only.

### 2. Extensive USB driver support

#### ■ USB drivers

- USB function driver
- Renesas Electronics provides free sample code. Driver software is available from one of our partner companies. USB host driver
- Driver software is available from one of our partner companies. Partner companies : Interface Co., Ltd. Tepco Uquest, Ltd.

Grape Systems Inc. Ubiguitous Corporation Remark MSC: Mass storage class

CDC: Communication device class

#### 3. Supportive development environment

Renesas Electronics offers two starter kits: one for USB host system development, and one for USB function system development. These starter kits help you develop and evaluate your application at the system level.



TK-850/JH3U-SP Made by TESSERA Technology Inc.



TK-850/JG3H Made by TESSERA Technology Inc.

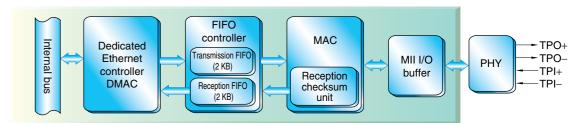
#### Ethernet microcontrollers (V850ES/Jx3-E)

#### 1. Control your networks and systems using only the internal memory.

Renesas Electronics' Ethernet microcontrollers incorporate up to 512 KB of flash memory and 124 KB of RAM. They also include a 10/100 base Ethernet MAC and feature a high CPU performance of 103MIPS (Dhrystone 1.1) when operating at 50 MHz. This enables network and system control using only the internal memory.

### 2. On-chip Ethernet controller lets you build a low-cost system

- MAC
- Enables IEEE802.3-compliant 10/100 Mbps full-duplex and half-duplex communication as well as flow control. Uses MII as the physical layer device (PHY) interface. Includes an on-chip VLAN detector.
- FIFO size: Transmission = 2 KB, Reception = 2 KB
- Dedicated Ethernet controller DMAC
- On-chip reception checksum calculator compliant with RFC1071

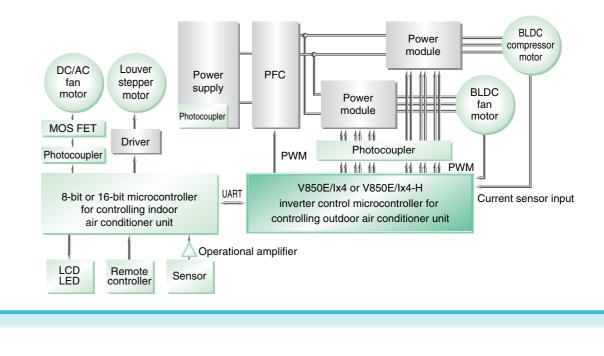


### 3. Enhanced development environment and network software Network software is also available from our partner companies. See the Application Examples page for more details.

### Inverter control microcontrollers (V850E/Ix4, V850E/Ix4-H)

The V850E/Ix4 and V850E/Ix4-H feature a CPU with a ramped up operating speed of 100 MHz—1.5 times faster than our previous model-enabling much finer inverter control.

Add this to the large-capacity internal flash memory (256 KB to up to 480 KB), and you have a microcontroller that is ideal for systems that need to run large-capacity programs, such as factory automation equipment. Our inverter control microcontrollers also have a wealth of built-in analog peripherals, including a 12-bit A/D converter, six operational amplifiers, and 12 comparators. This helps greatly to reduce the number of components in the system and reduce costs.



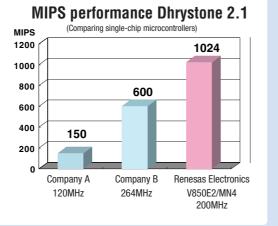
Renesas Electronics provides a starter kit that can be used for evaluation and development at the system level and network software.

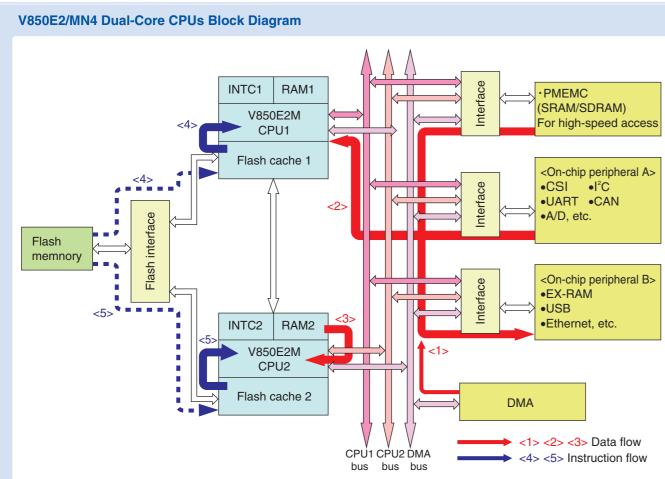
## **High performance**

#### On-chip high-performance CPU core V850E2M corresponding to the multi-core

All Flash microcontroller with dual-core 32-bit CPU boasting world-beating performance (V850E2/MN4)We have now developed the V850E2M, an ultrafast CPU core based on the high-performance V850E2 (with 2-way parallel superscalar architecture and a 7-stage pipeline) but featuring faster conditional branch instruction execution and a new variable-step division instruction to enable high-speed division calculations. The V850E2M realizes an outstanding performance of 2.56 MIPS/MHz-close to 1.4 times faster than our other cores when operating at the same frequency.

The V850E2/MN4 lineup features products that incorporate two of these ultrafast CPU cores to enable a world-beating performance of 1024 MIPS when operating at 200 MHz. Our dual-core microcontrollers also consume only 0.88 mW/MIPS-60% of the power of single-core products-and provide a wide range of on-chip peripherals, letting you build smaller systems with fewer components.





The V850E2/MN4 includes three high-speed internal buses to maximize the dual-core performance. These buses allow various types of processing to be performed in parallel.

By maximizing the performance of each unit in this way, the overall performance can be dramatically improved. Example of processing that can be performed in parallel:

<1> Data is transferred at high speed from an external memory to an Ethernet peripheral by using DMA.

<2> CPU1 executes CAN communication protocol processing while processing other data at the same time.

<3> CPU2 processes the data from internal RAM2 while its high-performance CPU core executes high-speed calculations.

<4>, <5> CPU1 and CPU2 execute no-wait instruction fetches from the microcontroller's large-capacity flash memory using the flash cache in each core.

#### **Rich development environment** Introducing Prism<sup>Note</sup>, a dynamic analysis tool for multi-core microcontrollers (V850E2/MN4)

Prism is an analysis and verification environment that provides software optimized for implementing multi-core architecture.

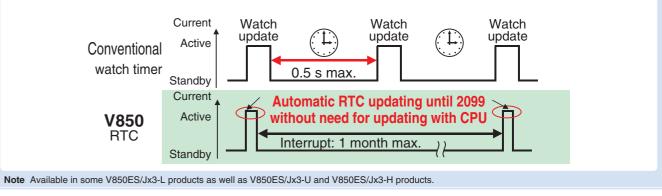
Prism provides virtual task division, core assignment, and data-dependent display features that allow software engineers to easily develop and realize the full potential of multi-core processors without the need to change the source code.

Note Made by CriticalBlue, Inc



High-performance RTC (real-time counter)<sup>Note</sup> Alleviates the software load and further reduces power consumption.

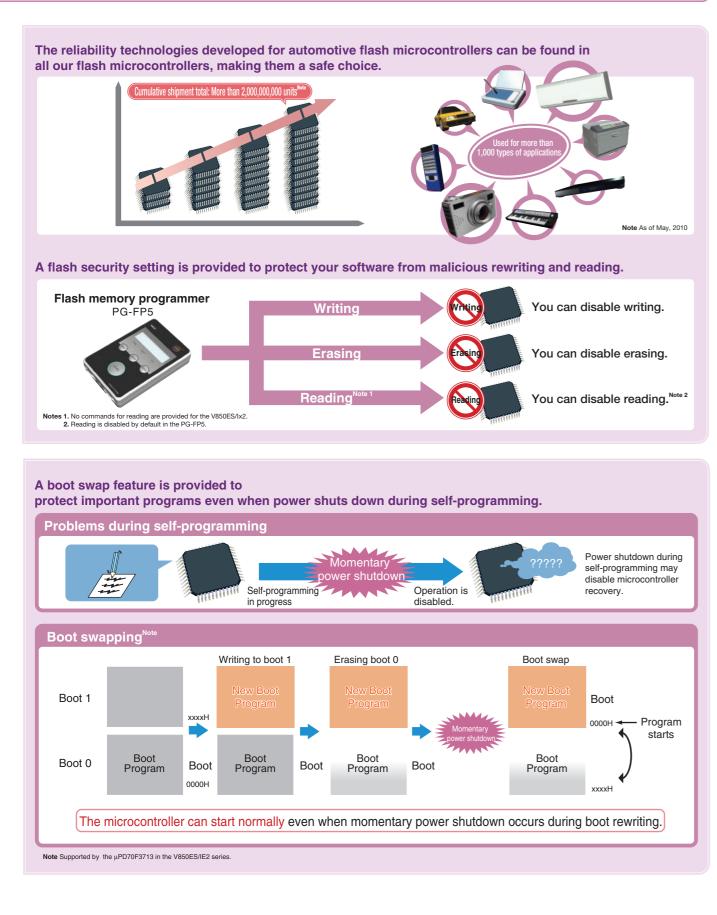
- Sustained clock operation without wakeup to reduce power consumption.
- Built-in alarm starts the microcontroller at the set time (day, hour, minute).



Ex3.ped 💽 Prism		PrismPAD.pad 23	<sup>#</sup> 1	2
Load Slice	Prism v PAD mo	ersion 2.1.205 dified 2010/07/1	15 1:55:14 cri	ticalbl
				<b>.</b>
			1	
	······································	·····		
	1		4	
ROK 1600K 1920K	2240K 2560K	2880K 3200K 35	206	
-				
e Performance 🔠	Architecture	🖪 🖲 f	4 🗐 🛯 🗧	<b>.</b> A
ce Call Coverage	Self Cycles	Total Cycles	Total Cycles	Histogr
0.0%	512	512	0.0%	
0.0%	196	1,079,867	30.3%	
0.0%	48,459	234,383	6.6%	
0.0%	160	682,512	19.2%	
0.0%	157	9,162	0.3%	
0.0%	120	153,614	4.3%	
0.0%	180	2,483,168	69.7%	
	0% Total T	ime		
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	2	SM	3M	3.5
24			F	2
2M	1 5	1 1		
	1 1	11		
	1 1	1		1 of 236

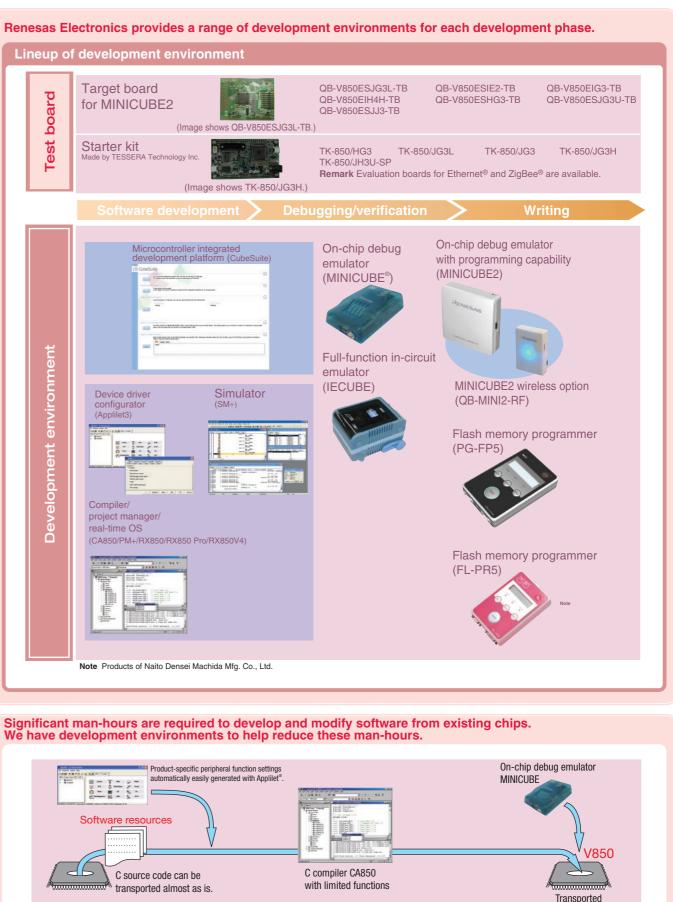
No need for updating using a program because the RTC has a calendar feature for automatic updating until 2099.

# **High reliability**

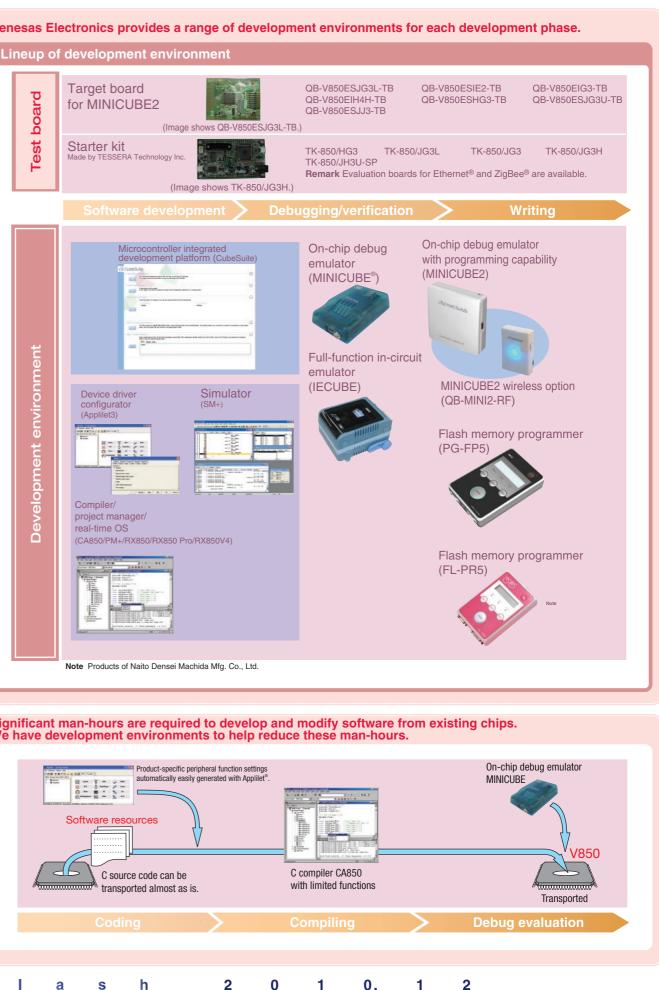


32 - b i t A I I

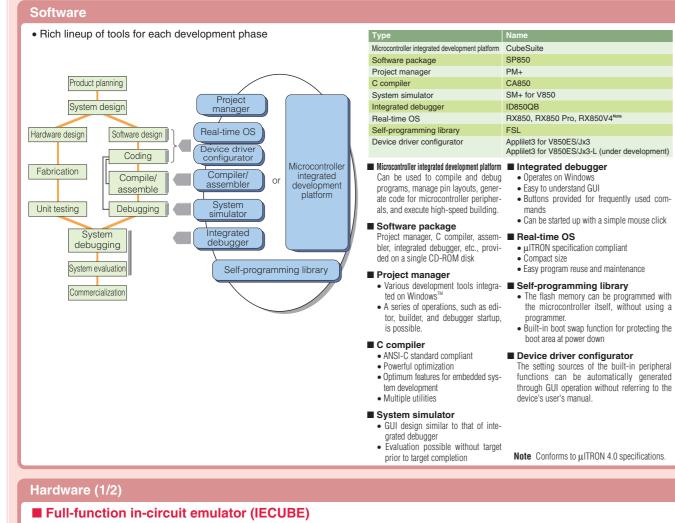
# **Supportive environment for development** (1/2)



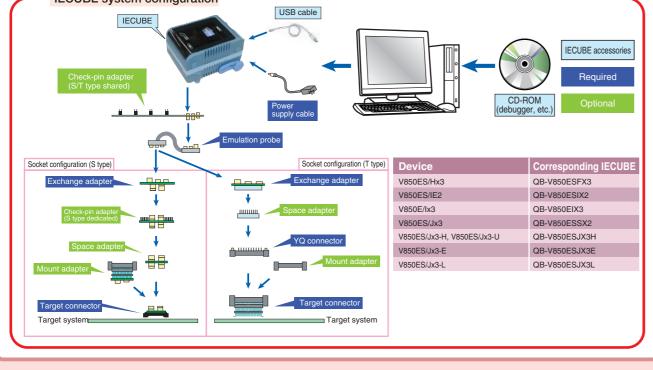
F.



## Supportive environment for development (2/2)



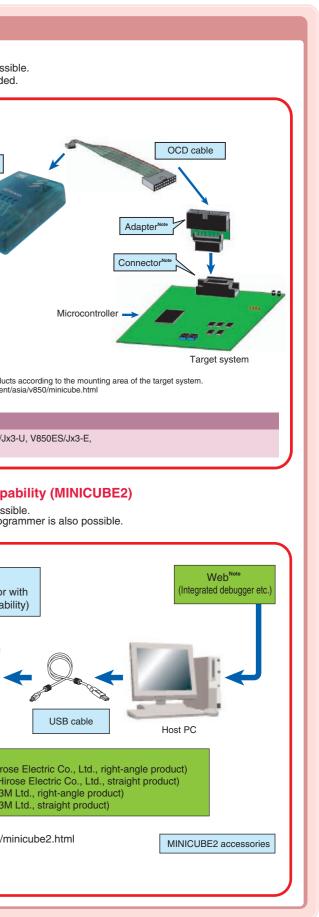
Enables detailed debugging through equivalent emulation of microcontrollers, using trace, time measurement, and other functions.
 IECUBE system configuration



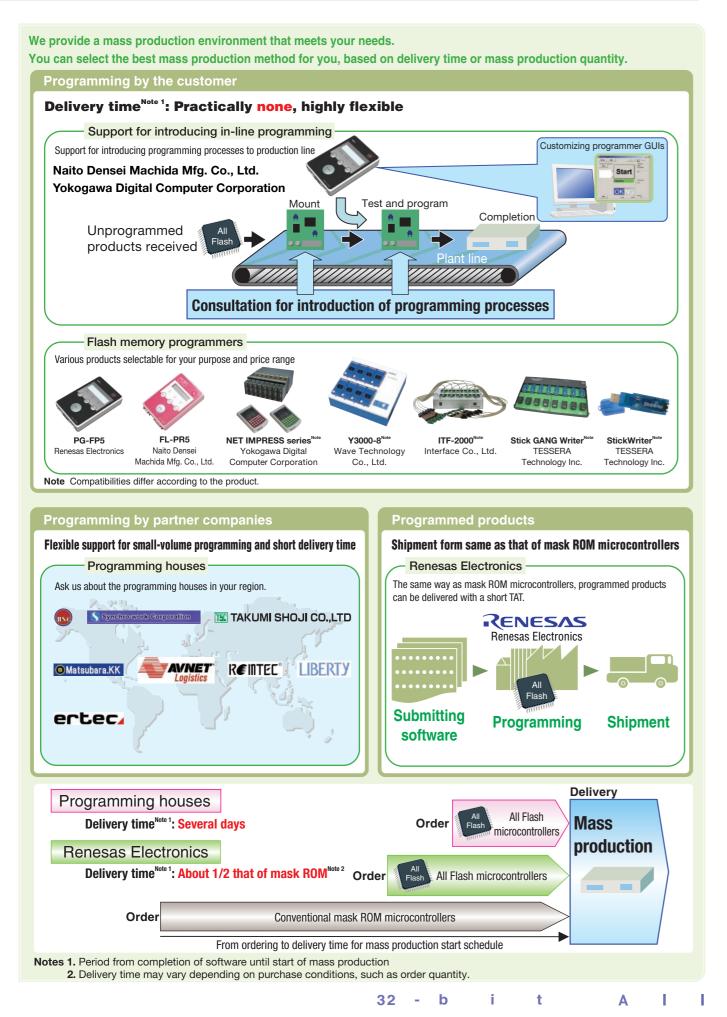
To order development tools, contact a Renesas Electronics sales representative or an authorized Renesas Electronics distributor.

<ul> <li>Debugging with</li> </ul>	ebug emulator (MINIC h the microcontroller mounter ng functions such as run, bre	d on the target system
	UBE system configuration	
CD-ROM (debugger etc.)	J 🔪 _	SB cable
	r and connector can be selected and p re information on MINICUBE: http://ww	
MINICUBE su	oported devices	
V850ES/Hx3,	V850E/IG3, V850ES/Jx3, V850ES	S/Jx3-L, V850ES/Jx3-H, V
On-chip d	50E/Ix4-H, V850E2/MN4	
<ul> <li>On-chip d</li> <li>Debugging wit</li> <li>In addition to s</li> </ul>		d on the target system ground monitor, use as
<ul> <li>On-chip d</li> <li>Debugging wit</li> <li>In addition to s</li> </ul>	ebug emulator with fl h the microcontroller mounte imple debugging with a foreg	d on the target system ground monitor, use as
<ul> <li>On-chip d</li> <li>Debugging wit</li> <li>In addition to s</li> </ul>	ebug emulator with fl h the microcontroller mounte imple debugging with a forego UBE2 system configurat	d on the target system ground monitor, use as ion MINICUE (On-chip debug er
On-chip d Debugging wit In addition to s MINIC	ebug emulator with fl h the microcontroller mounte imple debugging with a forego UBE2 system configurat Target cable	d on the target system ground monitor, use as ion MINICUE (On-chip debug er flash programming
On-chip d  Debugging wit In addition to s MINIC	ebug emulator with fl h the microcontroller mounte imple debugging with a forego UBE2 system configurat Target cable	d on the target system ground monitor, use as ion MINICUE (On-chip debug er flash programming flash programming contents

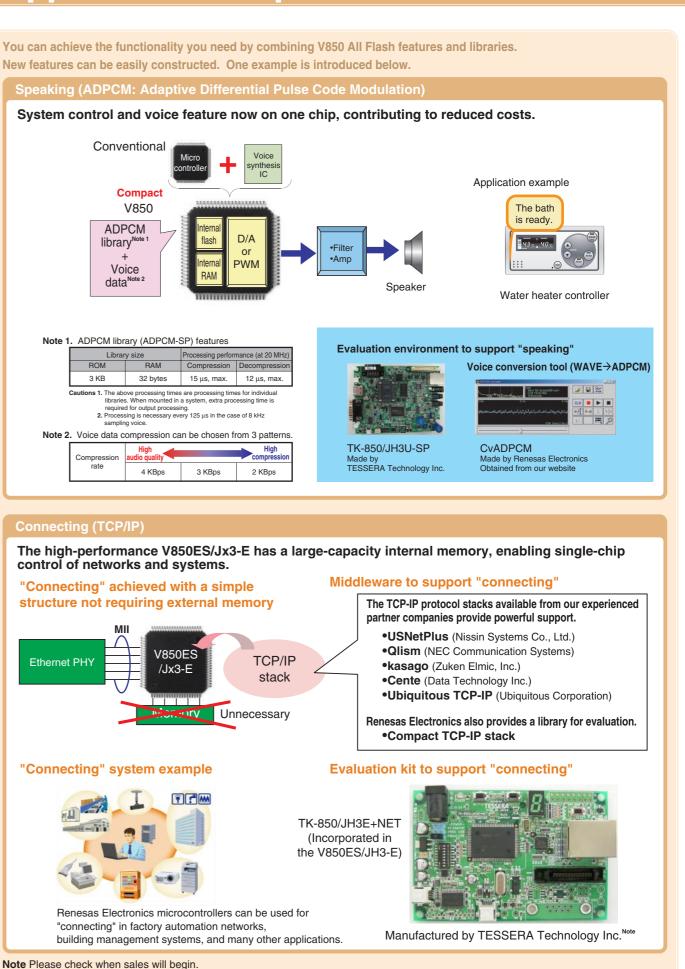
To order development tools, contact a Renesas Electronics sales representative or an authorized Renesas Electronics distributor.



# **Support for mass production**



# **Application examples**



h

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Note Please check when sales will begin.

S

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## **Product specifications (1/4)**

ieneric Name			V850ES/HE3	V850ES/HF3	V850ES/HG3	V850ES/HJ3	V850ES/IE2	V850E/IF3	V850E/IG3		V850E/IC	4		V850E/IH	4	V	850E/IG4	Н	V850	E/IH4-H
n Count			64-pin				64-pin	80-pin	100-pin 100-/16 <sup>-</sup>	1-pin	100-pin			128-pin			100-pin		1	28-pin
rt Number			۰. ۲.				<u>5</u> 4		5 53	·	α 4 	15	9	1	<u></u>	0	02	5	8	<u>ຄ</u>
			-374				-37	34	34!		39	30	33	-33	33	33	36	-33	30	330
			070F				1070F	70F	70F		1070F	1070F	1070	10F	1070	1070	10L	1070	L HOL	101
			H H															L L L		
PU core			V850ES core	V850ES core	V850ES core	V850ES core	V850ES core	V850E core	V850E core		V850E cor			V850E core			V850E core		V8	i0E core
PU performance (Dhr	rvstone)			69MIPS	(@ 32 MHz)	66MIPS (@ 32 MHz)	39MIPS (@ 20 MHz)	131MIPS (@ 64 MHz)	131MIPS (@ 64 MHz)	)	197MIPS (@ 100		197	7MIPS (@ 100		197	MIPS (@ 100	ЛНz)		6 (@ 100 MHz)
ash memory (bytes)	,,		128 K	256 K	256 K	256 K 512 K	64 K 128 K	128 K 256 K	128 K 256		256K 384 K	480 K	256K	384 K	480 K	256K	384 K	480 K		384 K 48
AM (bytes)			8 K	16 K	16 K	16 K 32 K	6K 6K	8 K 12 K	8 K 12 F		24 K 24 K	24 K	24 K	24 K	24 K	24 K	24 K	24 K		24 K 24
ternal bus	Bus type		-	-	-	Multiplexed	-	-	- Notes 1					_			Multiplexed <sup>Not</sup>			e/Multiplexed
erface	Address bus		_	_	_	16	_	_	- 8/16 <sup>N</sup>		_			_			16		Multiplexed:16	
	Data bus		_	-	_	8/16	_	_	- 8/16 <sup>N</sup>		_			-			8/16			8/16
	Chip select signa	1	_	-	_	4	_	_	- 2 <sup>Note</sup>		_			_			2			2
ower supply voltage				3.7 to 5.5 V (A	/D converter: 4	.0 to 5.5 V)	3.5 to 5.5 V	3.5 to	5.5 V		1.5 V/5.0 V			1.5 V/5.0 V		1	.5 V/5.0 V/3.3	V	1.5 V/	5.0 V/3.3 V
				`		,	(A/D converter:	(A/D converter	4.0 to 5.5 V)											
							4.5 to 5.5 V)													
ock	Main clock				4 to 32 MHz		20 MHz	4 to 64 MHz	4 to 64 MHz		10 to 12.5 M	Ηz		10 to 12.5 MH	łz		10 to 12.5 MH	z	10 to	12.5 MHz
	Subclock (crystal	resonator)			32.768 kHz		-	-	-		-			-			-			-
	Internal oscillatio	n clock			kHz/8 MHz (TY		-	-	-		-			-			-			-
ort	I/O		51	67	84	128	39	44	56		55			68			51			68
	Input		-	-	-	-	-	4	8		12			12			12			12
mer/counter	16-bit timer	Number of channel	s 1	1	2	3	2	2	2		2			2			2			2
	(TMQ/TAB)	Function					Interval tin	ner/external event counter	PWM output (3 char	nnels ma	ax.)/pulse width measu	rement/square	-wave outpu	it/one-shot pu	lse output					
	16-bit timer	Number of channel	<b>s</b> 5	5	5	5	4	5	5		3			3			3			3
	(TMP/TAA)	Function						Interval timer/externa	event counter/PWM	output/p	/pulse width measurem	ent/square-wa	ive output/or	ne-shot pulse	output					
	16-bit timer	Number of channel	s –	-	-	-	-	2	2		4			4			4			4
	(TMT)	Function	-	-	-	-	-							vent counter/l						
														nent/square-w tput/encoder						
	16-bit timer	Number of channel	s 1	1	1	1	1	4	4		4	0110 0	not paloo ou	4	oountoi		4			4
	(TMM)	Function								Inter	rval timer									
	Watch timer		1	1	1	1	_	-	_		_			-			_			_
	Real-time counte	(RTC)	_	_	_	_	_	_	_		_			_			_			_
	Watchdog timer (		1	1	1	1	1	1	1		1			1			1			1
erial interface	CSI	,	2	2	2	3 1	1	_	_					_			_			_
		c transmit/receive																		
	functions (32-byt		-	-	-	-	-	-	-		-			-			-			-
	UART	,	2 <sup>Note 4</sup>	2 <sup>Note 4</sup>	3 <sup>Note 4</sup>	3 <sup>Note 4</sup> 4 <sup>Notes 4, 5</sup>	2	_	_		_			_			_			-
	l <sup>2</sup> C		1	- 1	1	1 -	_	_	_		_			_			_			_
	UART/CSI		-	-	_	- 2 <sup>Notes 4, 5</sup>	_	3 <sup>Note 6</sup>	3 <sup>Note 6</sup>		3 <sup>Note 6</sup>			3 <sup>Note 6</sup>			3 <sup>Note 6</sup>			3 <sup>Note 6</sup>
	UART/I <sup>2</sup> C		_	-	-	- 1 <sup>Note 4</sup>	_	1	1		1			1			1			1
	CSI/I <sup>2</sup> C		_	_	_	_	_	_	_		_			_			_			_
	UART/CSI/I <sup>2</sup> C		_	-	_	_	_	_	_		_			-			_			1
	UART/I <sup>2</sup> C/CAN		_	-	_	_	_	_	_		_			_			_			_
SB controller	USB2.0 function		_	_	_	_	_	_	_		_			_			1 (full-speed)		1 (fu	III-speed)
	USB2.0 host		_	_	_	_	_	_	_		_			_			_		. (.	_
thernet controller			_	_	_	_	_	_	_					_			_			_
/D converter	12-bit		_	_	_	-	_	5×2 units	5×2 units		(4, 3) × 1 uni	s		4×2 units			(4, 3) × 1 units	;	4 >	2 units
	10-bit		10	12	16	24	4×2 units	4	8		12			12			12			12
A converter (8-bit)			-	-	-	-	-	-			-			-			-			-
A controller			4 <sup>Note 7</sup>	4 <sup>Note 7</sup>	4 <sup>Note 7</sup>	4 <sup>Note 8</sup>	_	4 <sup>Note 7</sup>	4 <sup>Note 7</sup>		7			7			7			7
errupt source	External		9	9	12	16	7	15	21		22			22			22			22
	Internal		43	43	51	58 64	36	74	75		82			82			84			84
-chip debug functio			Provided	Provided	Provided	Provided	-	-	Provided		Provided			Provided			Provided		D	ovided
and a start of the	MINICUBE2		Provided	Provided	Provided	Provided	Provided	Provided	Provided		Provided			Provided			Provided			ovided
her peripheral I/O fu			Flovided		ptor control (1 cl		Motor control (1 ch),	Motor contro			Provided	Motor cor	ntrol (2 ch),	Flovided			FIOVIDED	Motor or	ntrol (2 ch),	ovided
and peripheral i/O lu	monons				-Z output contro		Hi-Z output control.	Hi-Z outpu				Hi-Z outp	ut control,					Hi-Z out	out control,	
				01	n-chip POC/LVI		on-chip POC/LVI, RAM hold flag,	on-chip	POC/LVI,		oper	ational amplifie	er (6 ch), com	parator,			opera	ional amplifi	er (6 ch), comparat	or,
					clock monitor, V hold flag, SS(	G	clock monitor	operational amplifier	comparator, monitor				POC/LVI, monitor						POC/LVI, monitor	
				nAl				0001	monitor			01001/1						SIOCK		

Notes 1. Separate/Multiplexed (interface voltage: 5 V) 2. Provided only in the GC package products. 3. Interface voltage: 5 V 4. UART supports LIN. 5. This UART in one of these channels. 7. Transfer to/from on-chip peripherals and internal RAM. 8. Transfer to/from on-chip peripherals, internal RAM, and external memory.

# **Product specifications** (2/4)

Generic Name						V	850ES/	JC3-L <sup>Note</sup>	e 1					V850E	-s /J	E3-L N	ote 1	V850E	ES/JF3-L			V8	50ES /	IG3-L				V850E	S/.IG3_			V850	ES/JJ3	3
Pin Count					40-pin					48-pin					64 -pi			+	0-pin				100/121					100-					-pin	<u> </u>
art Number			~	ω		0	0	- I	N	<u>+0-pin</u> က	4	<u> </u>	с D	0	<u>⊳</u>		0	<u>ان</u> م	o-piii د	~	∞		<u>100/121</u>			i i	<u> </u>			2	8	4	-pin LΩ	
			379	379	379	380	383	380	380	380	380	383	380	380	380	380	384	373	373	373	373	379	379	94 %	95 <sup>%</sup>	%   96	373	374	374 <sup>.</sup>	374;	374:	374.	374	
			70F	ZOF	70F	ZOF	70F	70F	70F	70F	70F	70F	70F	70F	70F	70F	70F	70F	70F	70F.	70F	70F	ZOF	DF37	DF37	DF37	ZOF	ZOF	70F.	70F	70F	70F	70F	
			Ĝ	<u> </u>	<u> </u>	<u> </u>	l G		Ĝ	Ĝ	Ĝ.	Ê	Ĝ.	Ū I	G	l ĝ	ģ		Ĝ	Ĝ	Ĝ	D G	<u> </u>	PD7	PD7	PD7		Ĝ	D	D D	D	Ū	G	
PU core							V850E	ES core				-			)ES co	re		V850	0ES core			1 1	/850ES co	re		-2.		V850E	S core			V850E	S core	
PU performance (Dh	rystone)					43	3MIPS (@ 2									20 MHz)			6 (@ 20 MHz)				MIPS (@ 2					69MIPS (@			6		@ 32 MH	
lash memory (bytes)			16 K	32 K	64 K		256 K		32 K	64 K	128 K 2	56 K	16 K 3			128 K	256 K		. ,	128 K	256 K			256 K	384 K	512 K	384 K	512 K						
RAM (bytes)			8 K	8 K	8 K	8 K	16 K	8 K	8 K	8 K				8 K	8 K		16 K	8 K		8 K	16 K	32 K		_	40 K	40 K	32 K	40 K	60 K	60 K	32 K		60 K	
External bus	Bus type				-		_			-					-			Multi	ltiplexed			Sep	arate/Mul	tiplexed				Separate/N	Iultiplexe	d	S	eparate/	Multiplex	xed
nterface	Address bus				-					-					-				18				22					2	2			. 2	24	
	Data bus				-					-					-			8	8/16				8/16					8/1	16			8/	/16	
	Chip select sign	al			-					-					-				-				-					-					4	
Power supply voltage										2.2 to	3.6 V (A/D o	converter	r: 2.7 to 3.6	6 V)									3.6 V (A/D nverter:		2.0 to 3.6 eration: 3.0				(A/D	2.85 to converte		.6 V)		
Clock	Main staak				C +- 00 M				0	C += 00 MI	-			0.5.4-	00 14	-		0.5.44	- 00 MU		0.5.44		to 3.6 V)	A/D cor	overter: 2.7	to 3.6 V)		2.5 to 3					32 MHz	
Clock	Main clock	l roconstar)			2.5 to 20 M					.5 to 20 Mi	72				20 MH				o 20 MHz		2.5 to	20 MHz			z, 2.5 to 16 MHz (	USB operation								
	Subclock (crysta				32.768 kH 20 kHz (T)					2.768 kHz 20 kHz (T)	(P)				768 kH (H-7 (T-)				768 kHz				32.768 k					32.768					8 kHz	
Port	Internal oscillati	OILCIOCK	0		ich 5 V tol	,	7)			20 kHz (TY 34 (of whi			50.(0		Hz (T) V tole	YP.) erance: 28	8)		Hz (TYP.)	84 (of w	hich 5 V		220 kHz (T which 5 V	YP.) 80 (of w	hich 5 V t	olerance	84 (0	220 kHz f which 5 \	· /	e· 40)	128 (05		lz (TYP.) V tolera	
			2			oranoe. I	.,			V tolerance			50 (0			010100.20	.,		ance: 25)	toleran			ance: 31)	00 (01 W	28)	noranoe.	04 (0	WHICH 51	loierand	0.40)	120 (01	which a	Violera	nce. 0
	Input				-					-					-				-				-						-				-	
Timer/counter	16-bit timer (TMQ/TAB)	Number of channels			1					1					1				1				1					1					1	
		Function									Interva	al timer/ex	xternal eve	ent count		VM output	t (3 chann	nels max.)	.)/pulse wid	Ith measu	rement/s	quare-w		t/one-sho	t pulse ou	tput								
	16-bit timer (TMP/TAA)	Number of channels			6					6					6				4				6					e	;				9	
		Function										Interva	al timer/ext	ternal ev	ent co	unter/PW	M output/	/pulse wid	idth measu	rement/sc	quare-wa	ive outpu	ut/one-sho	t pulse ou	tput									
	16-bit timer (TMT)	Number of channels			-					-					-				-				-										-	
	()	Function			-					-					-				-				-					-	-				-	
	16-bit timer	Number of channels			1					1					1				1				1					1					1	
	(TMM)	Function																Interva	val timer															
	Watch timer				1					1					1				1				1					1					1	
	Real-time count				-					-					-				-	-	-			1					-				-	
	Watchdog timer	(WDT)			1					1					1				1				1					1					1	
Serial interface	CSI				1					2					3				2				3					3	5				4	
	CSI with automa functions (32-by	tic transmit/receive te buffer)			-					-					-				-				-					-					-	
	UART				1 Note 2					-					-			2'	2 <sup>Note 2</sup>	-	-			4 <sup>Note 2</sup>				-	-			1 <sup>N</sup>	ote 2	
	I <sup>2</sup> C				-					-					-				-				-					-					_	
	UART/CSI				-					1 Note 2				1'	Note 2				-				1 <sup>Note 2</sup>					1 <sup>No</sup>	te 2			1 <sup>N</sup>	ote 2	
	UART/I <sup>2</sup> C				1 Note 2					2 Note 2				2 '	Note 2			1'	1 <sup>Note 2</sup>				2 <sup>Note 2</sup>					2 <sup>No</sup>	te 2			2 <sup>N</sup>	lote 2	
	CSI/I <sup>2</sup> C				1					1					1				1				1					1					1	
	UART/CSI/I <sup>2</sup> C				-					-					-				-				-					-	-				-	
	UART/I <sup>2</sup> C/CAN				-					-					-				-				-						-				-	
USB controller	USB2.0 function				-					-					-				-			-		1	(full-spee	ed)		-	•				-	
	USB2.0 host				-					-					-				-				-					-	-				-	
Ethernet controller	10.1.1				-					-					-				-				-					-					-	
A/D converter	12-bit				-					-					-				-				-					-					-	
2/4	10-bit				5					6					10				8				12						2				16	
D/A converter (8-bit)										1 4 <sup>Note 3</sup>				4 N	1 Note 3				1 4 <sup>Note 3</sup>				2 4 <sup>Note 3</sup>					2 4 <sup>No</sup>					2 lote 3	
OMA controller nterrupt source	External				6					6				4	9			-	9				9					4						
	Internal				43					6 47					9 49				9 40	4	8		9	55				4					10 61	
On-chip debug functio					Provided	4				Provided				Provi					ovided	4			Provide					Provi					vided	
, access fanotic	MINICUBE2				Provided					Provided				Provid					ovided				Provide											
Other peripheral I/O fu					Trovidee	4				Tovided		On-ch	nip LVI, clo			al-time out	tput, CRC		UNDED				Tiovide	u			Provided Provided On-chip LVI, clock monitor, real-time output, CRC							
														40 to +8																40.1	+85°C			

# **Product specifications (3/4)**

0			Vacato	VICO II Note 1									V850ES/JH3-E	V850ES/JJ3-E
Generic Name				/JC3-H <sup>Note 1</sup>	V850ES/JE3-H Note 1	V850ES/ JG3-H	V850ES/JH3-H	V850ES/JG3-0	V850ES/JH3-U V		V850ES/JF3-E Note 1	V850ES/JG3-E Note 1		
Pin Count			40-pin	48-pin	64-pin	100 -pin	128-pin	100-pin	128-pin	64-pin	80-pin	100/113-pin	128-pin	144-pin
Part Number			μΡD70F3809 μΡD70F3810 μΡD70F3811 μΡD70F3812 μΡD70F3813	μΡD70F3814 μΡD70F3815 μΡD70F3816 μΡD70F3817 μΡD70F3818	μΡD70F3820 μΡD70F3821 μΡD70F3822 μΡD70F3823 μΡD70F3824	μΡD70F3760 μΡD70F3761 μΡD70F3762 μΡD70F3770	μΡD70F3765 μΡD70F3766 μΡD70F3767 μΡD70F3767	μΡD70F3763 μΡD70F3764	μΡD70F3768 μΡD70F3769	μΡD70F3826 μΡD70F3827 μΡD70F3828 μΡD70F3828	uPD70F3830 uPD70F3831 uPD70F3832 uPD70F3833	µРD70F3834 µРD70F3835 µРD70F3836 µРD70F3837	μРD70F3778 μРD70F3779 μРD70F3780 μРD70F3781 μΡD70F3782 шРD70F3783	μΡD70F3784 μΡD70F3785 μΡD70F3785
CPU core			V85	0ES core	V850ES core	V850ES core	V850ES core	V850ES	Score	V850ES core	V850ES core	V850ES core	V850ES core	V850ES core
CPU performance (Dhr	rystone)		98MIPS	S (@ 48 MHz)	98MIPS (@ 48 MHz)	98MIPS (@ 48 MHz)	98MIPS (@ 48 MHz)	98MIPS (@ 48 MHz) 9	88MIPS (@ 48 MHz)	103MIPS (@ 50 MHz)	103MIPS (@ 50 MHz)	103MIPS (@ 50 MHz)	103MIPS (@ 50 MHz)	103MIPS (@ 48 MHz)
Flash memory (bytes)			16 K 32 K 64 K 128 K 256 K	16 K 32 K 64 K 128 K 256 K 256 K	16 K 32 K 64 K 128 K 256 K 256 K	K 256 K 384 K 51 2 K 256 K	256 K 384 K 512 K 256	K 384 K 512 K 3	384 K 512 K 6	4 K 128 K 256 K 256 K	64 K 128 K 256 K 256 K	64 K 128 K 256 K 256 K	256 K 384 K 512 K 384 K 512 K 512	K 512 K 512 K 512
RAM (bytes)			8 K 16 K 24 K 24 K 24 K	8 K 16 K 24 K 24 K 24 K 24 K	8 K 16 K 24 K 24 K 24 K 24 K	40 K <sup>Note 2</sup> 48 K <sup>Note 2</sup> 56 K <sup>Note 2</sup> 40 K <sup>Note 2</sup>	40 K <sup>Note 2</sup> 48 K <sup>Note 2</sup> 56 K <sup>Note 2</sup> 40 K <sup>Not</sup>	<sup>te 2</sup> 48 K <sup>Note 2</sup> 56 K <sup>Note 2</sup> 4	18 K <sup>Note 2</sup> 56 K <sup>Note 2</sup> 32	K <sup>Note 3</sup> 48 K <sup>Note 3</sup> 64 K <sup>Note 3</sup> 64 K <sup>Note 3</sup>	32 K <sup>Note 3</sup> 48 K <sup>Note 3</sup> 64 K <sup>Note 3</sup> 64 K <sup>Note 3</sup>	<sup>3</sup> 32 K <sup>Note 3</sup> 48 K <sup>Note 3</sup> 64 K <sup>Note 3</sup> 64 K <sup>Note 3</sup>	76 K <sup>Note 3</sup> 76 K <sup>Note 3</sup> 76 K <sup>Note 3</sup> 124 K <sup>Note 4</sup> 124 K <sup>Note 4</sup> 124 K <sup>N</sup>	<sup>Note 4</sup> 76 K <sup>Note 3</sup> 124 K <sup>Note 4</sup> 124 K <sup>N</sup>
External bus	Bus type		_	_	_	Multiplexed	Separate/Multiplexed	d Multiplexed	Separate/Multiplexed	-	-	-	Separate/Multiplexed	Separate/Multiplexe
interface	Address bus		_	_	_	16	24	16	24	_	_	_	22	24
	Data bus		_	_	_	8/16	8/16	8/16	8/16	-	_	_	8/16	8/16
	Chip select signa	al	_	_	_	3	3	3	3	-	_	_	2	3
Power supply voltage				(A/D conve	2.85 to 3.6 V ter, USB controller: 3.0 to 3.6 V)			2.85 to (A/D conve controller: 3.	3.6 V erter, USB		(A/ D	2.85 to 3.6 V converter, USB controller:	3.0 to 3.6 V)	
Clock	Main clock		24 to 48 MHz	24 to 48 MHz	24 to 48 MHz	24 to 48 MHz	24 to 50 MHz	24 to 50 MHz	24 to 50 MHz	24 to 50 MHz	24 to 50 MHz			
	Subclock (crysta	l resonator)	32.768 kHz	32.768 kHz	32.768 kHz	32.768 kHz	32.768 kHz	32.768 kHz 3		32.768 kHz	32.768 kHz	32.768 kHz	32.768 kHz	32.768 kHz
	Internal oscillatio		220 kHz (TYP.)	220 kHz (TYP.)	220 kHz (TYP.)	220 kHz (TYP.)	220 kHz (TYP.)	220 kHz (TYP.) 2		220 kHz (TYP.)	220 kHz (TYP.)	220 kHz (TYP.)	220 kHz (TYP.)	220 kHz (TYP.)
Port	I/O		25 (of which 5 V tolerance: 13)	32 (of which 5 V tolerance: 15)	45 (of which 5 V tolerance: 18)	77 (of which 5 V tolerance: 24)	96 (of which 5 V tolerance: 27)	75 (of which 5 V 9 tolerance: 22)	6 (of which 5 V	29 (of which 5 V tolerance: 14)	41 (of which 5 V tolerance: 19)	64 (of which 5 V tolerance: 28)	84 (of which 5 V tolerance: 48)	100 (of which 5 v tolerance: 59)
	Input		-	-	-	-	-	-	-	-	-	-	-	-
Timer/counter	16-bit timer	Number of channels	1	1	1	2	2	2	2	1	1	1	2	2
	(TMQ/TAB)	Function			Interval timer/externa	l event counter/ PWM outp	out (3 channels max.)/p	ulse width mea	surement/squ	are-wave output/one-	shot pulse output			
	16-bit timer	Number of channels	4	4	4	6	6	6	6	4	4	4	6	6
	(TMP/TAA)	Function			Interval timer	r/external event counter/P	WM output/pulse widt	h measurement	/square-wave	output/one-shot puls	e output			
	16-bit timer	Number of channels	1	1	1	1	1	1	1	1	. 1	1	1	1
	(TMT)	Function				Interval timer, external e	vent counter/ PWM out one-shot pulse out			/square-wave output/				
	16-bit timer	Number of channels	4	4	4	4	4	4	4	4	4	4	4	4
	(TMM)	Function					Interv	al timer						
	Watch timer		_	_	_	_	_	_	_	-	_	-	_	_
	Real-time counte	or (BTC)	1	1	1	1	1	1	1	1	1	1	1	1
	Watchdog timer (		1	1	1	1	1	1	1	1	1	1	1	1
Serial interface	CSI	(WDT)	1	1	1	0		1	0	1	2	2	1 Notes 5, 6	1 Notes 5, 6
		tic transmit/receive te buffer)	-	-	-	-	-	-	-	-	-	-	-	-
	UART		-	-	-	-	-	-	-	-	-	-	-	-
	I <sup>2</sup> C		-	-	-	-	-	-	-	-	-	-	-	1
	UART/CSI		2 <sup>Note 7</sup>	2 <sup>Note 7</sup>	2 <sup>Note 7</sup>	2 <sup>Note 7</sup>	2 <sup>Note 7</sup>	2 <sup>Note 7</sup>	2 <sup>Note 7</sup>	1 <sup>Note 7</sup>	1 Note 7	1 <sup>Note 7</sup>	4 <sup>Notes 6, 7, 8, 9</sup>	6 <sup>Notes 7, 8, 9</sup>
	UART/I <sup>2</sup> C		-	1 <sup>Note 7</sup> –	1 Note 7 -	2 <sup>Note 7</sup> 1 <sup>Note 7</sup>	2 <sup>Note 7</sup> 1 <sup>Note</sup>	e <sup>7</sup> 2 <sup>Note 7</sup>	2 <sup>Note 7</sup>	1 <sup>Note 7</sup> –	1 Note 7 -	1 <sup>Note 7</sup> –	1 <sup>Note 7</sup> –	1 <sup>Note 7</sup> –
	CSI/I <sup>2</sup> C		-	_	_	-	_	-	-	_	-	-	_	_
	UART/CSI/I <sup>2</sup> C		1 <sup>Note 7</sup>	1 <sup>Note 7</sup>	1 <sup>Note 7</sup>	1 <sup>Note 7</sup>	1 Note 7	1 <sup>Note 7</sup>	1 <sup>Note 7</sup>	1 <sup>Note 7</sup>	2 <sup>Note 7</sup>	2 <sup>Note 7</sup>	3 <sup>Notes 5, 6, 7</sup>	3 <sup>Notes 5, 6, 7</sup>
	UART/I <sup>2</sup> C/CAN		-	- 1 <sup>Note 7</sup>	- 1 <sup>Note 7</sup>	7 – 1 <sup>Note 7</sup>	- 1 <sup>Note</sup>	e 7 _	-	- 1 <sup>Note 7</sup>	- 1 <sup>Note 7</sup>	7 – 1 <sup>Note 7</sup>	- 1 <sup>Note</sup>	e <sup>7</sup> – 1 <sup>Note</sup>
USB controller	USB2.0 function		1 (full-speed)	1 (full-speed)	1 (full-speed)	1 (full- speed)	1 (full-speed)	1 (full-speed) 1	(full-speed)	1 (full-speed)	1 (full-speed)	1 (full-speed)	1 (full-speed)	1 (full-speed)
	USB2.0 host		-	-	-	-	-	1 (full-speed) 1	(full-speed)	-	-	-	-	-
Ethernet controller			-	-	-	-	_	-	-	1	1	1	1	1
A/D converter	12-bit 10-bit		- 5	- 6	- 10	- 12	- 12	- 12	- 12	- 8	- 8	- 10	- 10	- 12
D/A converter (8-bit)			-	1	1	2	2	2	2	-	-	-	-	-
DMA controller			4 Note 10	4 Note 10	4 Note 10	4 <sup>Note 10</sup>	4 <sup>Note 10</sup>	4 <sup>Note 10</sup>	4 <sup>Note 10</sup>	4 <sup>Note 10</sup>	4 <sup>Note 10</sup>	4 <sup>Note 10</sup>	4 <sup>Note 10</sup>	4 <sup>Note 10</sup>
Interrupt source	External		10	10	11	17	20	15	20	11	20	22	22	27
	Internal		52	54 58	54 58	69 73	69 73	72	72	62 66	66 67	66 70	78 82	2 84 88
On-chip debug function	MINICUBE		Provided	Provided	Provided	Provided	Provided	Provided	Provided	Provided	Provided	Provided	Provided	Provided
	MINICUBE2		Provided	Provided	Provided	Provided	Provided	Provided	Provided	Provided	Provided	Provided	Provided	Provided
Other peripheral I/O fu			On-chip LVI, clock monitor, R/	AM hold flag, real-time output, CRC	On-chip LVI, clock mo	nitor, RAM hold flag, moto ontrol, real-time output, Cl		On-chip LVI, cl RAM hol motor contr Hi-Z output real-time out	lock monitor, ld flag, rol (1 ch), t control,	On-chip LVI, clock monitor, RAM hold flag, CRC	On-chip LVI, clock monitor, RAM hold flag, motor control (1 ch), Hi-Z output control, CRC		On-chip LVI, clock monitor, AM hold flag, motor control (1 ch), butput control, real-time output, CR(	
Operating ambient tem	nperature				-40 to +85°C			-40 to +	+85°C			-40 to +85°C		
	ent. <b>2.</b> Contains an 8 K			use only. 4. Contains a 64 KB area for d	ata use only. 5. One CSI channel is as									

6. One CSI channel includes a FIFO buffer. 7. UART supports LIN. 8. One UART/CSI channel is assigned to two different pins. 9. Two UART channels include a FIFO buffer. 7. UART supports LIN. 8. One UART/CSI channel is assigned to two different pins. 9. Two UART channels include a FIFO buffer. 7. UART supports LIN. 8. One UART/CSI channel is assigned to two different pins. 9. Two UART channels include a FIFO buffer. 7. UART supports LIN. 8. One UART/CSI channel is assigned to two different pins. 9. Two UART channels include a FIFO buffer. 7. UART supports LIN. 8. One UART/CSI channel is assigned to two different pins. 9. Two UART channels include a FIFO buffer. 7. UART supports LIN. 8. One UART/CSI channel is assigned to two different pins. 9. Two UART channels include a FIFO buffer. 7. UART supports LIN. 8. One UART/CSI channel is assigned to two different pins. 9. Two UART channels include a FIFO buffer. 7. UART supports LIN. 8. One UART/CSI channel is assigned to two different pins. 9. Two UART channels include a FIFO buffer. 7. UART supports LIN. 8. One UART/CSI channel is assigned to two different pins. 9. Two UART channels include a FIFO buffer. 7. UART supports LIN. 8. One UART/CSI channel is assigned to two different pins. 9. Two UART channels include a FIFO buffer. 7. UART supports LIN. 8. One UART/CSI channel is assigned to two different pins. 9. Two UART channels include a FIFO buffer. 7. UART supports LIN. 8. One UART/CSI channel is assigned to two different pins. 9. Two UART channels include a FIFO buffer. 7. UART supports LIN. 8. One UART/CSI channel is assigned to two different pins. 9. Two UART channels include a FIFO buffer. 7. UART supports LIN. 8. One UART/CSI channel is assigned to two different pins. 9. Two UART channels include a FIFO buffer. 7. UART supports LIN. 8. One UART/CSI channel is assigned to two different pins. 9. Two UART channels include a FIFO buffer. 7. UART supports LIN. 8. One UART/CSI channel is assigned to two different pins. 9. Two UART channels include a FIFO buffer. 7.

## **Product specifications (4/4)**

Generic Name				V850E2/I	MN4 Note 1								
Pin Count				304-									
Part Number			µРD70F3510	μΡD70F3510 μΡD70F3512 μΡD70F3514									
CPU core				50E2M	V850E2	2M × 2							
CPU performance (D	hrystone)			512MIPS (@	200MHz)								
Flash memory (bytes	s)		1 M	1 M	1 M	2 M							
RAM (bytes)			64 K 64 K 64 K × 2 64 K × 2										
External bus	Bus type		Separate										
interface	Address bus		26										
	Data bus		8/16/32, 16/32										
	Chip select signa	1	4, 5										
Power supply voltag	le		1.1 to 1.3 V (internal)/3.0 to 3.6 V (external)/analog system: 3.0 to 3.6 V or 4.5 to 5.5 V $^{\rm Note2}$										
Clock				144 to 2	00 MHz								
Port	I/O			18	1								
	Input			7									
Timer/counter	32-bit timer	Number of channels		4 channel	s×1 unit								
		Function		Timer ar	ray unit								
	16-bit timer	Number of channels		16 channe	ls×4 unit								
		Function		Timer ar	ray unit								
	16-bit encoder tir	ner	2										
	OS timer			1	2	2							
	Watchdog timer (	WDT)		1	2	2							
Serial interface	UART/CSI			4									
	UART/CSI/I <sup>2</sup> C		6 Note 3		4 Note 4								
	UART/CSI/I <sup>2</sup> C/CA	N	-		2 Note 5								
USB controller	USB2.0 function			1 (full-s									
	USB2.0 host			1 (full-s									
Ethernet controller	40 1.11		-		1								
A/Dconverter	12-bit			12 (using 5 V anal									
DMA controller	10-bit			12 (using 3.3 V ana									
Interrupt source	External		29	29	29	29							
interrupt source	Internal		180	190	29								
On-chip debug funct			100	Provi	-								
, and	MINICUBE2			FIOV									
Other peripheral I/O			H-bus shared memory: 64 KB H-bus memory side cache: 16 KB Dedicated DMA for secondary memory controller Inverter timer support possible Boundary scan										
Other peripheral I/O Operating ambient to				H-bus memory si Dedicated DMA for seco Inverter timer su	de cache: 16 KB ndary memory controller upport possible ry scan								

Notes 1. Under development

2. 10-bit resolution when using 3.3 V analog power supply and 12-bit resolution when using 5 V analog power supply.

3. Of which 4 UART/CSI channels include a FIFO buffer.

4. Of which 3 UART/CSI channels include a FIFO buffer.

5. Of which 1 UART/CSI channel includes a FIFO buffer.

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coresponds to the products formerly me	evened by NBC Electronics
	eclaration" a declaration that all the new
	ation, we have expanded ours. As of 2001 for products are contributing to developme
products are quietly helping to pr	ovide convenience to society in many diffe
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and a set of ran E feres	1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1
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A Deneral Purpose	See Pin-Count Vibrocontoliers
	75K25K01+ 75K2K02
	70K0K004
	70K0K0A
	70K0U/3
India Conter David Conter	uPortaPacak, uPortaPacas 75K01/2 uPortaPortik
U88	u#0707073x
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Remean OSs	Participating compar- pages every day.	ves we be entered i	nto the online datas
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