

3.3V 2.2W CCFL Programmable Inverter Module

PRODUCTION DATASHEET

DESCRIPTION

The LXMG1618A-03-2x[™] is a Single from the system battery, or AC adapter Output 2.2W Direct Drive[™] CCFL (Cold directly to high frequency, high-voltage Cathode Fluorescent Lamp) Inverter waveform required to ignite and operate Module designed for driving LCD CCFL lamps. A 5V input inverter is also backlight lamps. It is ideal for driving available (LXMG1618A-05-2x). typically 3.9" to 6.4" panels.

externally programmable over a range of CCFL backlight controller to provide a 3.5 to 5mA in 0.5mA steps (PanelMatch) wider temperature range (-30°C to 80°C) to allow the inverter to properly match to a compared to the existing solutions offered wide array of LCD panel lamp current specifications. The modules include a dimming input that permits brightness topology include stable fixed-frequency control from a DC voltage source, a PWM operation, secondary-side strike voltage signal or an external potentiometer.

LXMG1618A modules unlike the protection with fault timeout. LXMG1617A series do not provide wide range 'burst' mode dimming, rather modules are designed therefore as a dimming is provided by amplitude control higher of the output current waveform, and this replacement (see BRITE minimum input) limits the potential dim range to typically less than 5:1.

The module converts a DC voltage

IMPORTANT: For the most current data, consult MICROSEMI's website: http://www.microsemi.com Protected By U.S. Patents: 5,923,129; 5,930,121; 6,198,234; Patents Pending

The LXMG1618A modules contain The maximum output current is the newer highly integrated LX1691B by Microsemi.

> Other benefits of the inverter's regulation and both open/shorted lamp

The new LXMG1618A ("A Series") performance near drop-in for those customers and applications currently using the LXMG1618 inverters.

KEY FEATURES

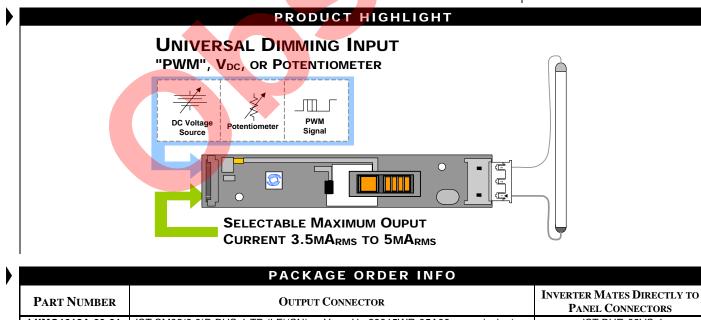
- Externally Programmable Maximum Output Current
- Easy to Use Brightness Control
- **Output Short-Circuit Protection** and Automatic Strike-Voltage Regulation and Timeout
- Analog Current Amplitude **Dimming Method**
- Fixed Frequency Operation Rated From -30°C to 80°C
- UL60950 E175910
- **RoHS** Compliant

APPLICATIONS

- Medical Instrument Displays Portable Instrumentation
- (GPS, etc.) Industrial Display Controls

BENEFITS

- Programmable Output Current Allows Inverter to Mate with a Wide Variety of LCD Panel's Specifications
- Output Open Circuit Voltage **Regulation Minimizes Corona Discharge For High Reliability**



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ABSOLUTE MAXIMUM RATINGS

Input Signal Voltage (V _{IN})	0.3V to 5V
Input Power	
Output Voltage, no load	Internally Limited to 1300V _{RMS}
Output Current	
Output Power	
Input Signal Voltage (SLEEP Input)	0.3V to 5V
Input Signal Voltage (BRITE)	
Ambient Operating Temperature, zero airflow	30°C to 80°C
Storage Temperature Range	

Exceeding these ratings could cause damage to the device. All voltages are with respect to Ground. Currents are positive into, negative out of specified terminal.

RECOMMENDED OPERATING CONDITIONS (R.C.)

This module has been designed to operate over a wide range of input and output conditions. However, best efficiency and performance will be obtained if the module is operated under the condition listed in the 'R.C.' column. Min. and Max. columns indicate values beyond which the inverter, although operational, may not function optimally.

Parameter	Symbol	Recommended Operating Conditions			Units	
raiameter	Symbol	Min	R.C.	Max	Units	
Input Supply Voltage Range (Fully Regulated Lamp Current)	V _{IN}	3	3.3	3.6	V	
Input Supply Voltage Range (Functional)		3		4.2		
Output Power	Po			2.2	W	
Linear BRITE Control Input Voltage Range ¹	V _{BRT} ADJ	0		2.0	V	
Lamp Operating Voltage	VLAMP	325	380	435*	V _{RMS}	
Lamp Current (Full Brightness)	IOLAMP	3.5		5.0	mA _{RMS}	
Operating Ambient Temperature Range	TA	-30		80	°C	
1						

¹ The BRITE minimum input voltage level is 0V, whereas it is 0.65V in the original LXMG1618-03-2x inverter, see application info on page 5. The minimum V_{BRT} _{ADJ} voltage depends on the panel characteristics and minimum lamp current specification, depending on the panel it can vary from 0V to 0.5V *Total output power must not exceed 2.2W. Higher voltage lamps may require maximum output current to be set lower than 5mA_{RMS}

ELECTRICAL CHARACTERISTICS

The following specifications apply over the recommended operating condition and ambient temperature of 0°C to 60°C except where otherwise noted.

Parameter	Symbol	Test Conditions	LXMO	LXMG1618A-03-2x		
Falameter	Symbol	Test conditions	Min	Тур	Max	Units
OUTPUT PIN CHARACTERISTICS						
Full Bright Lamp Current	I _{L(MAX)}	$V_{BRT_ADJ} \ge 2.0V, \ \overline{SLEEP} \ge 2.0V, V_{IN} = 3.3V$ $I_{SET1} = Ground, I_{SET2} = Ground$	3.0	3.5	4.0	mA _{RMS}
Full Bright Lamp Current	I _{L(MAX)}	$V_{BRT_ADJ} \ge 2.0V$, $\overline{SLEEP} \ge 2.0V$, $V_{IN} = 3.3V$ $I_{SET1} = Ground$, $I_{SET2} = Open$	3.5	4.0	4.5	mA _{RMS}
Full Bright Lamp Current	I _{L(MAX)}	$V_{BRT_ADJ} \ge 2.0V$, $\overline{SLEEP} \ge 2.0V$, $V_{IN} = 3.3V$ $I_{SET1} = Open$, $I_{SET2} = Ground$	4.0	4.5	5.0	mA _{RMS}
Full Bright Lamp Current	I _{L(MAX)}	$V_{BRT_ADJ} \ge 2.0V$, SLEEP $\ge 2.0V$, $V_{IN} = 3.3V$ $I_{SET1} = Open$, $I_{SET2} = Open$	4.5	5	5.5	mA _{RMS}
Min. Average Lamp Current ²	I _{L(MIN)}	$V_{BRT_ADJ} = 0V, \overline{SLEEP} \ge 2.0V, V_{IN} = 3.3V$ $I_{SET1} = I_{SET2} = Ground$		1.5		mA _{RMS}
Lamp Start Voltage	V _{LS}	$-30^{\circ}C < T_{A} < 80^{\circ}C, V_{IN} > 3V$	1000	1200		V_{RMS}
Operating Frequency	f _o	$V_{BRT_{ADJ}} = 2V, \overline{SLEEP} \ge 2.0V, V_{IN} = 3.3V$	85	90	94	kHz

² The inverter is capable of a lower output current than may be recommended by the panel manufacturer. It is the user responsibility to set the minimum brightness (BRITE) input at or above the panel specification for minimum lamp current.

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ELECTRICALS



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		Deremeter		Cumple of	Toot Conditions	LXMO	G1618A-	03-2x	11-12-
		Parameter		Symbol	Test Conditions	Min	Тур	Max	Units
•	BRITE IN	PUT							
	Input Curr	ent		I _{BRT}	$V_{BRT_{ADJ}} = 0V$		-9 0		μA
	Minimum	Input for Max 1	amp Current	V _{BRT_ADJ}	V _{BRT_ADJ} = 3V I _{O(LAMP)} = Maximum Lamp Current		1.8	2.0	μA V
	Minimum Input for Max. Lamp Current Maximum Input for Min. Lamp Current ³ Minimum PWM Input Frequency		VBRT_ADJ	$I_{O(LAMP)}$ = Minimum Lamp Current	0	1.0	2.0	v	
			F _{BRT PWM}		2			kHz	
•	SLEEP BAR INPUT			· BRI_FWW					
	RUN Mod	-		$V_{\overline{\text{SLEEP}}}$		2.0			V
	SLEEP M	ode		V _{SLEEP}		-0.3		0.8	V
•	SET _{1,2} INF			JLLLF					
		w Threshold		VL				0.4	V
	Input Curr			I _{SET}	V _{SET} = 0V		-300		μA
•	POWER CHARACTERISTICS								
	Sleep Current		I _{IN(MIN)}	V _{IN} = 3.3V, SLEEP ≤ 0.8V		3	10	μA	
	Run Curre	ent		I _{RUN}	V _{IN} = 3.3V, SLEEP ≥ 2.0V, I _{SET1} = Open I _{SET2} = Ground, V _{LAMP} = 380V _{RMS}		690		mA
			I _{RIPPLE}	$V_{IN} = 3.3V$, SLEEP $\geq 2.0V$, I _{SET1} = Open I _{SET2} = Ground, V _{LAMP} = 380V _{RMS}		400		mVpp	
			η	V_{IN} = 3.3V, SLEEP \ge 2.0V, I _{SET1} = Open I _{SET2} = Ground, V _{LAMP} = 380V _{RMS}		75		%	
	³ The BRITE	∃ minimum input v	oltage level is 0	V, whereas i	t is 0.65V in the original LXMG1618-03-2x inverter.				
				FUNC	TIONAL PIN DESCRIPTION				
	CONN	ΡιΝ			DESCRIPTION				
	N1 (Molex ssembly	: 53261-0871 (or equivalen	t) Mates w	ith 51021-0800 housing, 50079-8100 pins. M	ates with	LX95010	G input c	able
	CN1-1								
	CN1-2 V _{IN} Main Input			put Power Supply ($3V \le V_{IN} \le 3.6V$)					
	CN1-3								
	CN1-4 GND Power Su			er Supply Return					
	CN1-5	SLEEP	ON/OFF Control. (0V < $\overline{\text{SLEEP}} \le 0.8 = \text{OFF}$, $\overline{\text{SLEEP}} \ge 2.0\text{V} = \text{ON}$)						
	CN1-6	BRITE	Brightness Control (0V to 2.0V). 2.0V gives maximum lamp current.						

 CN1-8
 SET2
 SET2 LSB Connecting this pin to ground decreases the output current ~ 0.5mA (see Table 1)

 CN2 for LXMG1618A-03-21 and -22 (JST SM02(8.0)B-BHS-1-TB(LF)(SN) / Yeon Ho 20015WR-05A00 or SM02B-BHSS-1-TB(LF)(SN) / Yeon Ho 35001WR-02A00) or equivalent

 High voltage connection to high side of lamp.
 Connect to lamp terminal with shortest lead length

CN2-1	V _{HI}	High voltage connection to high side of lamp. Connect to lamp terminal with shortest lead length. DO NOT connect to Ground.
CN2-2	V _{LO}	Connection to low side of lamp. Connect to lamp terminal with longer lead length. DO NOT connect to Ground

ELECTRICALS



SET₁

(Pin 7)

Open

Open Ground

Ground

PanelMatch™ "A Series" LXMG1618A-03-2x

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Nominal Output Current

5.0mA

4.5mA

4.0mA 3.5mA

Ground * If driven by a logic signal it should be open collector or open drain only, not a voltage source.

SET₂

(Pin 8) Open

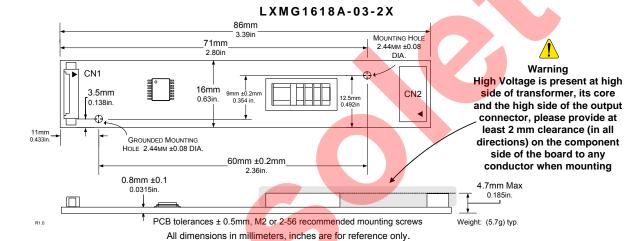
Ground

Open

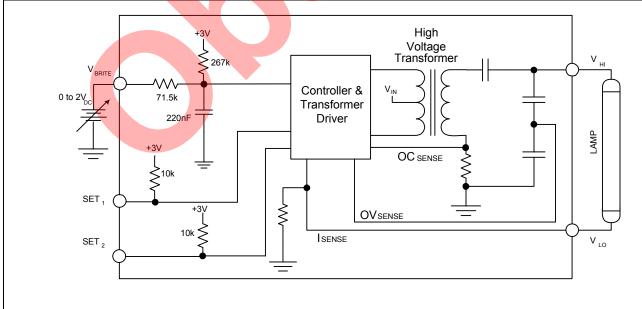
PHYSICAL DIMENSIONS

TABLE 1

OUTPUT CURRENT SETTINGS



SIMPLIFIED BLOCK DIAGRAM



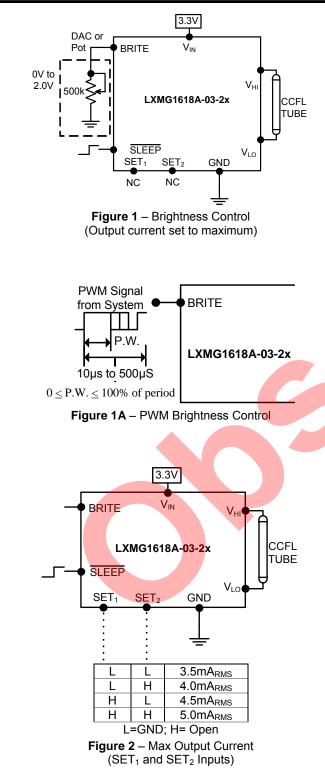
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TYPICAL APPLICATION



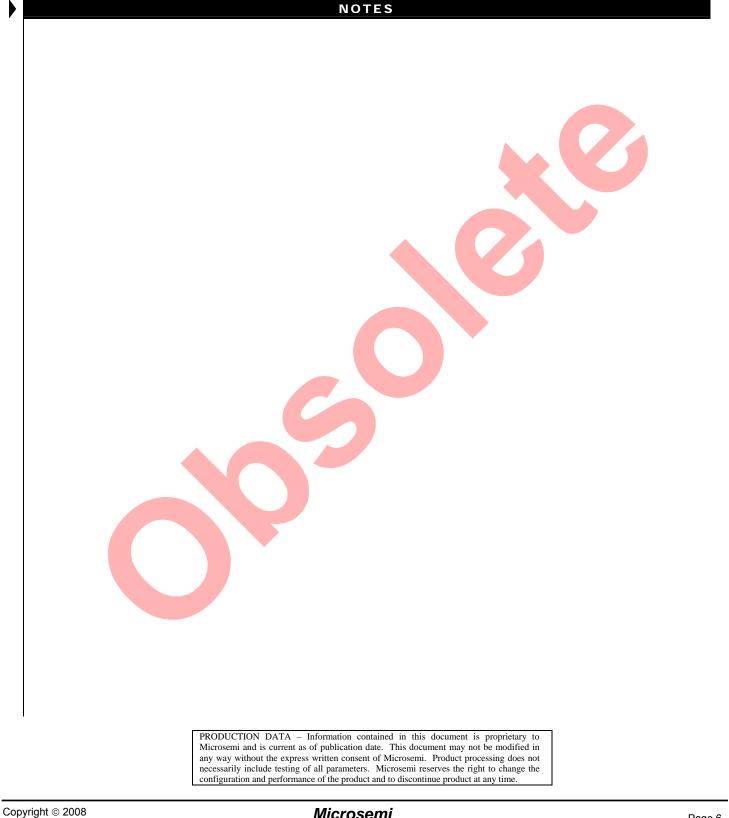
- The brightness control may be a voltage output DAC or other voltage source, a digital pot or 500K manual pot. The inverter contains effectively an internal 338k pull-up to 3V to bias the pot, if desired; add a resistor to set the lower threshold voltage above the absolute minimum dim level capability. A PWM logic level signal (figure 1A) may be used up to 5V; however the inverter will reach maximum current at less than 100% duty cycle. This can be calculated as approximately 2V divided by the logic high voltage level; with 3.3V logic level this corresponds to about 60% duty cycle for maximum lamp current.
- If you need to turn the inverter ON/OFF remotely, connect to TTL logic signal to the SLEEP input.
- Connect V_{HI} to high voltage wire from the lamp. Connect V_{LO} to the low voltage wire (wire with thinner insulation). Never connect V_{LO} to circuit ground as this will defeat lamp current regulation. If both lamp wires have heavy high voltage insulation, connect the longest wire to V_{LO} . This wire is typically white.
- Use the SET_1 and SET_2 (see Figure 2) inputs to select the desired maximum output current. Using these two pins in combination allows the inverter to match a wide variety of panels from different manufacturers. Generally the best lamp lifetime correlates with driving the CCFL at the manufacturer's nominal current setting. However the SET_1 and SET_2 inputs allow the user the flexibility to adjust the current to the maximum allowable output current to increase panel brightness at the expense of some reduced lamp life.
- Although the SET pins are designed such that just leaving them open or grounding them is all that is needed to set the output current, they can also be actively set. Using an open collector or open drain logic signal will allow you to reduce the lamp current for situations where greater dim range is required, as an example in nighttime situations. In conjunction with a light sensor or other timer the panel could be set to higher brightness (maximum output current) for daytime illumination and lower brightness (minimum or typical output current) at nighttime. Since the dim ratio is a factor of the peak output current, using this technique the effective dim ratio can be increased. Conversely, the SET inputs could be used to overdrive the lamp temporarily to facilitate faster lamp warm up at initial lamp turn on. Of course any possible degradation on lamp life from such practices is the user's responsibility since not all lamps are designed to be overdriven.
- The inverter has a built-in fault timeout function. If the output is open (lamp disconnected or broken) or shorted the inverter will attempt to strike the lamp up to about one second, after which (without success) the inverter will shutdown. Supply current draw in the shutdown mode is about 8mA. In order to restart the inverter it is necessary to toggle the sleep input or cycle the V_{IN} input supply.

APPLICATION



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Rev. 1.1, 2009-11-03