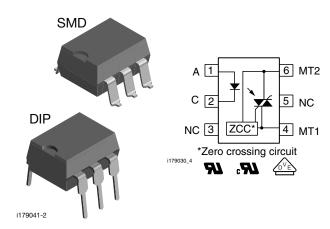


www.vishay.com

Vishay Semiconductors

Phototriac, Zero Crossing, 1.5 kV/µs dV/dt, 600 V



DESCRIPTION

The VO3062 and VO3063 triac driver family consists of a GaAs infrared LED optically coupled to a monolithic photosensitive zero crossing triac detector chip.

The 600 V blocking voltage permits control of off-line voltages up to 240 V_{AC} , with a safety factor of more than two, and is sufficient for as much as 380 V.

FEATURES

- 1500 V/µs dV/dt minimum
- 600 V blocking voltage
- 100 mA on-state current
- · Zero crossing detector
- Low input trigger current
- · 6 pin DIP package
- Material categorization: For definitions of compliance please see www.vishav.com/doc?99912

Pb-free



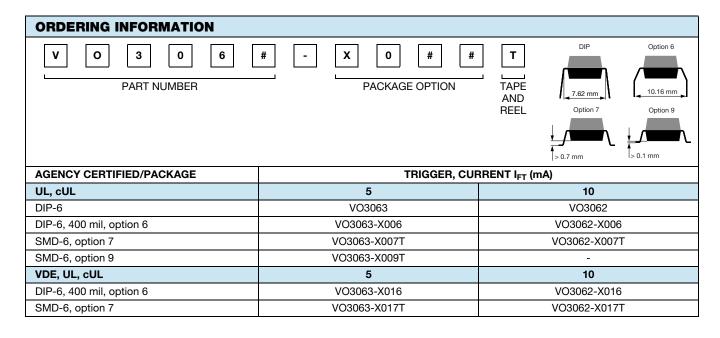
RoHS COMPLIAN

APPLICATIONS

- Household appliances
- Triac drive/AC motor drives
- Solenoid/valve controls
- Office automation equipment/machine
- Temperature (HVAC)/lighting controls
- Switching power supply

AGENCY APPROVALS

- UL1577, file no. E52744 system code H
- cUL file no. E52744, equivalent to CSA bulletin 5A
- DIN EN 60747-5-5 (VDE 0884-5) available with option 1





ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	PART	SYMBOL	VALUE	UNIT	
INPUT						
Reverse voltage			V_{R}	6	V	
Forward current - continuous			I _F	60	mA	
Power dissipation			P _{diss}	100	mW	
OUTPUT						
Off state output terminal voltage		VO3062, VO3063	V_{DRM}	600	V	
Peak non-repetitive surge current	PW = 100 μs, 120 pps		I _{TSM}	1	Α	
Power dissipation			P _{diss}	200	mW	
On-state RMS current			I _{T(RMS)}	100	mA	
COUPLER						
Isolation test voltage	t = 1 s		V _{ISO}	5300	V_{RMS}	
Total power dissipation			P _{tot}	300	mW	
Operating temperature range			T _{amb}	- 55 to + 100	°C	
Storage temperature range			T _{stg}	- 55 to + 150	°C	
Soldering temperature (1)	maximum ≤ 10 s		T _{sld}	260	°C	

Notes

- Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not
 implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute
 maximum ratings for extended periods of the time can adversely affect reliability.
- (1) Refer to reflow profile for soldering conditions for surface mounted devices (SMD). Refer to wave profile for soldering conditions for through hole devices (DIP) "Assembly Instructions" (www.vishay.com/doc?80054).

THERMAL CHARACTERISTICS			
PARAMETER	SYMBOL	VALUE	UNIT
Maximum LED junction temperature	T _{jmax.}	125	°C
Maximum output die junction temperature	T _{jmax.}	125	°C
Thermal resistance, junction emitter to board	θ_{JEB}	150	°C/W
Thermal resistance, junction emitter to case	θ_{JEC}	139	°C/W
Thermal resistance, junction detector to board	θ_{JDB}	78	°C/W
Thermal resistance, junction detector to case	θ_{JDC}	103	°C/W
Thermal resistance, junction emitter to junction detector	θ_{JED}	496	°C/W
Thermal resistance, case to ambient	$\theta_{\sf CA}$	3563	°C/W

Note

The thermal model is represented in the thermal network below. Each resistance value given in this model can be used to calculate the
temperatures at each node for a given operating condition. The thermal resistance from board to ambient will be dependent on the type of
PCB, layout and thickness of copper traces. For a detailed explanation of the thermal model, please reference Vishay's Thermal
Characteristics of Optocouplers application note.



ELECTRICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
INPUT							
Reverse current	V _R = 6 V		I _R			10	μA
Forward voltage	I _F = 30 mA		V _F		1.2	1.5	V
OUTPUT							
Leakage with LED off, either direction	V _{DRM} = 600 V		I _{DRM}		10	500	nA
Critical rate of rise off-state voltage	V _D = 400 V		dV/dt	1500	2000		V/µs
COUPLER							
LED trigger current,		VO3063	I _{FT}			5	mA
current required to latch output		VO3062	I _{FT}			10	mA
Peak on-state voltage, either direction	$I_{TM} = 100 \text{ mA Peak},$ $I_F = \text{Rated } I_{FT}$		V_{TM}		1.7	3	V
Holding current, either direction			I _H		200		μA
Inhibit voltage (MT1-MT2 voltage above which device will not trigger)			V _{INH}		12	22	V
Leakage in inhibited state	$I_F = 10$ mA maximum, at rated V_{DRM} , off state		V _{DRM2}		250	1000	μΑ

Note

 Typical values are characteristics of the device and are the result of engineering evaluations. Typical values are for information only and are not part of the testing requirements.

SAFETY AND INSULATION RATINGS					
PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT
Climatic classification (according to IEC 68 part 1)			55/100/21		
Pollution degree (DIN VDE 0109)			2		
Comparative tracking index	CTI	175			
Peak transient overvoltage	V _{IOTM}	8000			V _{peak}
Peak working insulation voltage	V _{IORM}	890			V_{peak}
Isolation resistance at T _{amb} = 100 °C, V _{DC} = 500 V	R _{IO}	10 ¹¹			Ω
Isolation resistance at T _{amb} = 25 °C, V _{DC} = 500 V	R _{IO}	10 ¹²			Ω
Partial discharge test voltage (method a, $V_{pd} = V_{IORM} \times 1.875$)	V_{pd}	1325			V _{peak}
Safety rating - power	P _{SO}			400	mW
Safety rating - input current	I _{SI}			150	mA
Safety rating - temperature	T _{SI}			165	°C
Clearance distance (Standard DIP-6)		7			mm
Creepage distance (Standard DIP-6)		7			mm
Clearance distance (400 mil DIP-6)		8			mm
Creepage distance (400 mil DIP-6)		8			mm

Note

According to DIN EN60747-5-5 (see figure 4). This optocoupler is suitable for safe electrical isolation only within the safety ratings.
 Compliance with the safety ratings shall be ensured by means of suitable protective circuits.

TYPICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

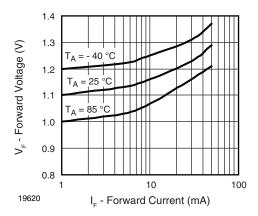


Fig. 1 - Forward Voltage vs. Forward Current

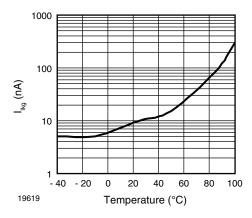


Fig. 2 - Off-State Leakage Current vs. Temperature

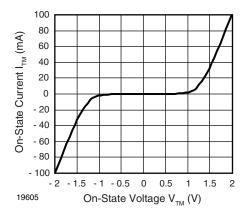


Fig. 3 - On-State Current vs. V_{TM}

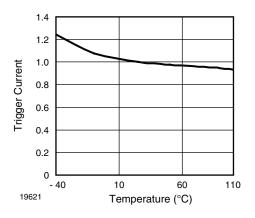


Fig. 4 - Normalized Trigger Current vs. Temperature

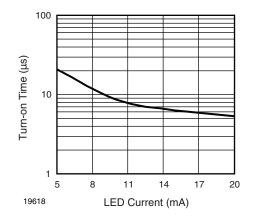


Fig. 5 - Turn-on Time vs. LED Current

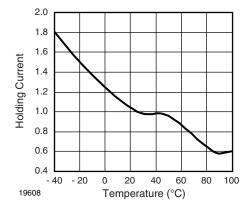
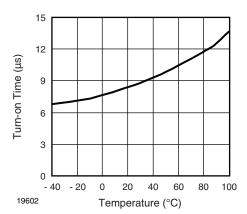


Fig. 6 - Normalized Holding Current vs. Temperature







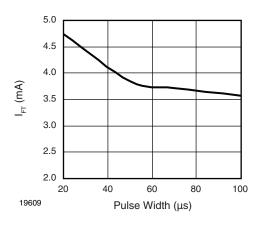
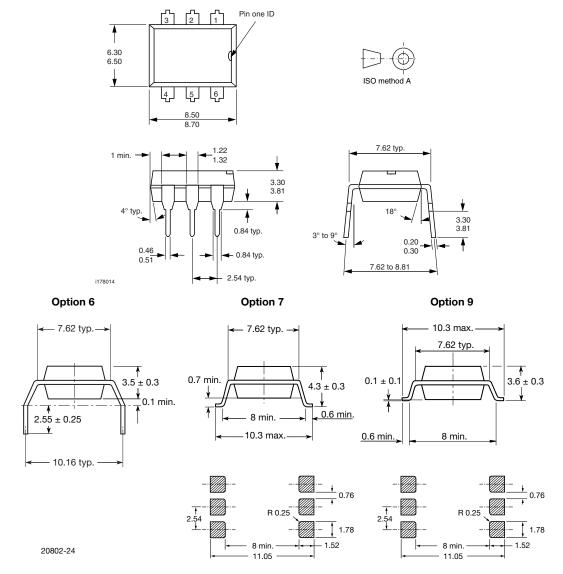


Fig. 8 - Trigger Current vs. Pulse Width

PACKAGE DIMENSIONS in millimeters





PACKAGE MARKING (example)



Notes

- The VDE logo is only marked on option1 parts.
- Tape and reel suffix (T) is not part of the package marking.



Footprint and Schematic Information

Vishay Semiconductors

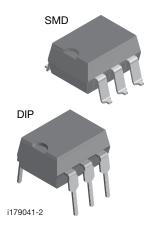
Footprint and Schematic Information for VO3062, VO3063

The footprint and schematic symbols for the following parts can be accessed using the associated links. They are available in Eagle, Altium, KiCad, OrCAD / Allegro, Pulsonix, and PADS.

Note that the 3D models for these parts can be found on the Vishay product page.

PART NUMBER	FOOTPRINT / SCHEMATIC
VO3062	www.snapeda.com/parts/VO3062/Vishay/view-part
VO3062-X007T	www.snapeda.com/parts/VO3062-X007T/Vishay/view-part
VO3062-X017T	www.snapeda.com/parts/VO3062-X017T/Vishay/view-part
VO3063	www.snapeda.com/parts/VO3063/Vishay/view-part
VO3063-X007T	www.snapeda.com/parts/VO3063-X007T/Vishay/view-part
VO3063-X009T	www.snapeda.com/parts/VO3063-X009T/Vishay/view-part
VO3063-X017T	www.snapeda.com/parts/VO3063-X017T/Vishay/view-part

For technical issues and product support, please contact optocoupleranswers@vishay.com.





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