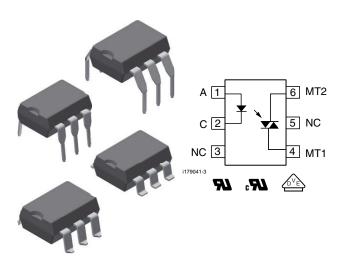


Optocoupler, Phototriac Output, Non-Zero Crossing, 1.5 kV/µs dV/dt, 600 V



DESCRIPTION

The VO3052 and VO3053 triac driver family consists of a GaAs infrared LED optically coupled to a monolithic photosensitive non-zero crossing triac detector chip.

The 600 V blocking voltage permits control of off-line voltages up to 240 V_{AC} , with a safety factor or more than two, and is sufficient for as much as 380 V.

FEATURES

- 1500 V/µs dV/dt minimum 2000 V/µs typical
- 600 V blocking voltage
- 100 mA on-state current
- · Low input trigger current
- 6 pin DIP package
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912





COMPLIAN

APPLICATIONS

- · Household appliances
- Triac drive / AC motor drives
- Solenoid / valve controls
- · Office automation equipment / machine
- Temperature (HVAC) / lighting controls
- Switching power supply

AGENCY APPROVALS

- UL-file E52744
- cUL file no. E52744, equivalent to CSA bulletin 5A
- DIN EN 60747-5-5 (VDE 0884) available with option 1

ORDERING INFORMATION			
V 0 3 0 5	# - X 0 # #	DIP Option 6	
PART NUMBER	PACKAGE OPTION	TAPEAND Option 7 Option 9	
AGENCY CERTIFIED/PACKAGE	TRIGGER, CURRENT I _{FT} (mA)		
UL, cUL	5	10	
DIP-6	VO3053	VO3052	
DIP-6, 400 mil, option 6	VO3053-X006	VO3052-X006	
	VO3053-X007T VO3052-X007T		
SMD-6, option 7	VO3053-X007T	VO3052-X007T	
SMD-6, option 7 SMD-6, option 9	VO3053-X007T VO3053-X009T	VO3052-X007T VO3052-X009T	
SMD-6, option 9	VO3053-X009T	VO3052-X009T	



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ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
INPUT					
Reverse voltage		V_R	6	V	
Forward current - continuous		I _F	60	mA	
Power dissipation		P _{diss}	100	mW	
OUTPUT					
Off state output terminal voltage		V_{DRM}	600	V	
Peak non-repetitive surge current	PW = 100 ms, 120 pps	I _{TSM}	1	Α	
Power dissipation		P _{diss}	200	mW	
On-state RMS current		I _{T(RMS)}	100	mA	
COUPLER					
Total power dissipation		P _{tot}	300	mW	
Operating temperature		T _{amb}	-55 to +100	°C	
Storage temperature		T _{stg}	-55 to +150	°C	
Soldering temperature	10 s	T _{sld}	260	°C	

Note

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not
implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute
maximum ratings for extended periods of the time can adversely affect reliability

THERMAL CHARACTERISTICS			
PARAMETER	SYMBOL	VALUE	UNIT
Maximum LED junction temperature	T _{jmax.}	125	°C
Maximum output die junction temperature	T _{jmax.}	125	°C
Thermal resistance, junction emitter to board	θ_{JEB}	150	°C/W
Thermal resistance, junction emitter to case	θ_{JEC}	139	°C/W
Thermal resistance, junction detector to board	θ_{JDB}	78	°C/W
Thermal resistance, junction detector to case	θ_{JDC}	103	°C/W
Thermal resistance, junction emitter to junction detector	θ_{JED}	496	°C/W
Thermal resistance, case to ambient	θ_{CA}	3563	°C/W

Note

• The thermal model is represented in the thermal network below. Each resistance value given in this model can be used to calculate the temperatures at each node for a given operating condition. The thermal resistance from board to ambient will be dependent on the type of PCB, layout and thickness of copper traces. For a detailed explanation of the thermal model, please reference Vishay's Thermal Characteristics of Optocouplers application note



ELECTRICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
INPUT	INPUT						
Reverse current	$V_R = 6 V$		I _R	ı	-	10	μA
Forward voltage	$I_F = 30 \text{ mA}$		V_{F}	-	1.2	1.5	V
OUTPUT							
Leakage with LED off, either direction	$V_{DRM} = 600 V$		I _{DRM}	ı	10	500	nA
Critical rate of rise off-state voltage	$V_D = 400 \text{ V}$		dV/dt _{cr}	1500	2000	-	V/µs
COUPLER							
LED trigger current,		VO3053	I _{FT}	ı	-	5	mA
current required to latch output		VO3052	I _{FT}	-	-	10	mA
Peak on-state voltage, either direction	$I_{TM} = 100 \text{ mA peak},$ $I_F = \text{rated } I_{FT}$		V _{TM}	-	1.7	3	V
Holding current, either direction			I _H	-	200	-	μΑ
Coupling capitance	10 kHz		C _{IO}		0.4	-	pF

Note

Typical values are characteristics of the device and are the result of engineering evaluations. Typical values are for information only and are
not part of the testing requirements.

SAFETY AND INSULATION RATINGS				
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Climatic classification	According to IEC 68 part 1		55 / 100 / 21	
Pollution degree	According to DIN VDE 0109		2	
Comparative tracking index	Insulation group IIIa	CTI	175	
Maximum rated withstanding isolation voltage	According to UL1577, t = 1 min	V _{ISO}	4420	V_{RMS}
Tested withstanding isolation voltage	According to UL1577, t = 1 s	V _{ISO}	5300	V_{RMS}
Maximum transient isolation voltage	According to DIN EN 60747-5-5	V _{IOTM}	8000	V _{peak}
Maximum repetitive peak isolation voltage	According to DIN EN 60747-5-5	V _{IORM}	890	V _{peak}
Isolation resistance	$T_{am}b = 25 ^{\circ}C, V_{IO} = 500 V$	R _{IO}	≥ 10 ¹²	Ω
	T _{amb} = 100 °C, V _{IO} = 500 V	R _{IO}	≥ 10 ¹¹	Ω
Output safety power		P _{SO}	500	mW
Input safety current		I _{SI}	250	mA
Input safety temperature		T _S	175	°C
Creepage distance	DID 6 CMD 6 with entire 7 and 0		≥ 7	mm
Clearance distance	DIP-6, SMD-6 with option 7 and 9		≥ 7	mm
Creepage distance	DID 6 400 mil. option 6		≥ 8	mm
Clearance distance	DIP-6, 400 mil, option 6		≥ 8	mm
Insulation thickness		DTI	≥ 0.4	mm
Input to output test voltage, method A	V_{IORM} x 1.6 = V_{PR} , 100 % sample test with t_M = 10 s, partial discharge < 5 pC	V _{PR}	1669	V _{peak}

Note

As per IEC 60747-5-5, § 7.4.3.8.2, this optocoupler is suitable for "safe electrical insulation" only within the safety ratings. Compliance with
the safety ratings shall be ensured by means of protective circuits

TYPICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

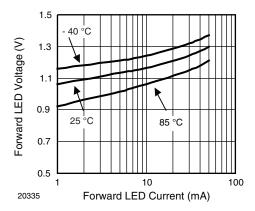


Fig. 1 - Forward Voltage vs. Forward Current

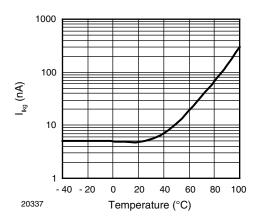


Fig. 2 - Off-State Leakage Current vs. Temperature

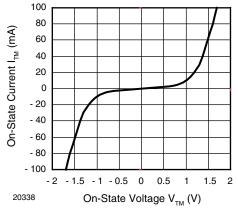


Fig. 3 - On-State Current vs. V_{TM}

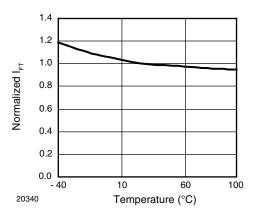


Fig. 4 - Normalized Trigger Current vs. Temperature

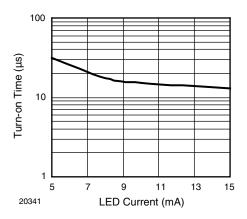


Fig. 5 - Turn-on Time vs. LED Current

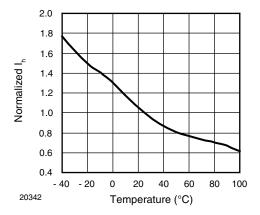


Fig. 6 - Normalized Holding Current vs. Temperature



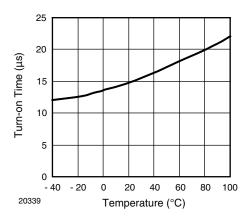


Fig. 7 - Turn-on Time vs. Temperature

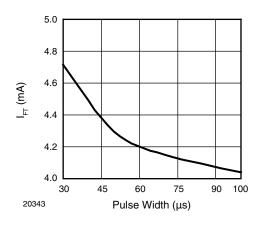
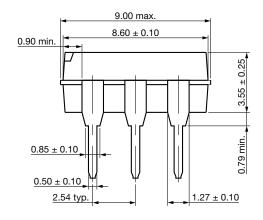
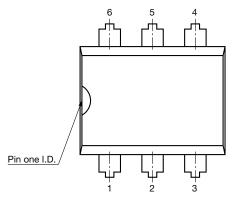


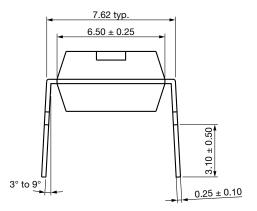
Fig. 8 - Trigger Current vs. Pulse Width

PACKAGE DIMENSIONS (in millimeters)

DIP-6

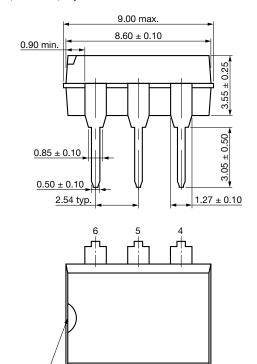


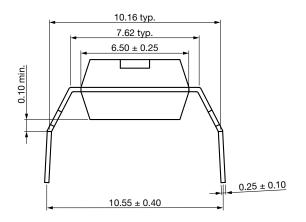






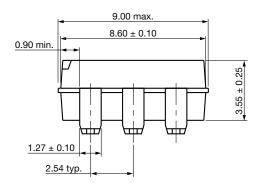
DIP-6, 400 mil, Option 6

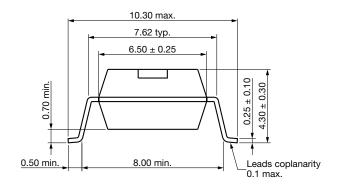


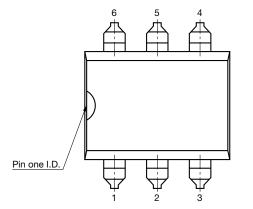


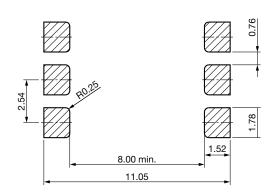
SMD-6, Option 7

Pin one I.D.



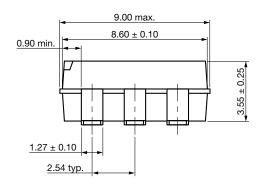


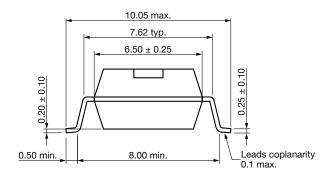


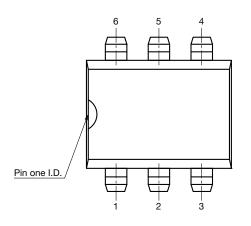


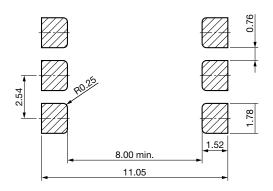


SMD-6, Option 9

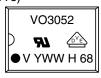








PACKAGE MARKING (example of VO3052-X016)



Notes

- The VDE logo is only marked on option1 parts
- Tape and reel suffix (T) is not part of the package marking

PACKING INFORMATION (in millimeters)

Tube

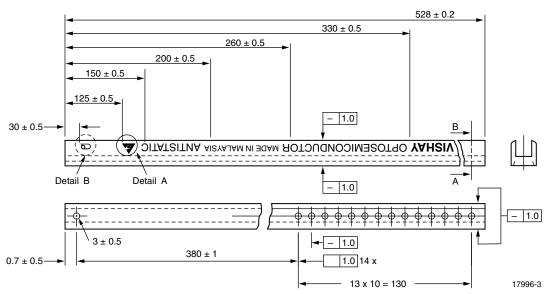


Fig. 9 - Shipping Tube Specifications for DIP-6 Packages

DEVICES PER TUBS			
TYPE	UNITS/TUBE	TUBES/BOX	UNITS/BOX
DIP-6	50	40	2000

DIP-6

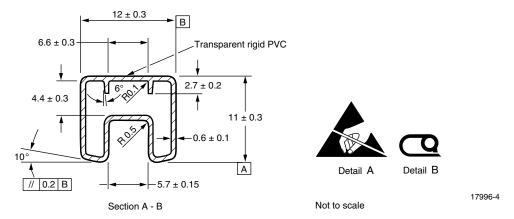


Fig. 10 - Tube Shipping Medium

DIP-6, 400 mil, Option 6

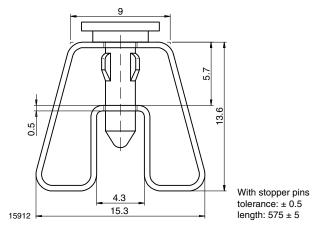


Fig. 11 - Tube Shipping Medium

Tape and Reel

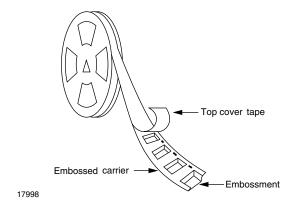


Fig. 12 - Tape and Reel Shipping Medium

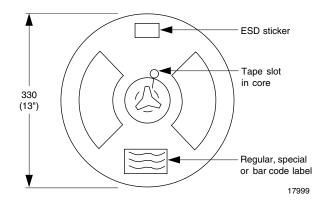


Fig. 13 - Tape and Reel Shipping Medium

SMD-6, Option 7

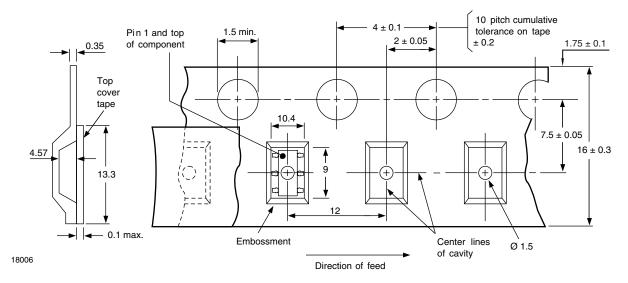


Fig. 14 - Tape and Reel Packing (1000 pieces on Reel)

SMD-6, Option 9

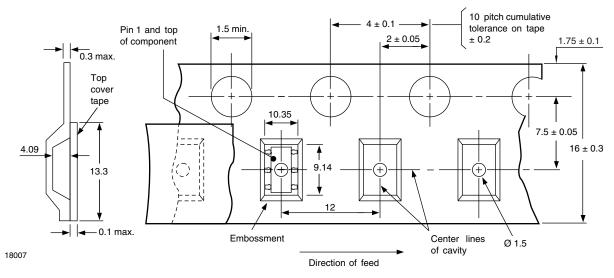


Fig. 15 - Tape and Reel Shipping Medium

SOLDER PROFILES

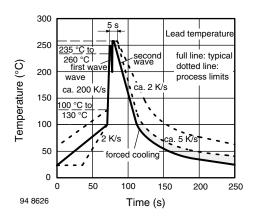


Fig. 16 - Wave Soldering Double Wave Profile According to J-STD-020 for DIP Devices

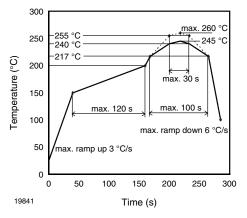


Fig. 17 - Lead (Pb)-free Reflow Solder Profile According to J-STD-020 for SMD Devices

HANDLING AND STORAGE CONDITIONS

ESD level: HBM class 2 Floor life: unlimited

Conditions: $T_{amb} < 30$ °C, RH < 85 %

Moisture sensitivity level 1, according to J-STD-020



Footprint and Schematic Information

Vishay Semiconductors

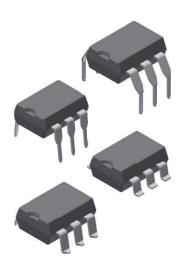
Footprint and Schematic Information for VO3052, VO3053

The footprint and schematic symbols for the following parts can be accessed using the associated links. They are available in Eagle, Altium, KiCad, OrCAD / Allegro, Pulsonix, and PADS.

Note that the 3D models for these parts can be found on the Vishay product page.

PART NUMBER	FOOTPRINT / SCHEMATIC		
VO3052	www.snapeda.com/parts/VO3052/Vishay/view-part		
VO3052-X007T	www.snapeda.com/parts/VO3052-X007T/Vishay/view-part		
VO3053	www.snapeda.com/parts/VO3053/Vishay/view-part		
VO3053-X007T	www.snapeda.com/parts/VO3053-X007T/Vishay/view-part		
VO3053-X009T	www.snapeda.com/parts/VO3053-X009T/Vishay/view-part		

For technical issues and product support, please contact optocoupleranswers@vishay.com.





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